



# SA50-28 Triple Series

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## Radiation-Hardened Isolated DC-to-DC Converter

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### Introduction

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The SA50-28 is an Isolated DC-to-DC converter capable of delivering up to 50W of output power in a small size design. The SA family provides a radiation hardened option with top class TID and SEE performance for space and military applications. With forward converter topology and a patented magnetic feedback, the SA50-28 is optimized for applications where isolated DC voltage conversion is required. The discrete surface mount design facilitates customization with reasonable lead time and modest NRE cost.

To achieve MIL-STD-461 EMI compliance, an external filter is required. Off the shelf filters such as Microchip's SF200-28-28S are available.

As the only non-hybrid space grade DC-DC power converter module in the market, the SA50-28 series excels in its robustness in the applications with  $8.22 \times 10^6$  hours of MTBF.

The SA50-28 is available in 3.055" x 2.055" x 0.5" package.

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### 1. Benefits and Features

- Up to 50W output power
- 20VDC to 40VDC input range
- 4 output configurations available

Main	Aux A/B	Base Part number
3.3V	12V	SA50-28-3R3-12T
3.3V	15V	SA50-28-3R3-15T
5V	12V	SA50-28-5-12T
5V	15V	SA50-28-5-15T

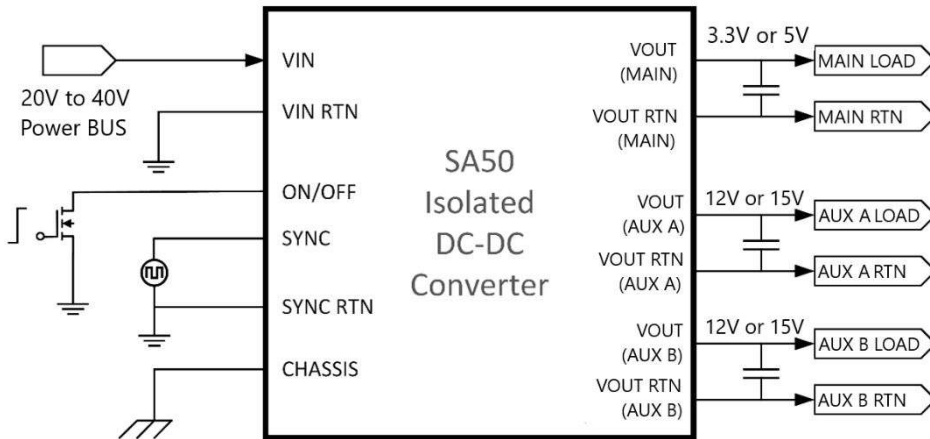
- Dual isolated 12V or 15V auxiliary outputs
- Up to 85% efficiency @ full load
- <1% output ripple
- Forward topology
- Patented magnetic feedback
- Inhibit pin for electrical ON/OFF
- Isolated synchronization input
- Low mass 120g
- Flight proven technology with >8.22 x 10<sup>6</sup> hours of MTBF.
- This product is classified as EAR99
- Customization of input/output voltages available upon request.

## 2. Radiation Performance

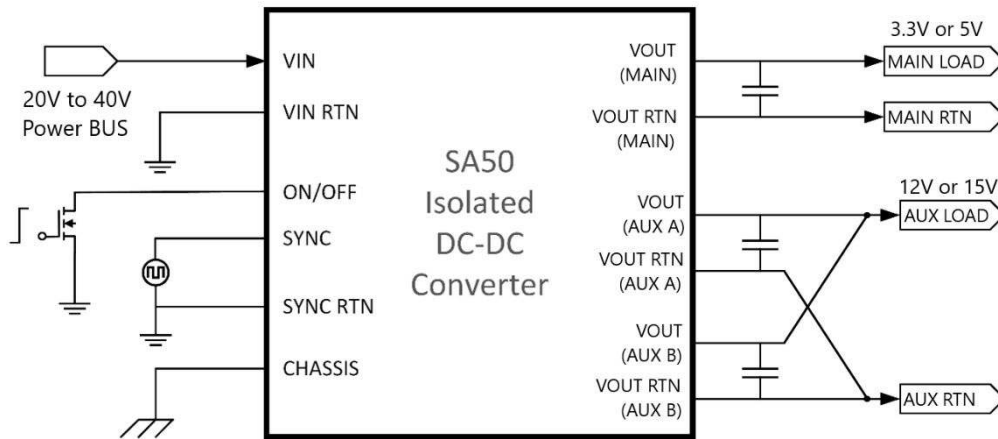
- TID>100krad(Si) and 30krad(Si) ELDRS (<10mrad/s) per MIL-STD-883 Method 1019
- SEE (SEGR, SEB, SET, SEL) immunity 82 MeV·cm<sup>2</sup>/mg

### 3. Typical Applications Circuits

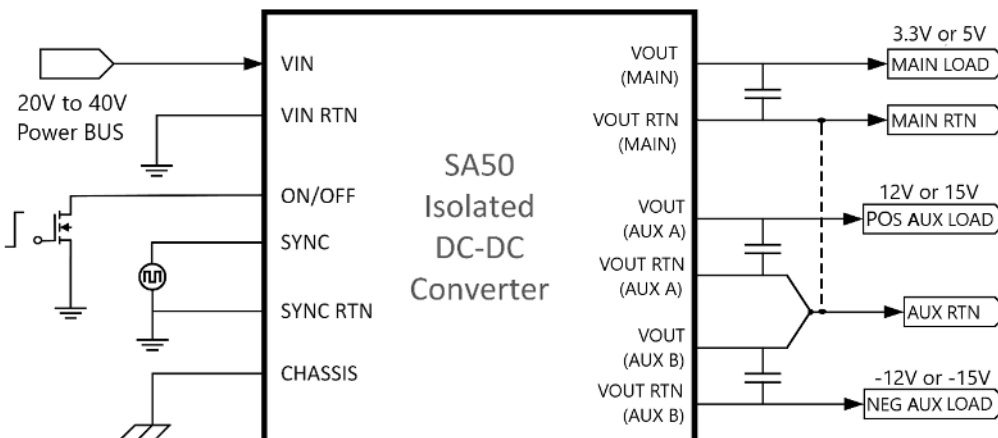
**Figure 3-1. SA50-28T Single Typical Application Circuit**



**Figure 3-2. SA50-28T AUX Outputs Parallel Application Circuit**



**Figure 3-3. SA50-28T AUX Split Outputs Application Circuit**



NOTE: With each output of the SA50 isolated from each other there are more possible connections. These are circuits that will be found in typical applications.

**4. Absolute Maximum Ratings**

Rating	Value
V <sub>IN</sub> range	-0.5 VDC to 60 VDC
Output power	50 W
Lead temperature	300 °C for 10 s
Operating temperature	-55 °C to 125 °C
Storage temperature	-55 °C to 125 °C
Shock	1500 g <sub>pk</sub> , 0.5 ms, ½ sine
Constant acceleration	50 g
Random vibration	24.06 g <sub>rms</sub> , 50 Hz to 2000 Hz

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## Electrical Parameters

### 5. Electrical Parameters

This section shows the electrical parameters of the SA50-28 Triple Series device under the following conditions unless otherwise specified:

Parameter	Output	Conditions	Min	Nom	Max	Units
<b>Input voltage all configurations</b>						
(Vin)		Note 2	20	28	40	V
<b>Output Voltages by configuration (V<sub>out</sub>)</b>						
<b>SA50-28-3R3-12T-x-x</b>		I <sub>OUT</sub> = 100% rated load				V
(MAIN)	3.3V		3.27	3.30	3.33	
(AUX A/B)	12V		11.59	12.00	12.41	
<b>SA50-28-3R3-15T-x-x</b>						
(MAIN)	3.3V		3.27	3.30	3.33	
(AUX A/B)	15V		14.25	14.75	15.25	
<b>SA50-28-5-12T-x-x</b>						
(MAIN)	5V		5.08	5.10	5.12	
(AUX A/B)	12V		11.75	12.00	12.24	
<b>SA50-28-5-15T-x-x</b>						
(MAIN)	5V		5.08	5.10	5.12	
(AUX A/B)	15V		14.25	14.75	15.25	
<b>Output Power by configuration (P<sub>out</sub>)</b>						
<b>SA50-28-3R3-12T-x-x</b>		Note 3	5.0		50.0	W
<b>SA50-28-3R3-15T-x-x</b>			5.0		50.0	
<b>SA50-28-5-12T-x-x</b>			4.32		43.2	
<b>SA50-28-5-15T-x-x</b>			4.32		43.2	

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## Electrical Parameters

.....continued								
Parameter	Output	Conditions	Min	Nom	Max	Units		
<b>Output current by configuration (<math>I_{out}</math>)</b>								
<b>SA50-28-3R3-12T-x-x</b>		In all cases Output power must be kept within $P_{out}$ rating.  Note 13, 14, 15, 16				mA		
(MAIN)	3.3V		400		4000			
(AUX A/B)	12V		125		1250			
<b>SA50-28-3R3-15T-x-x</b>								
(MAIN)	3.3V		400		4000			
(AUX A/B)	15V		100		1000			
<b>SA50-28-5-12T-x-x</b>								
(MAIN)	5V		400		4000			
(AUX A/B)	12V		125		1250			
<b>SA50-28-5-15T-x-x</b>								
(MAIN)	5V		400		4000			
(AUX A/B)	15V		100		1000			
<b>Line regulation by configuration (<math>VR_{LINE}</math>)</b>								
<b>SA50-28-3R3-12T-x-x</b>			$V_{IN} = 20V, 28V, 40V$ $I_{OUT} = 10\%, 50\%, 100\%$ rated Note 12					mV
(MAIN)	3.3V	-10			10			
(AUX A/B)	12V	-120			120			
<b>SA50-28-3R3-15T-x-x</b>								
(MAIN)	3.3V	-10			10			
(AUX A/B)	15V	-150			150			
<b>SA50-28-5-12T-x-x</b>								
(MAIN)	5V	-10			10			
(AUX A/B)	12V	-120			120			
<b>SA50-28-5-15T-x-x</b>								
(MAIN)	5V	-10			10			
(AUX A/B)	15V	-150			150			



# SA50-28 Triple Series

## Electrical Parameters

.....continued						
Parameter	Output	Conditions	Min	Nom	Max	Units
<b>Load regulation by configuration (<math>V_{R_{LOAD}}</math>)</b>						
<b>SA50-28-3R3-12T-x-x</b>		$V_{IN} = 20V, 28V, 40V$ $I_{OUT} = 10\%, 50\%, 100\%$ rated Note 11				mV
(MAIN)	3.3V		-50		50	
(AUX A/B)	12V		-400		400	
<b>SA50-28-3R3-15T-x-x</b>						
(MAIN)	3.3V		-50		50	
(AUX A/B)	15V		-500		500	
<b>SA50-28-5-12T-x-x</b>						
(MAIN)	5V		-50		50	
(AUX A/B)	12V		-400		400	
<b>SA50-28-5-15T-x-x</b>						
(MAIN)	5V		-50		50	
(AUX A/B)	15V		-500		500	
<b>Cross regulation (<math>V_{R_{cross}}</math>)</b>						
(Aux)		$V_{IN} = 20V, 28V, 40V$ $I_{OUT} = 2.5A$ to 1A and 2.5A to 4A on main, and $\pm$ half the rated current on the Aux outputs	-3.0		3.0	%
<b>Input current all configurations (<math>I_{IN}</math>)</b>						
(lin)		$I_{OUT}=0$ , pin3 open		100	150	mA
		Pin 3 short to pin 2		2	5	
<b>Output ripple all configurations (<math>V_{RIP}</math>)</b>						
(Main)		$V_{IN} = 20V, 28V, 40V$ $I_{OUT} = 100\%$ rated, Note 4		25	50	mVpp
(Aux)				37.5	75	
<b>Switching frequency all configurations (FS)</b>						
(FS)		Sync input (pin 4) open	200	220	240	kHz
<b>Efficiency by configuration (eff)</b>						
<b>SA50-28-3R3-12T-x-x</b>		$I_{OUT} = 100\%$ rated load	75	84		%
<b>SA50-28-3R3-15T-x-x</b>			75	84		
<b>SA50-28-5-12T-x-x</b>			80	84		
<b>SA50-28-5-15T-x-x</b>			80	84		

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## Electrical Parameters

.....continued						
Parameter	Output	Conditions	Min	Nom	Max	Units
<b>Inhibit input all configurations</b>						
Inhibit input: ON Threshold		Note 1	4.5			V
Inhibit input: OFF (sink)			1000			μA
Inhibit input: OFF threshold					2	V
<b>Current limit all configurations</b>						
Current limit point (% rated output)		When $V_{OUT} = 90\%$ of nominal set point	105		145	%
<b>Synchronization all configurations</b>						
Synchronization frequency range		The external clock on sync input (pin 4)  Note 1	500		600	kHz
Synchronization pulse-high level			4.0		10.0	V
Synchronization pulse-low level			-0.5		0.5	V
Synchronization pulse-transition rate			200			V/μs
Synchronization pulse-duty cycle			10		80	%
<b>Power dissipation all conditions, load fault</b>						
$(P_D)$		Short circuit, overload, Note 6			22	W
<b>Output response to step load changes all configurations</b>						
$(V_{TLD})$		(50% to/from 100%) rated load Note 7	-300		300	mVpk
<b>Recovery time, step load changes all configurations</b>						
$(T_{TLD})$		(50% to/from 100%) rated load Notes 7, 8		200	2000	μs
<b>Output response to step line changes all configurations</b>						
$(V_{TLN})$		20V to / from 40V $I_{OUT} = 100\%$ rated load Note 9	-300		300	mVpk
<b>Recovery time, step line changes all configurations</b>						
$(T_{TLN})$		20V to/from 40V $I_{OUT} = 100\%$ rated load Notes 8, 9		50	2000	μs

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## Electrical Parameters

.....continued						
Parameter	Output	Conditions	Min	Nom	Max	Units
<b>Turn-on response: overshoot by configuration (<math>V_{OS}</math>) (main)</b>						
<b>SA50-28-3R3-12T-x-x</b>		(0% to 100%) rated load Notes 3, 4, 10				mV
(MAIN)	3.3V				500	
(AUX A/B)	12V				750	
<b>SA50-28-3R3-15T-x-x</b>						
(MAIN)	3.3V				500	
(AUX A/B)	15V				750	
<b>SA50-28-5-12T-x-x</b>						
(MAIN)	5V				500	
(AUX A/B)	12V				750	
<b>SA50-28-5-15T-x-x</b>						
(MAIN)	5V				500	
(AUX A/B)	15V				750	
<b>Turn-on response: turn-on delay all configurations</b>						
( $T_{DLY}$ )		Note 10	0.1		10	ms
<b>Capacitive load by configuration (<math>C_{LOAD}</math>)</b>						
<b>SA50-28-3R3-12T-x-x</b>		Note 5				$\mu$ F
(MAIN)	3.3V				1000	
(AUX A/B)	12V				250	
<b>SA50-28-3R3-15T-x-x</b>						
(MAIN)	3.3V				1000	
(AUX A/B)	15V				200	
<b>SA50-28-5-12T-x-x</b>						
(MAIN)	5V				1000	
(AUX A/B)	12V				250	
<b>SA50-28-5-15T-x-x</b>						
(MAIN)	5V				1000	
(AUX A/B)	15V				200	
<b>Line rejection</b>						
		DC to 50 kHz, $I_{OUT} = 100\%$ rated load	40	60		dB

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## Electrical Parameters

.....continued						
Parameter	Output	Conditions	Min	Nom	Max	Units
<b>Isolation</b>						
		50V @25°C 1. Input (1-3) to All (4-12) 2. Sync (4-5) to All (1-3, 6-12) 3. Chassis (6) to All (1-5, 7-12)	100			MΩ
<b>Mass</b>						
		Standard case style A, B		120		g
<b>MTBF</b>						
		MIL-HDBK-217F2, SF, 35°C		8.22x10 <sup>6</sup>		hrs

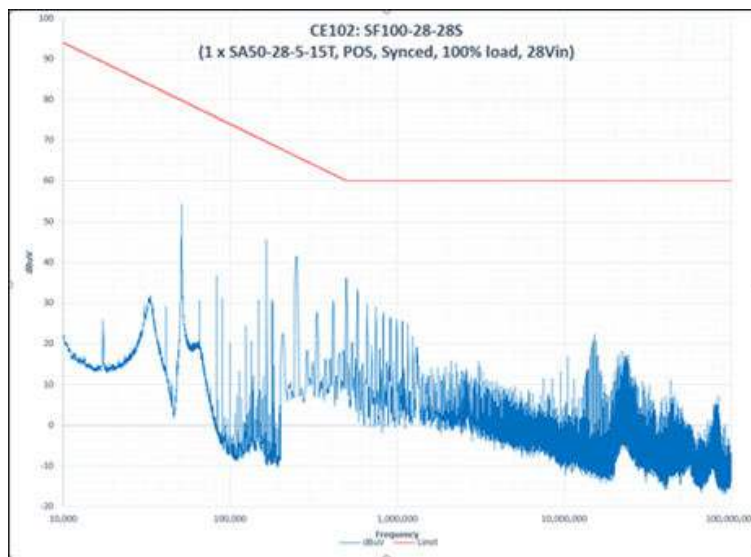
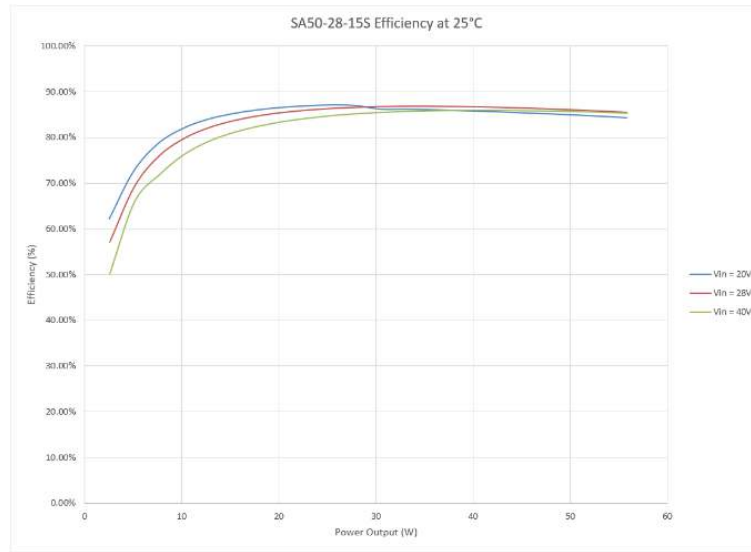
## 6. Radiation Specification (Note 1)

Environment	Conditions	Min	Unit
TID (gamma)	MIL-STD-883, method 1019 The operating bias applied during exposure	100	krad (Si)
Dose rate (gamma dot temporary saturation survival)	MIL-STD-883, method 1023 The operating bias applied during exposure Full-rated load	1E10	rad (Si)/s
Neutron fluence	MIL-STD-883, 1017	1E12	Neutrons
SEE/SEU, SEL, SEGR, SEB	Heavy ions [LET] The operating bias applied during exposure	82	MeV•cm <sup>2</sup> /mg

Notes:

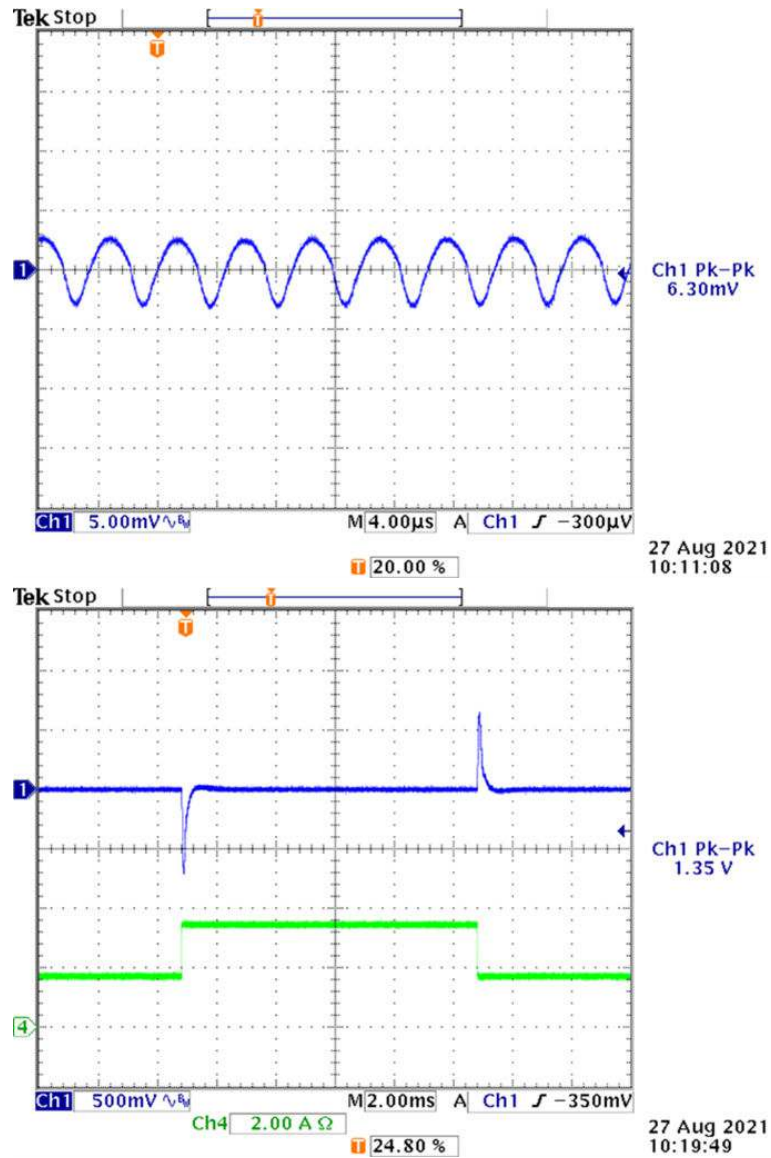
1. Parameter not 100% tested, and only assured by design.
2. Parameter verified during line and load regulation tests. Regulation is specified for 10% to 100% loading on all outputs.
3. The "-H" option incorporates FET technology providing a > 82 MeV•cm<sup>2</sup>/mg (gold ion) SEE capability to the design. The "-P" option is not rated for radiation.
4. Tested and verified using a 20 kHz to 10 MHz bandwidth. Ripple is measured across a 50 Ohms termination with a 10nF Cap in series. Results applicable for DC to 20MHz bandwidth.
5. The capacitive load may be any value from 0 to the maximum limit without compromising DC performance. A capacitive load exceeding the maximum limit may interfere with the proper operation of the converter's overload protection, potentially causing erratic behavior during turn-on.
6. Overload power dissipation is defined as the device power dissipation with the load set such that:  
 $V_{OUT} = 90\%$  of nominal.
7. The load step transition time is  $\geq 10 \mu\text{s}$ .
8. Recovery time is measured from the initiation of the transient to where  $V_{OUT}$  has returned to within  $\pm 1\%$  of its steady-state value.
9. The line step transition time is  $\geq 100 \mu\text{s}$ .
10. Turn-on delay time from either a step application of input power or a logic low to a logic high transition on the inhibit pin (pin 3) to the point where  $V_{OUT} = 90\%$  of nominal.
11. Load regulation relative to the output voltage at 50% rated load.
12. Line regulation relative to the output voltage at 28 VDC input.
13. For operation at temperatures between 85 °C and 125 °C: de-rate power linearly from 50 W (or rated maximum) to zero. Parameter limits are not guaranteed.
14. Auxiliary output regulation is not maintained if main output load is less than 10%.
15. Auxiliary output require at least 10% loading for specified regulation. Voltage may increase at lighter loads and is limited by overvoltage Zener diodes.
16. Unless otherwise specified rated load means 20W on the main, and 15W on each Auxiliary output. Other load settings are acceptable provided the total of 50W is not exceeded, and minimum output current limits are satisfied.

### 7. Sample Electrical Waveforms (for reference only)



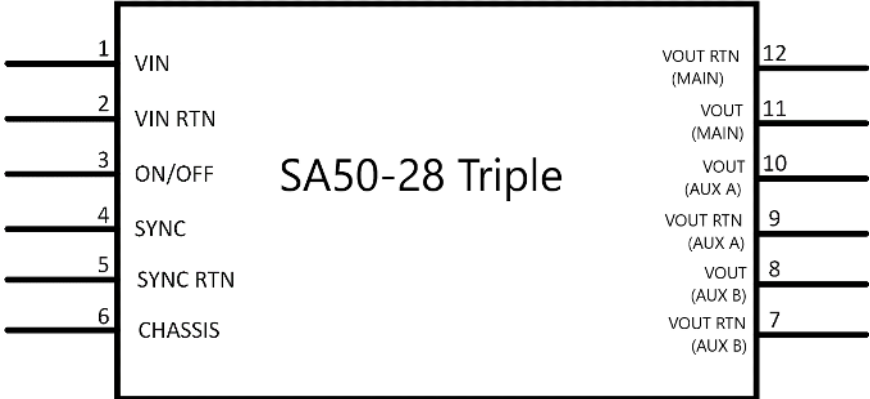
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## Sample Electrical Waveforms (for reference only)



8. Pin Configuration

Figure 8-1. SA50-28 Triple Pin Configuration





**9. Pin Description**

<b>PIN</b>	<b>NAME</b>	<b>Description</b>
1	VIN	Input Voltage
2	VIN RTN	Input Voltage Return/Ground
3	ON/OFF (INHIBIT)	Power Supply ON/OFF, [ON(OPEN/HIGH), OFF(SHORT/LOW)]
4	SYNC	External Clock Signal Input
5	SYNC RTN	External Clock Signal Return
6	CHASSIS	Chassis Pin
7	VOUT (AUX B) RTN	Auxiliary B Vout return
8	VOUT (AUX B)	Auxiliary B Vout
9	VOUT (AUX A) RTN	Auxiliary A Vout return
10	VOUT (AUX A)	Auxiliary A Vout
11	VOUT (MAIN)	Main Vout
12	VOUT (MAIN) RTN	Main Vout return

**10. Radiation Performance (-H) Hardened**

- TID>100krad(Si) and 30krad(Si) ELDRS (<10mrad/s) per MIL-STD-883 Method 1019
- SEE (SEGR, SEB, SET, SEL) immunity 82 MeV·cm<sup>2</sup>/mg (H-hardened)

**11. Radiation Performance (-P) Prototype**

Prototype units that are functionally the same except that components are not radiation hardened. To be used for system checkout.

**12. Mechanical Outline (-A) Package**

Figure 12-1. Axial Pins and Thru-hole Tabs Package

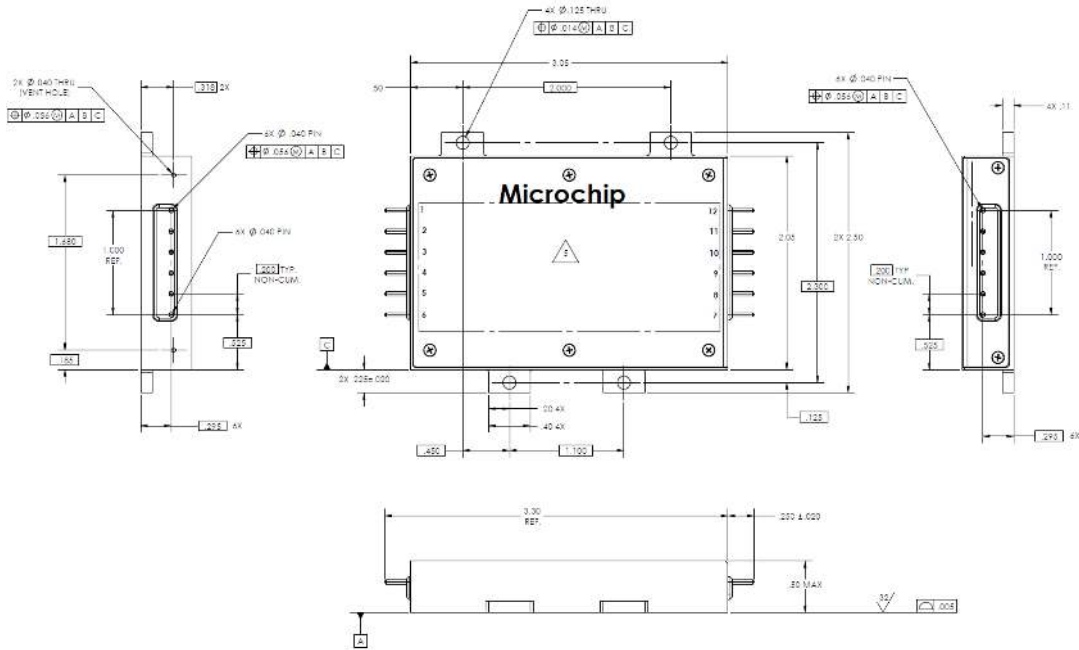
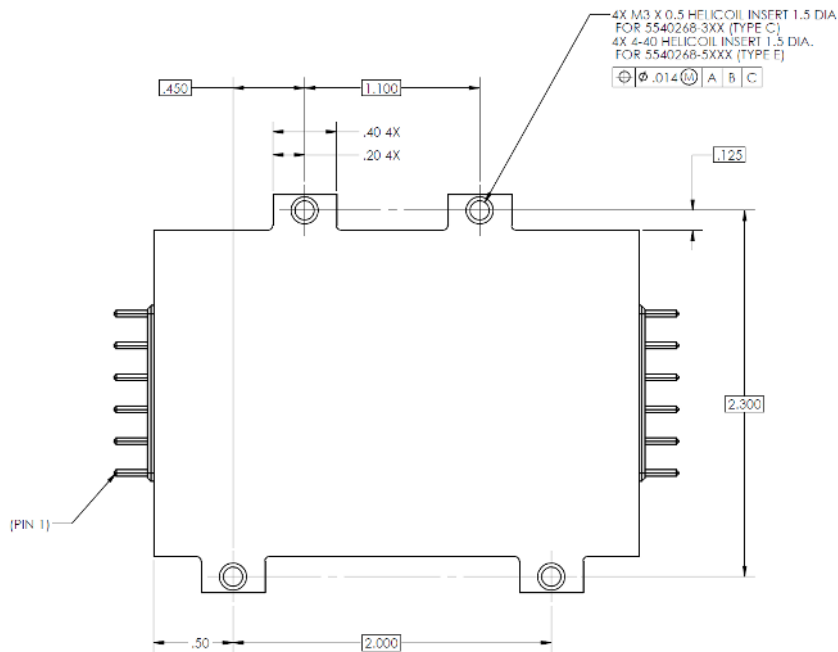


Figure 12-2. Axial Pins and Thru-hole Tabs Bottom View



### 13. Mechanical Outline (-B) Package

Figure 13-1. Radial Pins and Threaded Tabs Package

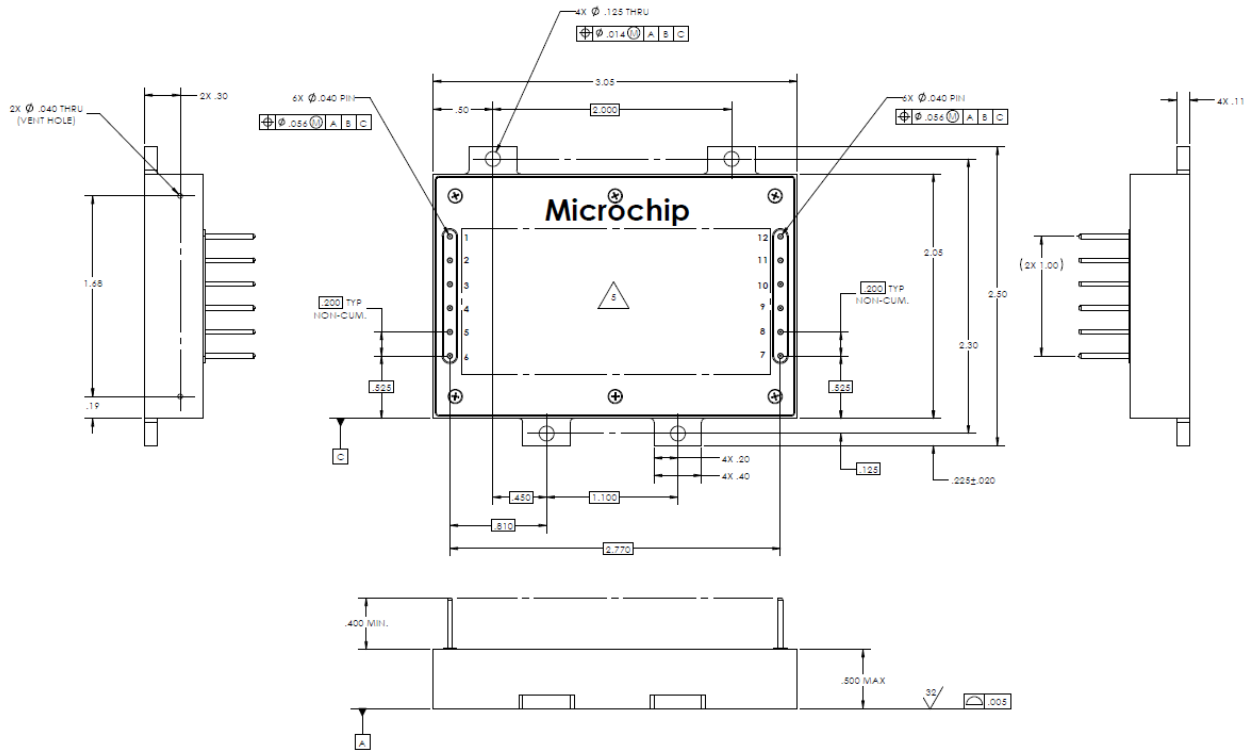
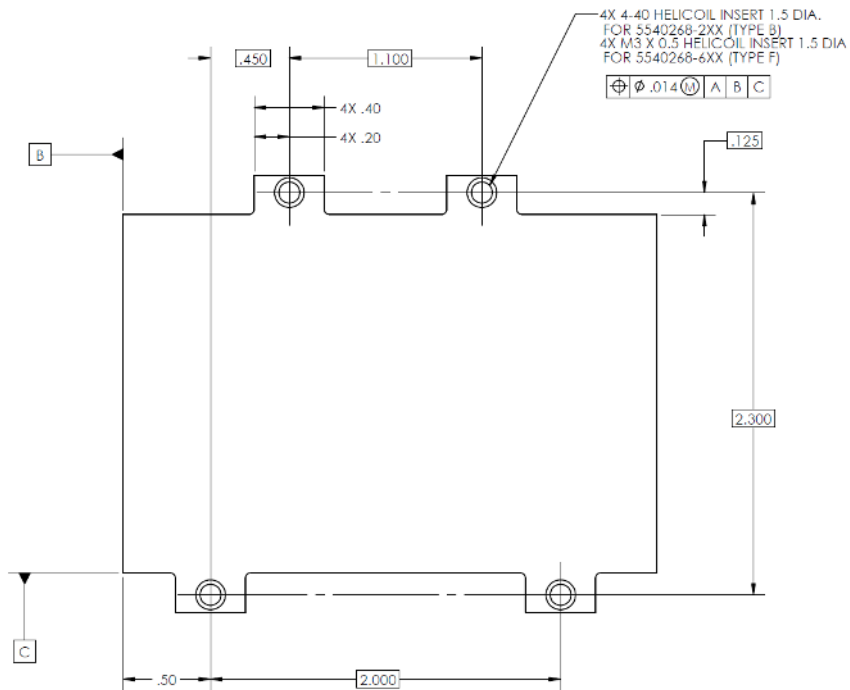


Figure 13-2. Axial Pins and Threaded Tabs Package Bottom View



**14. Qualification Test (Reference Report QTR996)**

Test	Conditions
External visual	Per O&M—dimensions, and mass or STD 883 2009
Electrical	Read and record (–55 °C, 25 °C, 85 °C)
Shock, non-operating	MIL-STD-202, method 213B, test condition F, 1500 gpk, 0.5 ms ½ sine pulse. Three pulses in each direction of each axis, 18 pulses total.
Vibration, operating	MIL-STD-202, method 214A, condition II-F, 24.06 grms random vibrations, 50 Hz– 2000 Hz, 3 min/axis (9 min total). Outputs monitored.
Temperature cycling	10 cycles from base plate temperature, MIL-STD-883, method 1010.9, condition C
EMI	CE101, CE102, CS101, RE101, RE102, RS101, RS102 per MIL-STD-461 with setup per MIL-STD-462.
External Visual inspection	No damage
Steady state life test	1000 hrs at Tc = 105 °C, 50% of rated load
End-point electricals	Read and record ( –55 °C, 25 °C, 85 °C)

**15. ATP Screening Test (-H) Hardened**

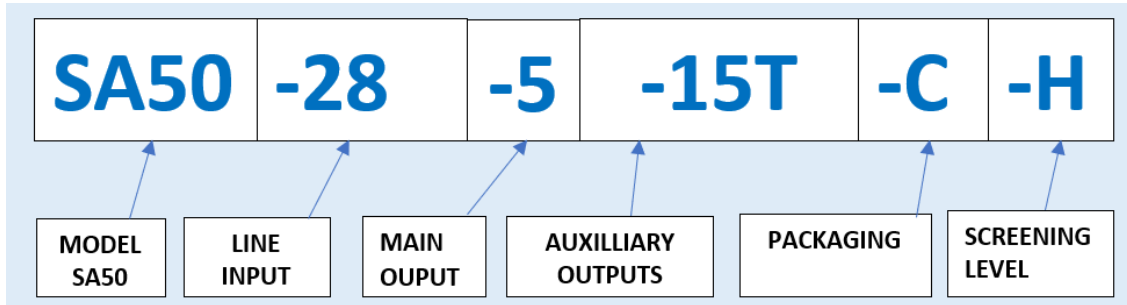
Requirement	Test Method/Condition
External Visual	O&M – dimensions and mass
Initial Electrical	Full performance at +25°C
Vibration	Workmanship non-operating vibration MIL-STD-202, Method 214, 6 g <sub>rms</sub> (50Hz-2kHz), 1-minute perpendicular to the board
Post Vibration Electrical	Full performance at +25°C
Temperature Cycle	MIL-STD-883, Method 1010, Condition A, 1 cycle, +85°C to -55°C, operating Outputs monitored during thermal cycles
Burn-in	40 Hrs @ 105°C, 50% of rated load (outputs monitored)
Final Electrical	Full performance at +25°C (deliverable data)
External Visual	No damage

**16. ATP Screening Test (-P) Prototypes**

Requirement	Test Method/Condition
External Visual	O&M – dimensions and mass
Electrical	Full performance at +25°C
Vibration	None
Temperature Cycle	None
Burn-in	None
External Visual	No damage



### 17. Ordering Information



<b>Model</b>	SA50	Standard Applications 50W, 28V input modules.		
<b>Line input</b>	-28	28.0V	Line input voltage. Nominal input line	
<b>Main</b>	-3R3	3.3V	Main output voltage	
	-5	5.0V		
<b>Aux</b>	-12T	12.0V	Auxiliary output voltages. (A and B outputs identical) (T is for triple)	
	-15T	15.0V		
<b>Mechanical Package</b>	-A	Axial	0.125in thru-hole	Mechanical packaging options. Electrical connections are either Radial or Axial. And mounting holes are drilled thru-hole or Threaded.
	-B	Radial	4-40 thread	
	-C	Axial	M3 thread	
	-D	Radial	0.125in thru-hole	
	-E	Axial	4-40 thread	
	-F	Radial	M3 thread	
<b>Radiation Hardness</b>	-H	Hardened	We offer units with two levels of radiation screening. Hardened and Prototype (non-hardened) units.	
	-P	Prototype		

NOTE: Other input voltage and output voltage combinations are available. Please contact your local sales representative.

We also offer a thermal interface, the ST-2X3; this is a non-silicon, space-approved thermal interface. Datasheet available upon request.

**18. Revision History**

Revision	Date	Description
D	07/2022	Updated Electrical information in the ATP Screening Test (-P) Prototypes table.
C	04/2022	Updated Figure 13-1.

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- Embedded Solutions Engineer (ESE)
- Technical Support

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