

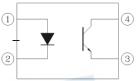
DATASHEET

4 PIN DIP PHOTOTRANSISTOR PHOTOCOUPLER **EL816 Series**









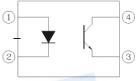
Features:

- Compliance Halogens Free (Only copper leadframe) (Br < 900 ppm, Cl < 900 ppm, Br+Cl < 1500 ppm)
- Current transfer ratio

(CTR: $50\sim600\%$ at $I_F = 5mA$, $V_{CE} = 5V$) (CTR: $63\sim320\%$ at $I_F = 10mA$, $V_{CE} = 5V$)

- High isolation voltage between input and output (Viso=5000Vrms)
- Creepage distance > 7.62mm
- Operating temperature up to +110°C
- · Compact small outline package
- The product itself will remain within RoHS compliant version
- Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

Schematic



Pin Configuration

- 1. Anode
- 2. Cathode
- 3. Emitter
- 4. Collector

Description

The EL816 series of devices each consist of an infrared emitting diodes, optically coupled to a phototransistor detector.

They are packaged in a 4-pin DIP package and available in wide-lead spacing and SMD option.

Applications

- Programmable controllers
- System appliances, measuring instruments
- Telecommunication equipments
- Home appliances, such as fan heaters, etc.
- Signal transmission between circuits of different potentials and impedances



Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
	Forward current	I _F	60	mA
	Peak forward current (1us, pulse)	I _{FP}	1	А
Input	Reverse voltage	V_{R}	6	V
	Power Dissipation No derating required up to $T_a = 100^{\circ}C$	P_{D}	100	mW
	Power dissipation		150	mW
	Derating factor (above $T_a = 80^{\circ}C$)	P _C —	5.8	mW/°C
Output	Collector current	I _C	50	mA
	Collector-Emitter voltage	V _{CEO}	80	V
	Emitter-Collector voltage	V _{ECO}	6	V
Total Powe	er Dissipation	P _{TOT}	200	mW
Isolation V	oltage*1	V _{ISO}	5000	Vrms
Operating	Temperature	T _{OPR}	-55 to 110	°C
Storage Te	emperature	T _{STG}	-55 to 125	°C
Soldering	Temperature*2	T _{SOL}	SOL 260	

Notes:

^{*1} AC for 1 minute, R.H.= $40 \sim 60\%$ R.H. In this test, pins 1, 2 are shorted together, and pins 3, 4 are shorted together.

^{*2} For 10 seconds



Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Forward Voltage	V_{F}	-	1.2	1.4	V	I _F = 20mA
Reverse Current	I _R	-	-	10	μΑ	$V_R = 4V$
Input capacitance	C _{in}	-	30	250	рF	V = 0, f = 1kHz

Output

Parameter	Symbol	Min	Тур.	Max.	Unit	Condition	
Collector-Emitter dark	I _{CEO}	_	_	100	nA	$V_{CE} = 20V, I_F = 0mA$	
current	ICEO			100	117 (• GE = 20 v, IF = 0111/1	
Collector-Emitter	BV_CEO	80	_	_	V	$I_C = 0.1 \text{mA}$	
breakdown voltage	D A CEO	00			v	IC = 0.1111A	
Emitter-Collector	BV_ECO	6	_	_	V	I _E = 0.1mA	
breakdown voltage	D A ECO	O	_		· ·	IE = 0. IIIIA	

Transfer Characteristics

Param	eter	Symbol	Min	Тур.	Max.	Unit	Condition
	EL816	CTR	50	-	600	- - - % -	I _F = 5mA ,V _{CE} = 5V
	EL816A		80	-	160		
	EL816B		130	-	260		
	EL816C		200	-	400		
	EL816D		300	-	600		
Current Transfer ratio	EL816X		100	-	200		
	EL816Y		150	-	300		
	EL816I	 CTR 	63	-	125		$I_F = 10 \text{mA}, V_{CE} = 5 \text{V}$
	EL816J		100	-	200		
	EL816K		160	-	320	2/	
	EL816I		22	-	-	%	$I_F = 1 \text{mA}$, $V_{CE} = 5 \text{V}$
	EL816J		34	-	-		
	EL816K		56	-	-		



Transfer Characteristics (T_a=25°C unless specified otherwise) Continuity

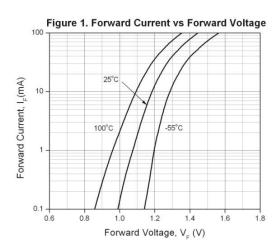
Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Collector-Emitter saturation voltage	$V_{\text{CE(sat)}}$	-	0.1	0.2	V	$I_F = 20 \text{mA}, I_C = 1 \text{mA}$
Isolation resistance	R _{IO}	5×10 ¹⁰	-	-	Ω	V _{IO} = 500Vdc, 40~60% R.H.
Floating capacitance	C_{IO}	-	0.6	1.0	рF	$V_{IO} = 0$, $f = 1MHz$
Cut-off frequency	fc	-	80	-	kHz	$V_{CE} = 5V$, $I_C = 2mA$ $R_L = 100\Omega$, -3dB
Rise time	t _r	-	4	18	μs	$V_{CE} = 2V$, $I_C = 2mA$,
Fall time	t _f	-	3	18	μs	$R_L = 100\Omega$

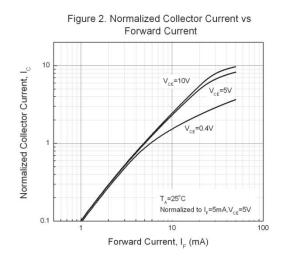
^{*} Typical values at T_a = 25°C

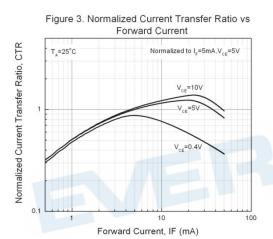


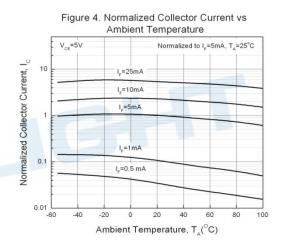


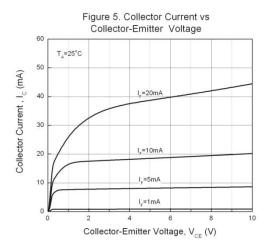
Typical Electro-Optical Characteristics Curves

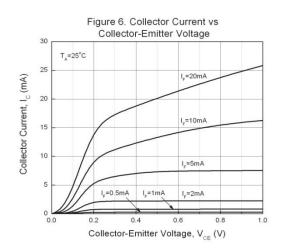




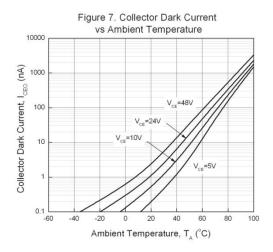


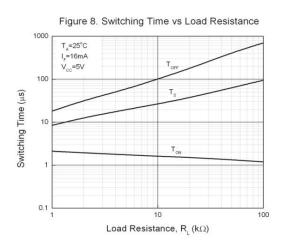


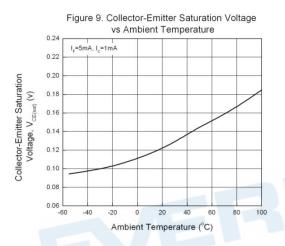












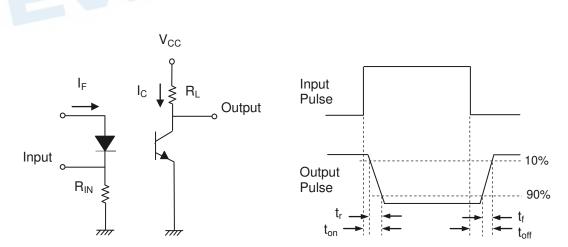


Figure 10. Switching Time Test Circuit & Waveforms



Order Information

Part Number

EL816X(Y)(Z)-FV

Note

= Lead form option (S1, S2, M or none)

= CTR Rank (A, B, C, D, X, Y, I, J, K or none) = Tape and reel option (TU, TD or none). Υ

Ζ

= Lead frame option (F: Iron, None: copper)

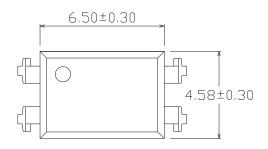
= VDE safety (optional).

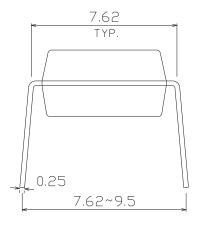
Option	Description	Packing quantity
None	Standard DIP-4	100 units per tube
М	Wide lead bend (0.4 inch spacing)	100 units per tube
S1 (TU)	Surface mount lead form (low profile) + TU tape & reel option	1500 units per reel
S1 (TD)	Surface mount lead form (low profile) + TD tape & reel option	1500 units per reel
S2 (TU)	Surface mount lead form (low profile) + TU tape & reel option	2000 units per reel
S2 (TD)	Surface mount lead form (low profile) + TD tape & reel option	2000 units per reel

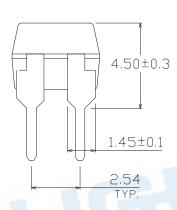


Package Dimension (Dimensions in mm)

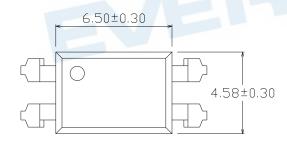
Standard DIP Type

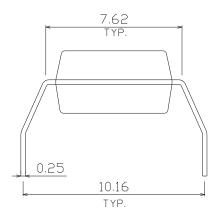


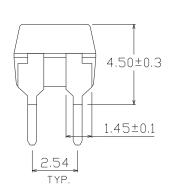




Option M Type

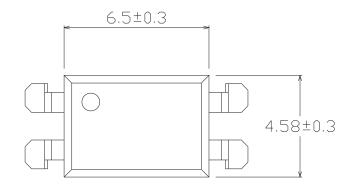


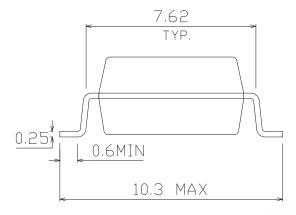


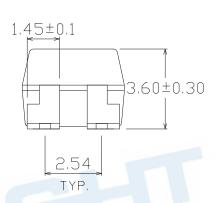




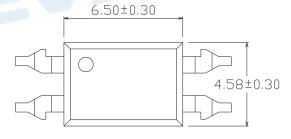
Option S1 Type

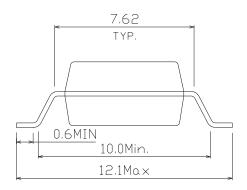


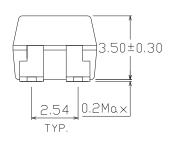




Option S2 Type



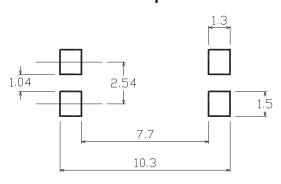




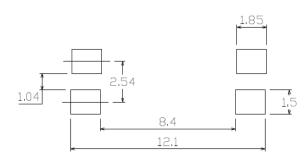


Recommended pad layout for surface mount leadform

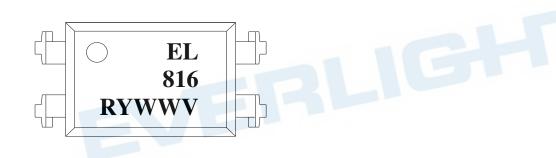
For S1 option



For S2 option



Device Marking



Notes

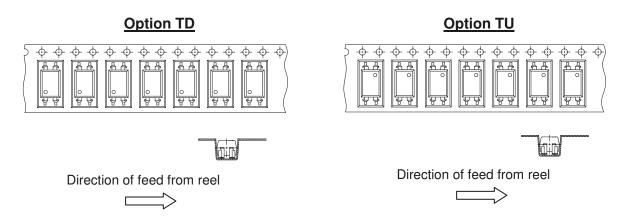
EL denotes EVERLIGHT 816 denotes Device Number

R denotes CTR Rank(A, B, C, D, X, Y, I, J, K or none)

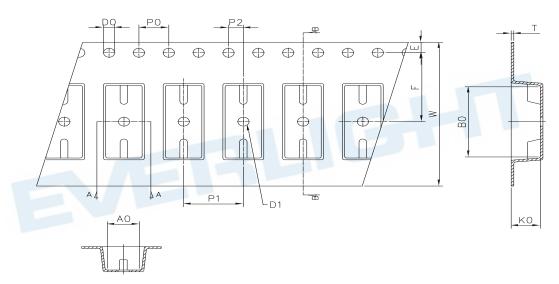
Y denotes 1 digit Year code WW denotes 2 digit Week code V denotes VDE (optional)



Tape & Reel Packing Specifications



Tape dimensions



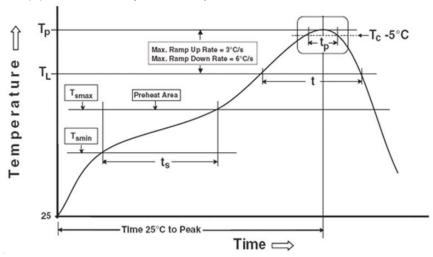
Dimension No.	Ao	Во	Do	D1	E	F
Dimension (mm) S1	4.90±0.1	10.40±0.1	1.5±0.1	1.50±0.1	1.75±0.1	7.50±0.1
Dimension (mm) S2	4.88±0.1	12.55±0.1	1.5±0.1	1.50±0.1	1.75±0.1	11.5±0.1
Dimension No.	Ро	P1	P2	t	W	Ko
Dimension No. Dimension (mm) S1	Po 4.00±0.1	P1 8.00±0.	P2 2.00±0.1	0.40±0.1	W 16.00±0.3	Ko 4.60±0.1



Precautions for Use

1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

Preheat

Temperature min (T_{smin}) Temperature max (T_{smax})

Time $(T_{smin} \text{ to } T_{smax}) (t_s)$

Average ramp-up rate (T_{smax} to T_p)

Other

Liquidus Temperature (T_L)

Time above Liquidus Temperature (t₁)

Peak Temperature (T_P)

Time within 5 °C of Actual Peak Temperature: T_P - 5°C

Ramp- Down Rate from Peak Temperature

Time 25°C to peak temperature

Reflow times

Reference: IPC/JEDEC J-STD-020D

150 °C

200°C

60-120 seconds

3 °C/second max

217 °C

60-100 sec

260°C

30 s

6°C /second max.

8 minutes max.

3 times



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