

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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MONOLITHIC POWER MOS FET ARRAY

DESCRIPTION

The  $\mu$ PA1600 is Monolithic N-channel Power MOS FET Array that built in 8 circuits and Gate Protection Diode designed for LED, Relay, Thermal Head, and so on.

FEATURES

- Direct driving is possible by standard Logic IC or Microcomputer. (4 V driving is possible)
- Output Voltage:  $V_o = 30$  V MAX.  
Output Current:  $I_o = 500$  mA MAX.
- $R_{on} = 3 \Omega$  at:  $I_o = 200$  mA,  $V_i = 4$  V
- Gate Protection Diode, built in.

ORDERING INFORMATION

Part Number	Package	Quality Grade
$\mu$ PA1600CX	20-Pin DIP	Standard
$\mu$ PA1600GS	20-Pin SOP	Standard

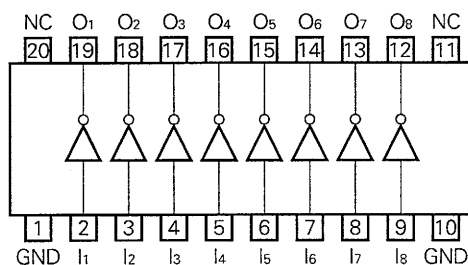
Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )

Output Voltage	$V_o$	30	V
Input Voltage	$V_i$	-0.5 to +10	V
Output Current	$I_o$	500	mA/unit
Input Current	$I_i$	$\pm 10$	mA/unit
GND Terminal Current*	$I_{GND}$	2.0	A
Total Power Dissipation	$P_T$	1.0	W/PKG
Operating Temperature	$T_{opt}$	-40 to +85	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$

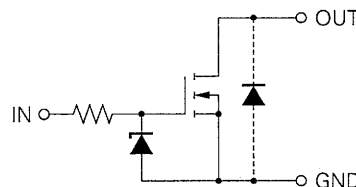
\* Case of 2 GND terminals set up 0 V level

CONNECTION DIAGRAM



I : Input  
O : Output

Equivalent Circuits (1/8 Circuit)



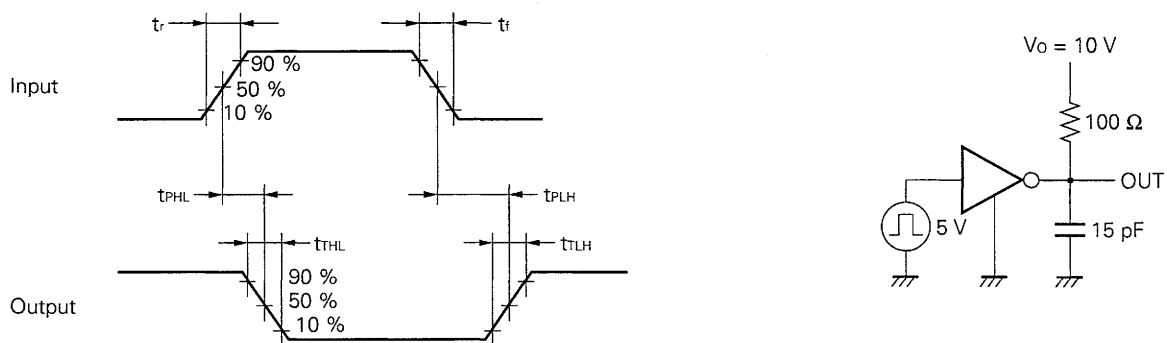
**RECOMMENDED OPERATING CONDITIONS ( $T_a = -40$  to  $+85$  °C)**

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Output Voltage	$V_o$			25	V	
Output Current	$I_o$			300	mA/unit	DC, 1 circuit
	$I_{O(pulse)}$			150	mA/unit	$PW \leq 10$ ms, Duty Cycle $\leq 50$ %, 8 circuits
High-Level Input Voltage	$V_{IH}$	4			V	
Low-Level Input Voltage	$V_{IL}$			0.8	V	

**ELECTRICAL CHARACTERISTICS ( $T_a = 25$  °C)**

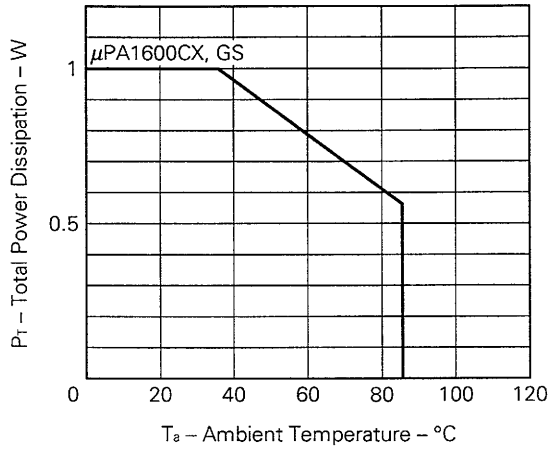
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Output Leakage Current	$I_{O(OFF)}$			10	$\mu$ A	$V_i = 0, V_o = 30$ V
Output On-state Resistance	$R_{on}$		3	4	$\Omega$	$V_i = 4$ V, $I_o = 200$ mA
Output On-state Voltage	$V_{O(ON)1}$			0.1	V	$V_i = 5$ V, $I_o = 10$ mA
	$V_{O(ON)2}$			0.8	V	$V_i = 5$ V, $I_o = 200$ mA
Input Voltage	$V_{I(OFF)}$			0.8	V	$V_o = 30$ V, $I_o = 100$ $\mu$ A
	$V_{I(ON)1}$	2			V	$V_o = 0.8$ V, $I_o = 1$ mA
	$V_{I(ON)2}$	4			V	$V_o = 0.8$ V, $I_o = 200$ mA
Input Current	$I_{IH}$			1	$\mu$ A	$V_i = 8$ V, $V_o = 0$ V
	$I_{IL}$			-1	$\mu$ A	$V_i = 0$ V, $V_o = 20$ V
Input Capacitance	$C_{iss}$		28		pF	$V_i = 0$ V
Output Capacitance	$C_{oss}$		23		pF	$V_o = 10$ V
Reverse Transfer Capacitance	$C_{rss}$		13		pF	$f = 1$ MHz
Delay Time	$t_{PHL}$		50		ns	$V_o = 10$ V, $R_L = 100$ $\Omega$ $C_L = 15$ pF $t_r, t_f \leq 5$ ns See Fig. 1
	$t_{PLH}$		250		ns	
Rise Time	$t_{TLH}$		220		ns	
Fall Time	$t_{THL}$		180		ns	

**Fig. 1 Switching Wave Forms and Test Circuits**

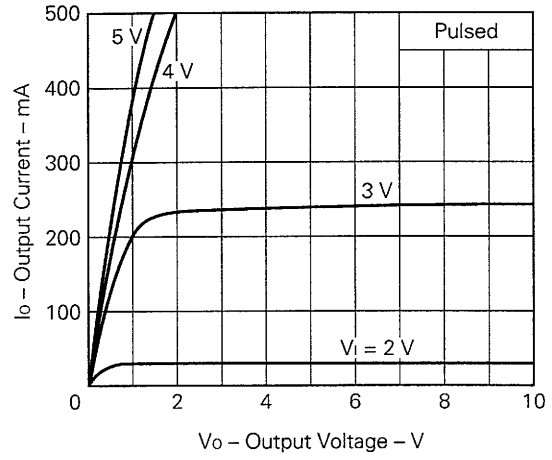


TYPICAL CHARACTERISTICS ( $T_a = 25^\circ\text{C}$ )

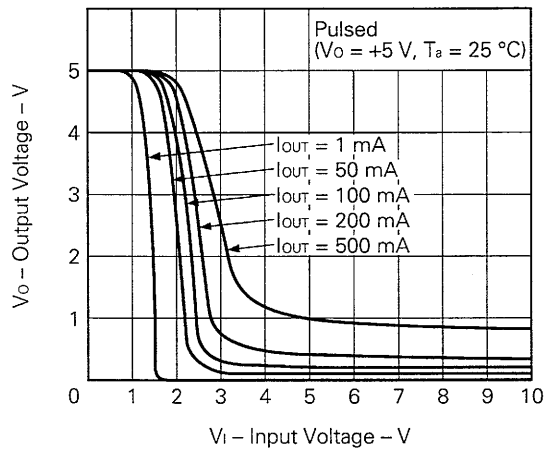
$P_T - T_a$  Characteristic



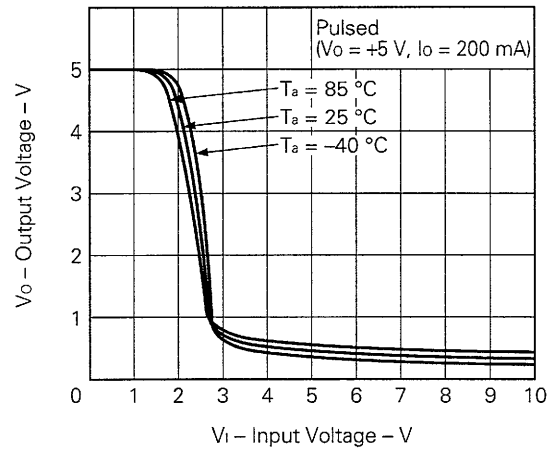
$I_o - V_o$  Characteristic



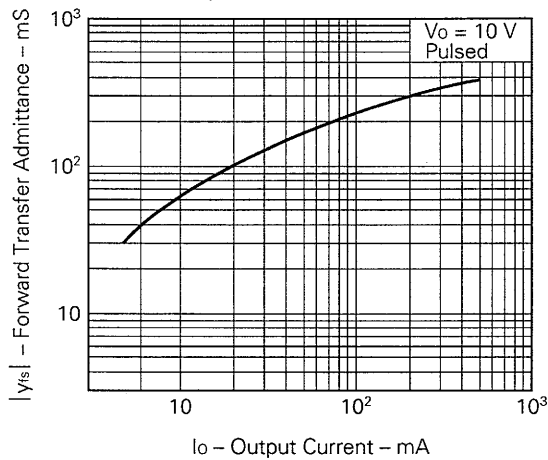
$V_o - V_i$  Characteristic



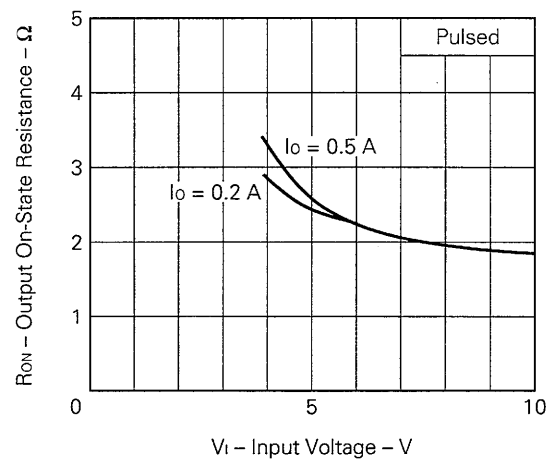
$V_o - V_i$  Characteristic

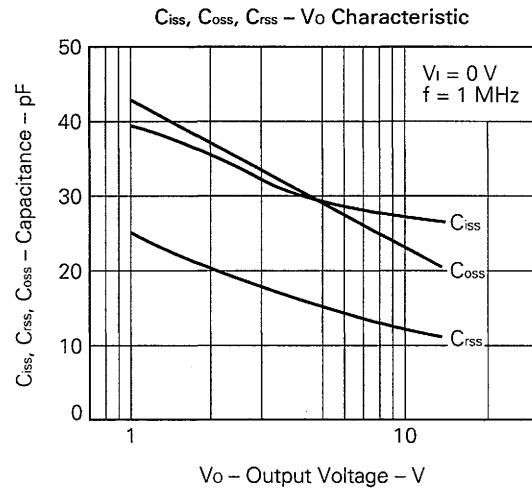
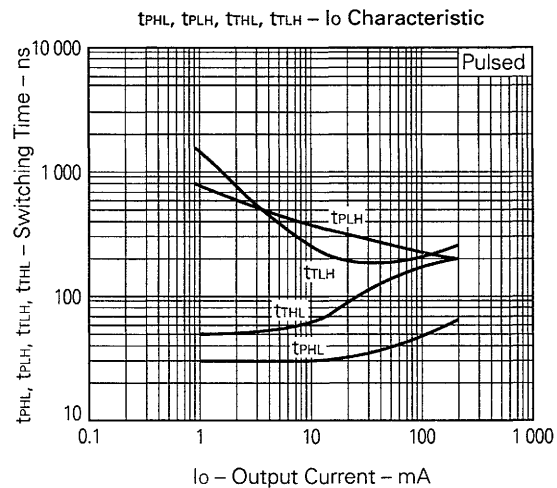
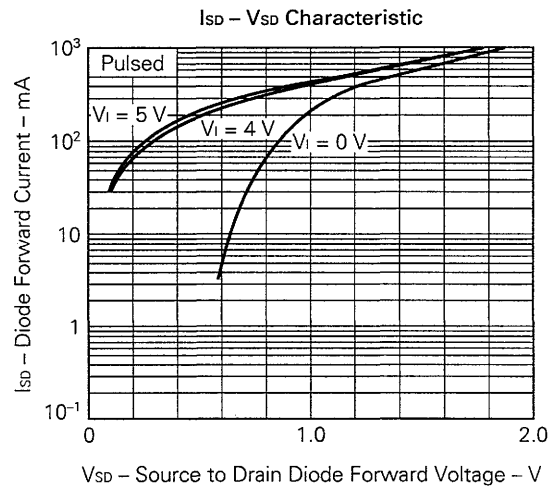
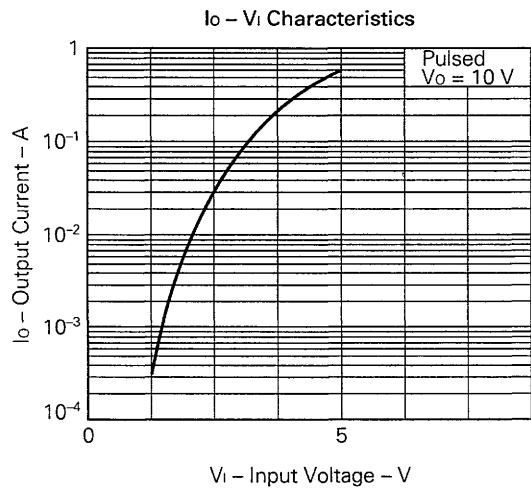
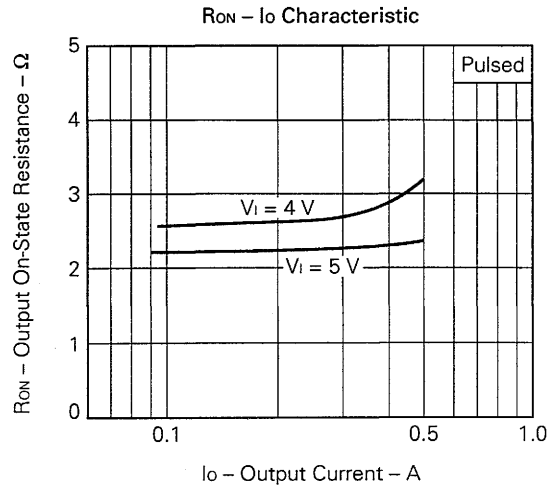
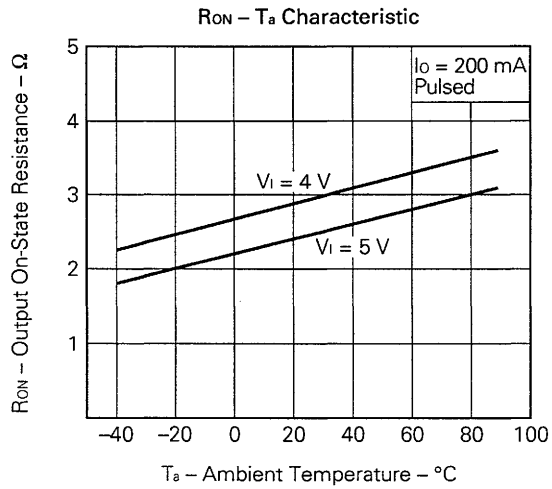


$|y_{fs}| - I_o$  Characteristic



$R_{ON} - V_i$  Characteristic

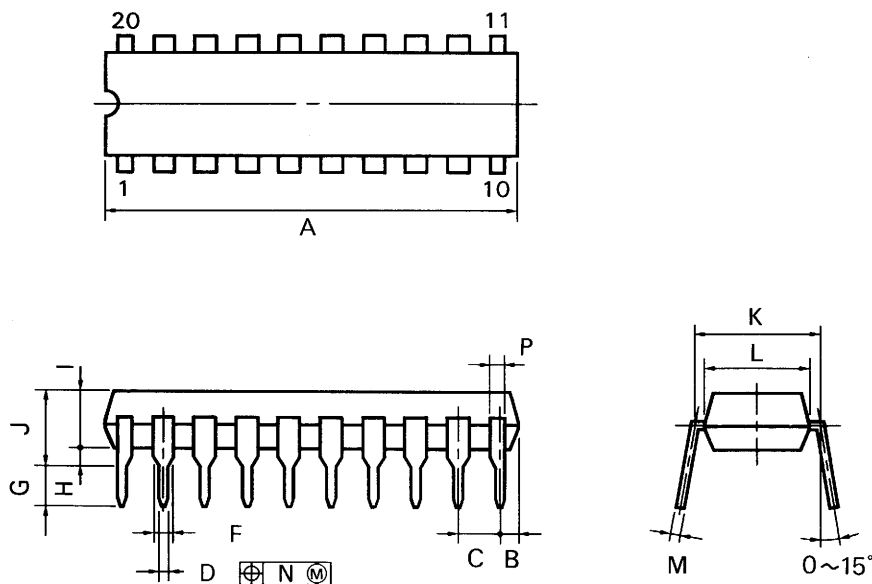




PACKAGE DIMENSIONS

• μPA1600CX

20PIN PLASTIC DIP (300 mil)



P20C-100-300A,C

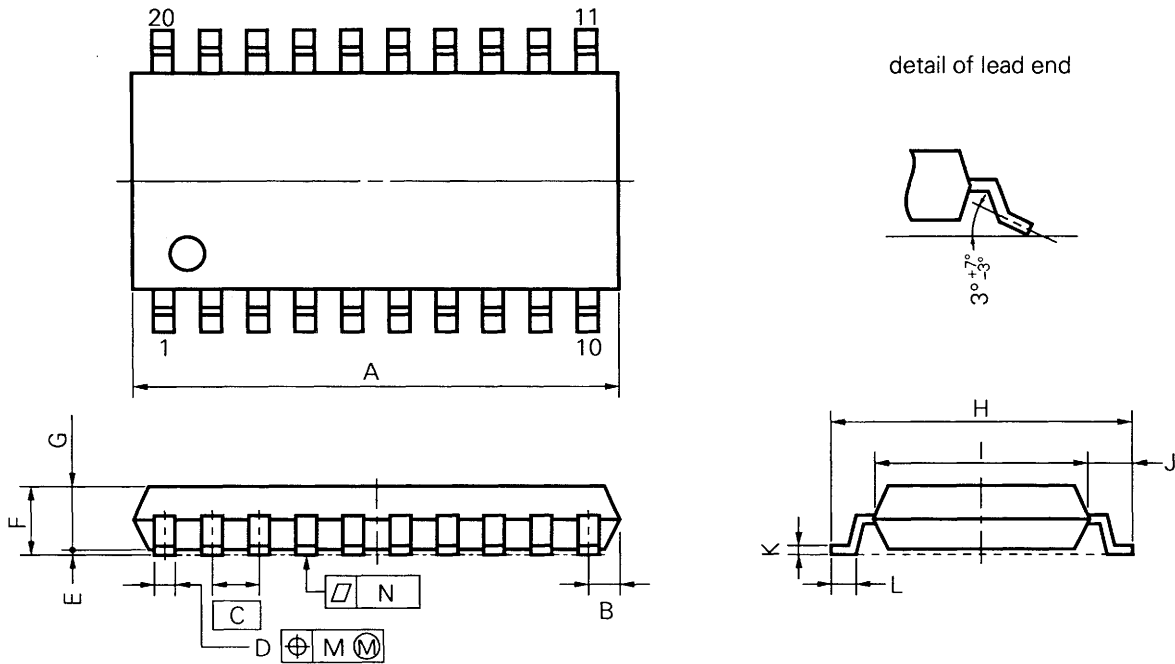
NOTES

- 1) Each lead centerline is located within 0.25 mm (0.01 inch) of its true position (T.P.) at maximum material condition.
- 2) Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS	INCHES
A	25.40 MAX.	1.000 MAX.
B	1.27 MAX.	0.050 MAX.
C	2.54 (T.P.)	0.100 (T.P.)
D	0.50 ±0.10	0.020 <sup>+0.004</sup> / <sub>-0.005</sub>
F	1.1 MIN.	0.043 MIN.
G	3.5 ±0.3	0.138 ±0.012
H	0.51 MIN.	0.020 MIN.
I	4.31 MAX.	0.170 MAX.
J	5.08 MAX.	0.200 MAX.
K	7.62 (T.P.)	0.300 (T.P.)
L	6.4	0.252
M	0.25 <sup>+0.10</sup> / <sub>-0.05</sub>	0.010 <sup>+0.004</sup> / <sub>-0.003</sub>
N	0.25	0.01
P	0.9 MIN.	0.035 MIN.

• μPA1600GS

20 PIN PLASTIC SOP (300 mil)



P20GM-50-300B,C-3

**NOTE**

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
A	13.00 MAX.	0.512 MAX.
B	0.78 MAX.	0.031 MAX.
C	1.27 (T.P.)	0.050 (T.P.)
D	0.40 <sup>+0.10</sup> <sub>-0.05</sub>	0.016 <sup>+0.004</sup> <sub>-0.003</sub>
E	0.1±0.1	0.004±0.004
F	1.8 MAX.	0.071 MAX.
G	1.55	0.061
H	7.7±0.3	0.303±0.012
I	5.6	0.220
J	1.1	0.043
K	0.20 <sup>+0.10</sup> <sub>-0.05</sub>	0.008 <sup>+0.004</sup> <sub>-0.002</sub>
L	0.6±0.2	0.024 <sup>+0.008</sup> <sub>-0.009</sub>
M	0.12	0.005
N	0.10	0.004



**RECOMMENDED SOLDERING CONDITIONS**

The following conditions (see table below) must be set when soldering this product.

Please consult with our sales offices in case other soldering process is used, or in case soldering is done under different conditions.

**TYPES OF SURFACE MOUNT DEVICE**

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (IEI-1207).

μPA1600GS

Soldering process	Soldering conditions	Symbol
Infrared ray reflow	Peak package's surface temperature: 235 °C or below, Reflow time: 30 seconds or below (210 °C or higher), Number of reflow process: Inside of 2 times, Exposure limit*: None	IR35-00-2
VPS	Peak package's surface temperature: 215 °C or below, Reflow time: 40 seconds or below (200 °C or higher), Number of reflow process: Inside of 2 times, Exposure limit*: None	VP15-00-2
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below, Number of flow process: 1, Exposure Limit*: None	WS-60-00-1

\*: Exposure limit before soldering after dry-pack package is opened.  
Storage conditions: 25 °C and relative humidity at 65 % or less.

**Note:** Do not apply more than a single process at once, except for "Partial heating method".

**TYPES OF THROUGH HOLE MOUNT DEVICE**

μPA1600CX

Soldering process	Soldering conditions	Symbol
Wave soldering	Solder temperature: 260 °C or below, Flow time: 10 seconds or below	

**Reference**

Document name	Document No.
Quality control of NEC semiconductors devices.	TEI-1202
Quality control guide of semiconductors devices.	MEI-1202
Assembly manual of semiconductors devices.	IEI-1207
Semiconductor device package manual	IEI-1213
SMD surface mount technology manual	IEI-1207

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