

BD534/536/538

Medium Power Linear and Switching Applications

- · Low Saturation Voltage
- Complement to BD533, BD535 and BD537 respectively



1.Base 2.Collector 3.Emitter

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Para	Value	Units	
V _{CBO}	Collector-Base Voltage	: BD534	- 45	V
020		: BD536	- 60	V
		: BD538	- 80	V
V _{CEO}	Collector-Emitter Voltage	: BD534	- 45	V
		: BD536	- 60	V
		: BD538	- 80	V
V _{EBO}	Emitter-Base Voltage		- 5	V
I _C	Collector Current (DC)		- 8	Α
I _B	Base Current		- 1	Α
P _C	Collector Dissipation (T _C =25°C)	50	W	
T _J	Junction Temperature		150	°C
T _{STG}	Storage Temperature		- 65 ~ 150	°C

Electrical Characteristics $T_{C}=25^{\circ}C$ unless otherwise noted

Symbol	Para	meter		Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off C	urrent : B	D534	V _{CB} = - 45V, I _E = 0			- 100	μΑ
		: BD	536	$V_{CB} = -60V, I_{E} = 0$			- 100	μΑ
		: BD	538	$V_{CB} = -80V, I_{E} = 0$			- 100	μΑ
I _{CES}	Collector Cut-off C	urrent : B	D534	$V_{CE} = -45V, V_{BE} = 0$			- 100	μΑ
		: BD	536	$V_{CE} = -60V, V_{BE} = 0$			- 100	μΑ
		: BD	538	$V_{CE} = -80V, V_{BE} = 0$			- 100	μΑ
I _{EBO}	Emitter Cut-off Cu	rrent		$V_{EB} = -5V, I_{C} = 0$			- 1	mA
h _{FE}	* DC Current Gain	: ALL DEVI	ICE	$V_{CE} = -2 \text{ V}, I_{C} = -500 \text{mA}$	40			
		: BD534/536		$V_{CE} = -5V, I_{C} = -10mA$	20			
		: BD538		-	15			
		: BD534/536		$V_{CE} = -2V, I_{C} = -2A$	25			
		: BD538			15			
h _{FE}	h _{FE} Groups							
	J	: ALL DEVIC	Ε	$V_{CE} = -2V, I_{C} = -2A$	30		75	
				$V_{CE} = -2V, I_{C} = -3A$	15			
	K	: ALL DEVIC	Ε	$V_{CE} = -2V, I_{C} = -2A$	40		100	
				$V_{CE} = -2V, I_{C} = -3A$	20			
V _{CE} (sat)	* Collector-Emitter Saturation Voltage		I _C = - 2A, I _B = - 0.2A			- 0.8	V	
				$I_C = -6A$, $I_B = -0.6A$		- 0.8		V
V _{BE} (on)	* Base-Emitter ON Voltage		$V_{CE} = -2V, I_{C} = -2A$			- 1.5	V	
f⊤	Current Gain Bandwidth Product		$V_{CF} = -1V, I_{C} = -500 \text{mA}$	3	12		MHz	

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Typical Characteristics

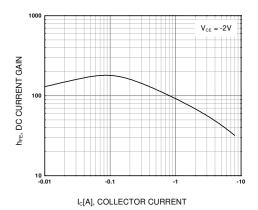


Figure 1. DC current Gain

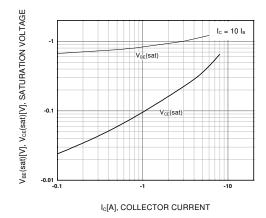


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

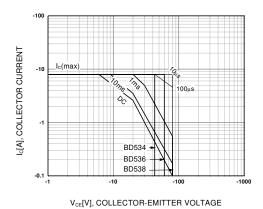


Figure 3. Safe Operating Area

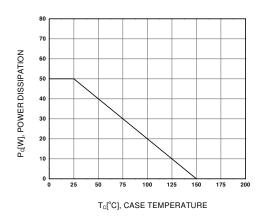
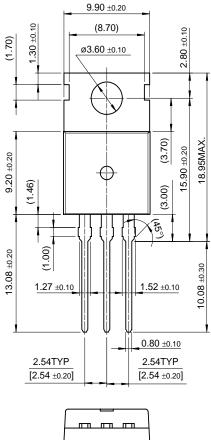


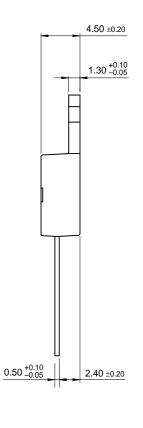
Figure 4. Power Derating

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Package Demensions

TO-220





10.00 ±0.20

Dimensions in Millimeters

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