

SIDC05D60C8

Fast switching diode chip in Emitter Controlled 3 -Technology

Features:

- 600V Emitter Controlled 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- small temperature coefficient

This chip is used for:

- Power module
- Discrete components



Applications:

- **Drives**
- White goods
- Resonant applications

Chip Type	V_{R}	I F	Die Size	Package
SIDC05D60C8	600V	15A	1.9 x 2.37 mm ²	sawn on foil

Mechanical Parameters

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Raster size	1.9 x 2.37			
Area total	4.5	mm ²		
Anode pad size	1.47 x 1.94			
Thickness	70	μm		
Wafer size	200	mm		
Max. possible chips per wafer	6224			
Passivation frontside	Photoimide			
Pad metal	3200 nm AlSiCu			
Backside metal	Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	Electrically conductive glue or solder			
Wire bond	Al, ≤500μm			
Reject ink dot size	Ø 0.65mm; max 1.2mm			
Recommended storage environment	Store in original container, in dry nitrogen, in dark environment, < 6 month at an ambient temperature of 23°C			



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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit	
Repetitive peak reverse voltage	V_{RRM}	<i>T</i> _{vj} = 25 ℃	600	V	
Continuous forward current	I _F	<i>T</i> _{vj} < 150℃	1)	- A	
Maximum repetitive forward current	I _{FRM}	<i>T</i> _{vj} < 150℃	30		
Junction temperature range	T _{vj}		-40+175	°C	
Operating junction temperature	T _{vj}		-40+150	°C	
Dynamic ruggedness ²⁾	P_{max}	$I_{\text{Fmax}} = 30\text{A}, \ V_{\text{Rmax}} = 600\text{V}, \ T_{\text{vj}} \le 150\text{°C}$	tbd	kW	

¹⁾ depending on thermal properties of assembly

Static Characteristics (tested on wafer), $T_{vj} = 25 \text{ }^{\circ}\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	Oiiit
Reverse leakage current	I_{R}	V _R =600V			27	μA
Cathode-Anode breakdown Voltage	$V_{\rm BR}$	I _R =0.25mA	600			V
Diode forward voltage	V_{F}	/ _F =15A	1.25	1.6	1.95	V

Further Electrical Characteristics

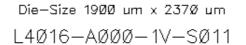
Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

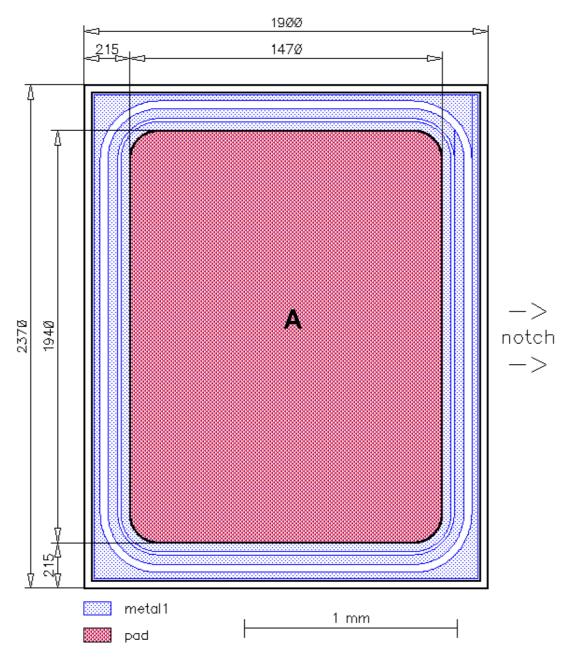
²⁾ not subject to production test - verified by design/characterisation





Chip Drawing





A: Anode pad



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Description
AQL 0,65 for visual inspection according to failure catalogue
Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Version	Subjects (major changes since last revision)	Date

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