

Vishay Siliconix

P-Channel 20-V (D-S) MOSFET with Schottky Diode

PRODUCT SUMMARY						
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)			
- 20	0.070 at V _{GS} = - 5.0 V	- 5.0	4.5 nC			
	0.105 at V _{GS} = -2.5 V	- 4.2	4.0110			

TSOP-6 Top View

2

3 mm S

G

V _{KA} (V)	V _f (V) Diode Forward Voltage	I _F (A) ^a
20	0.45 at 1 A	2

6

5

4

2.85 mm -

D/K

D/K

D/K

FEATURES

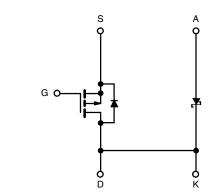
- Halogen-free According to IEC 61249-2-21
 Definition
- LITTLE FOOT[®] *Plus* Schottky Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

HDD

Lot Traceability and Date Code

- DC-DC Converter
- Asynchronous Rectification



Ordering Information: Si3879DV-T1-E3 (Lead (Pb)-free) Si3879DV-T1-GE3 (Lead (Pb)-free and Halogen-free)

P-Channel MOSFET

Parameter		Symbol	Limit	Unit
Drain-Source Voltage (MOSFET)	V _{DS}	- 20		
Reverse Voltage (Schottky)		V _{KA}	20	V
Gate-Source Voltage (MOSFET)		V _{GS}	± 12	
	T _C = 25 °C		- 5.0	
Continuous Drain Current ($T_J = 150 ^{\circ}$ C) (MOSFET)	T _C = 70 °C		- 4.0	
	T _A = 25 °C	I _D	- 4.0 ^{b, c}	
	T _A = 70 °C		- 3.0 ^{b, c}	
Pulsed Drain Current (MOSFET)		I _{DM}	- 20	A
Continuous Source-Drain Diode Current	T _C = 25 °C		- 2.7	_
(MOSFET Diode Conduction)	T _A = 25 °C	I _S	- 1.6 ^{b, c}	
Average Forward Current (Schottky)		I _F	2 ^b	
Pulsed Forward Current (Schottky)		I _{FM}	5	
	T _C = 25 °C		3.3	
Maximum Dawar Dissinction (MOCEET)	T _C = 70 °C	1	2.1	_
Maximum Power Dissipation (MOSFET)	T _A = 25 °C	1	2.0 ^{b, c}	
	T _A = 70 °C	P _D	1.2 ^{b, c}	14/
	T _C = 25 °C		1.9	W
Maximum Dawar Dissinction (Schottlar)	T _C = 70 °C	1 –	1.2	
Maximum Power Dissipation (Schottky)	T _A = 25 °C	1 –	1.3 ^{b, c}	
	T _A = 70 °C	1	0.9 ^{b, c}	
Operating Junction and Storage Temperature Range	-	T _J , T _{stg}	- 55 to 150	°C

Marking Code

Part # Code



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THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient (MOSFET) ^{b, d}	t ≤ 5 s	R _{thJA}	51	62.5	
Maximum Junction-to-Foot (Drain) (MOSFET)	Steady State	R _{thJF}	30	37	°C/W
Maximum Junction-to-Ambient (Schottky) ^{b, e}	t ≤ 5 s	R _{thJA}	73	90	0/11
Maximum Junction-to-Foot (Drain) (Schottky)	Steady State	R _{thJF}	50	65	

Notes: a. Based on $T_C = 25$ °C. b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under Steady State conditions is 105 °C/W.
e. Maximum under Steady State conditions is 125 °C/W.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static		1					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		- 20			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μΑ		3		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.6		- 1.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -20 V, V_{GS} = 0 V$			- 1		
	IDSS	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10	- μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5$ V, V_{GS} = - 4.5 V	- 8			A	
	Brach	V _{GS} = - 4.5 V, I _D = - 3.5 A		0.058	0.070	Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 3.0 A		0.085	0.105	- 12	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 3.5 A		10		S	
Dynamic ^b							
Input Capacitance	C _{iss}			480			
Output Capacitance	C _{oss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		132		pF	
Reverse Transfer Capacitance	C _{rss}			55			
Total Gate Charge	Qg	V_{DS} = - 10 V, V_{GS} = - 10 V, I_{D} = - 5.0 A		9.7	14.5	nC	
Iotal Gate Charge				4.5	7		
Gate-Source Charge	Q _{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4.5 \text{ A}$		1.0			
Gate-Drain Charge	Q _{gd}			1.0			
Gate Resistance	R _g	f = 1 MHz		7.5		Ω	
Turn-On Delay Time	t _{d(on)}			6	10		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 2.0 Ω		54	85	- ns	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 5.0 A, V_{GEN} = - 10 V, R_g = 1 Ω		19	30		
Fall Time	t _f]		8	15		
Turn-On Delay Time	t _{d(on)}			26	40	115	
Rise Time	t _r	V_{DD} = - 10 V, R_L = 2.0 Ω		80	120		
Turn-Off DelayTime	t _{d(off)}	$\text{I}_\text{D}\cong$ - 5.0 A, V_GEN = - 4.5 V, R_g = 1 Ω		20	30		
Fall Time	t _f			10	15		



SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted									
Parameter	Symbol	Test Conditions Mi		Тур.	Max.	Unit			
Drain-Source Body Diode Characteristics									
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 2.7	А			
Pulse Diode Forward Current	I _{SM}				- 20	A			
Body Diode Voltage	V _{SD}	I _S = - 1.0 A, V _{GS} = 0 V		- 0.75	- 1.2	V			
Body Diode Reverse Recovery Time	t _{rr}			25	40	ns			
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 3.5 A, dl/dt = 100 A/μs, T _{.1} = 25 °C		12	20	nC			
Reverse Recovery Fall Time	t _a	$F = -3.5 \text{ A}, \text{ u/ut} = -100 \text{ A/} \mu \text{s}, \text{ 1} \text{ J} = 25 \text{ C}$		9					
Reverse Recovery Rise Time	t _b			16		ns			

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

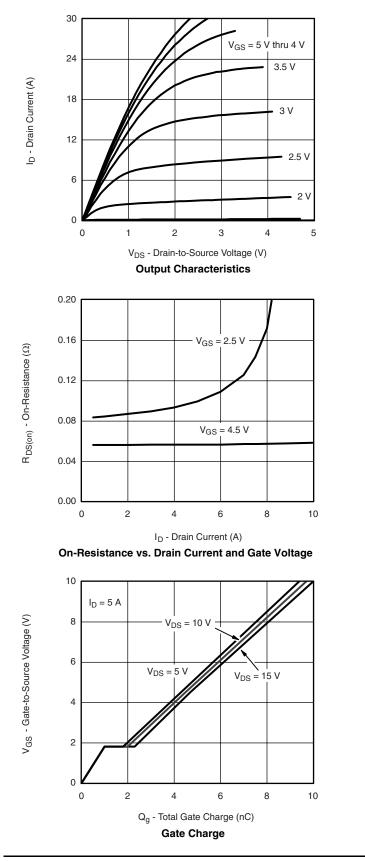
SCHOTTKY SPECIFICATIONS								
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Forward Voltage Drop	V _F	I _F = 1 A		0.41	0.45	v		
	۷F	I _F = 1 Α, Τ _J = 125 °C		0.36	0.41			
		V _R = 5 V		0.015	0.08	mA		
		V _R = 5 V, T _J = 85 °C		0.50	5.00			
Maximum Reverse Leakage Current	I _{rm}	V _R = 20 V		0.02	0.10			
		V _R = 20 V, T _J = 85 °C	V, T _J = 85 °C 0.7	7.00	1			
		V _R = 20 V, T _J = 125 °C		5	50			
Junction Capacitance	C _T	V _R = 10 V		60		pF		

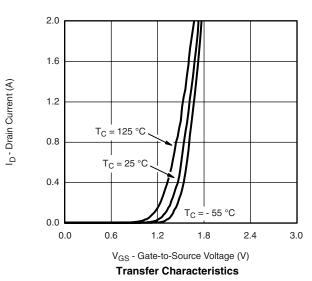
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

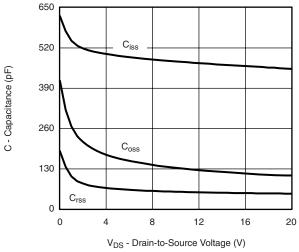


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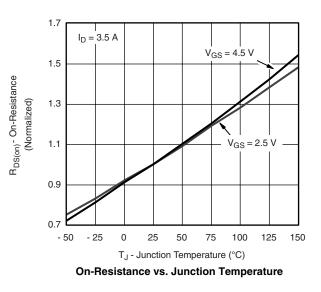
MOSFET TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted





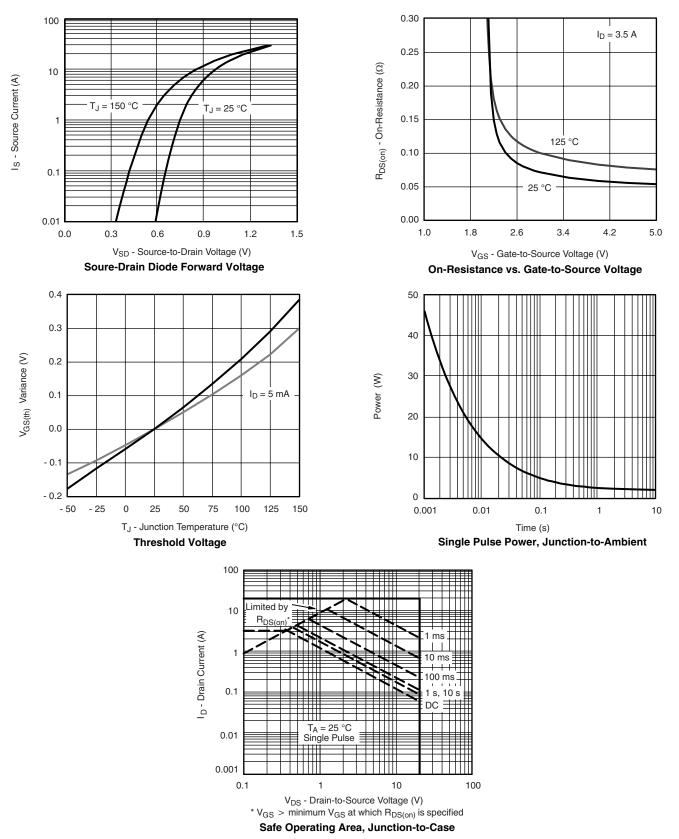








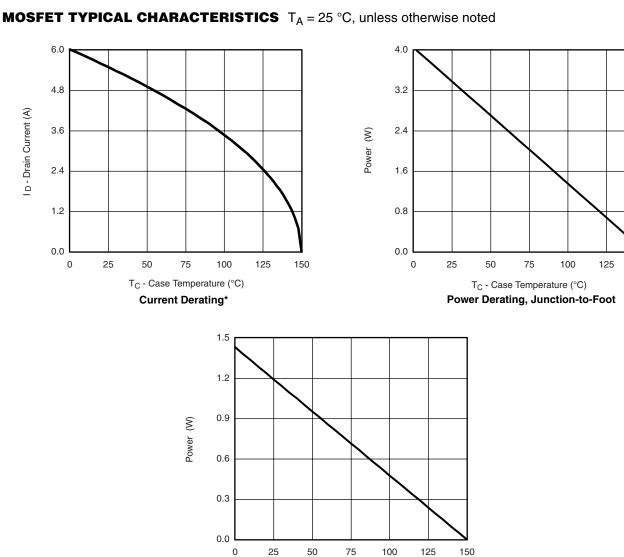
MOSFET TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted





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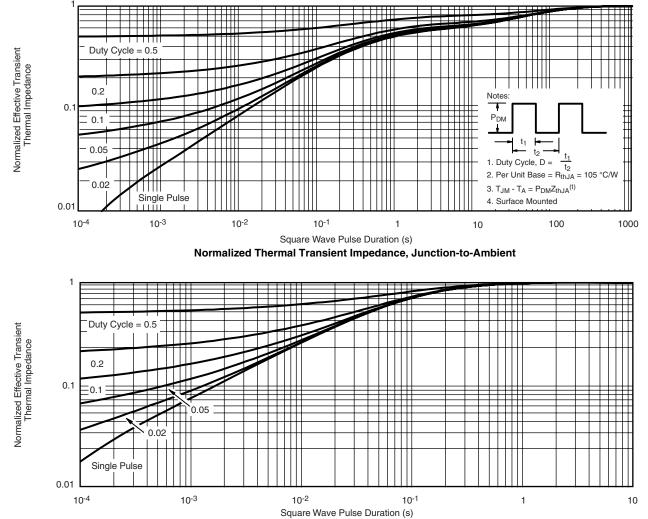


T_A - Ambient Temperature (°C) Power Derating, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



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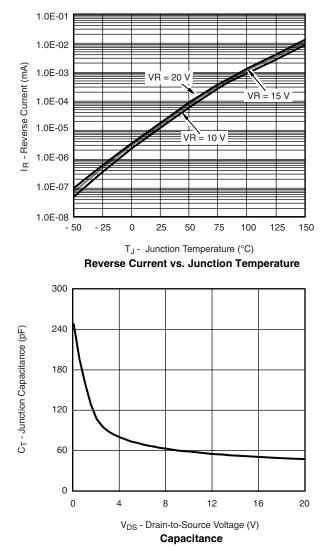
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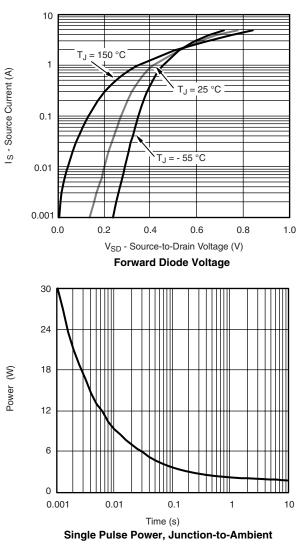
Normalized Thermal Transient Impedance, Junction-to-Foot



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SCHOTTKY TYPICAL CHARACTERISTICS $T_A = 25 \ ^\circ C$, unless otherwise noted





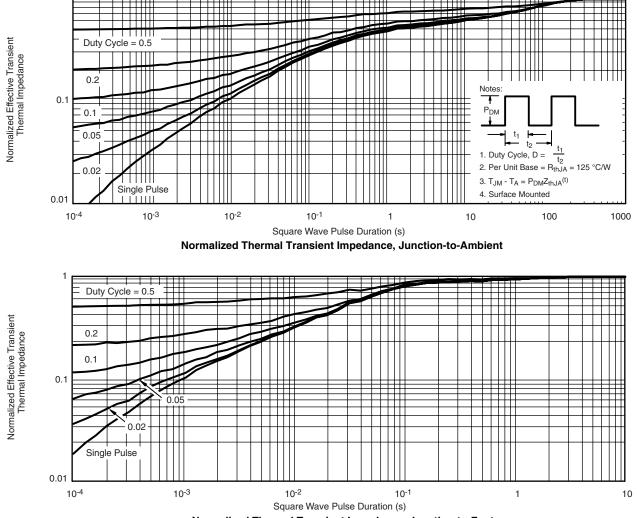


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Si3879DV

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Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?74958.



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