
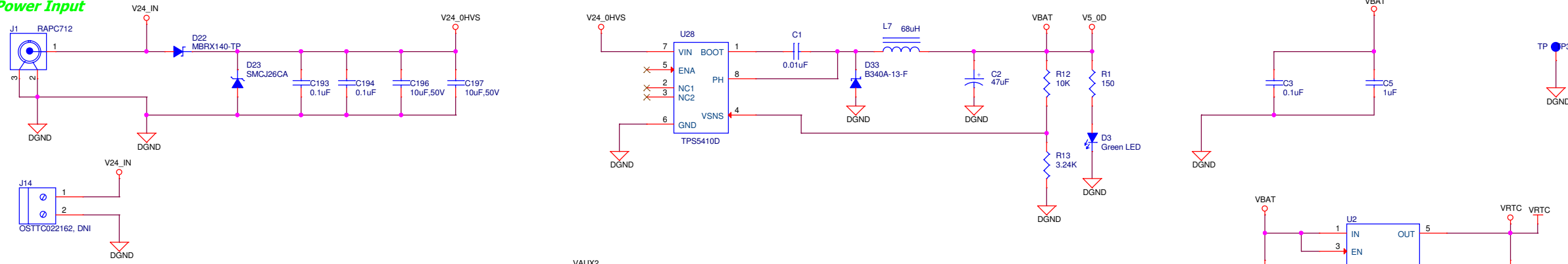


REV	Description	DATE	BY
A	Alpha release	10/28/11	CCO
A2	Beta release	12/20/11	CCO

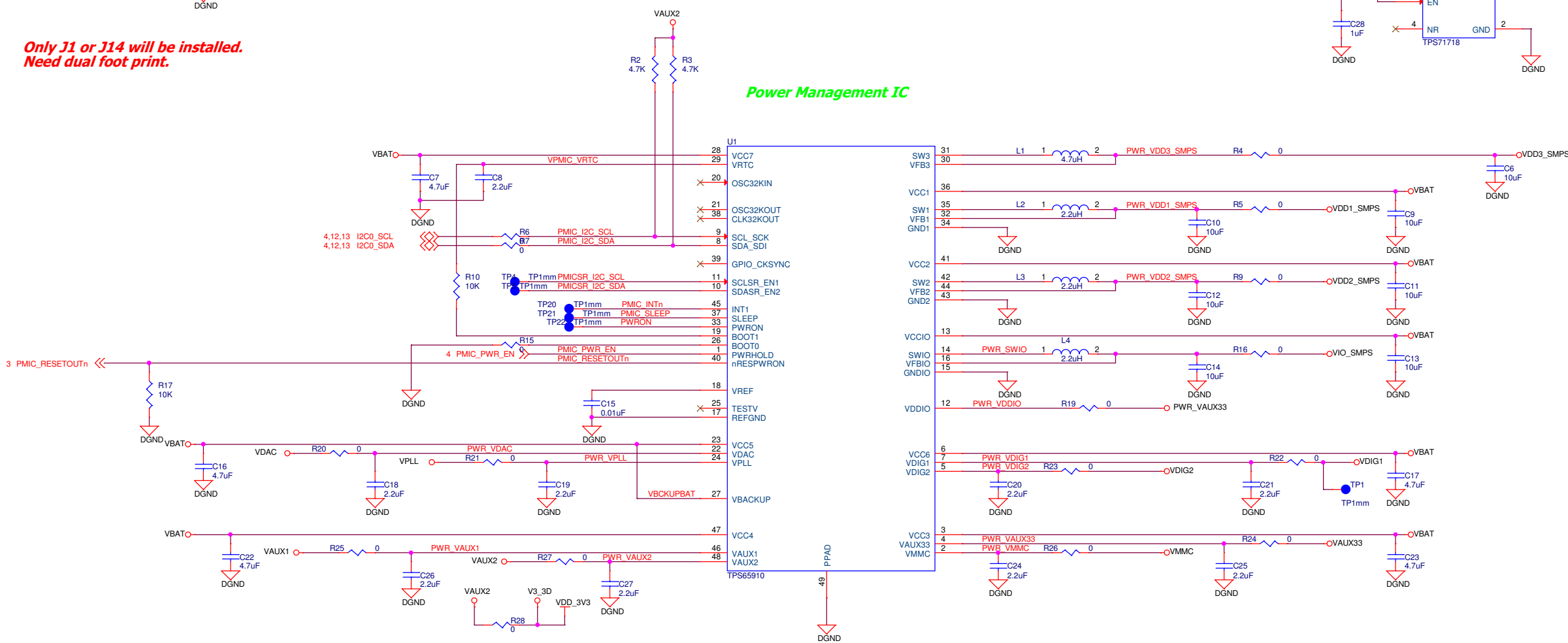
<b>ARM MPU</b>	<b>Texas Instruments, Inc.</b>			
	ARM MPU Business Unit 12500 TI Blvd Dallas, TX 75243			
	AM335X 15X15 ICE BOARD			
	Title			
Size	Document Number	Modified By:	Rev	
B	AM335X_15X15_ICE.dsn	CCO	A2	
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**Main Power Input**

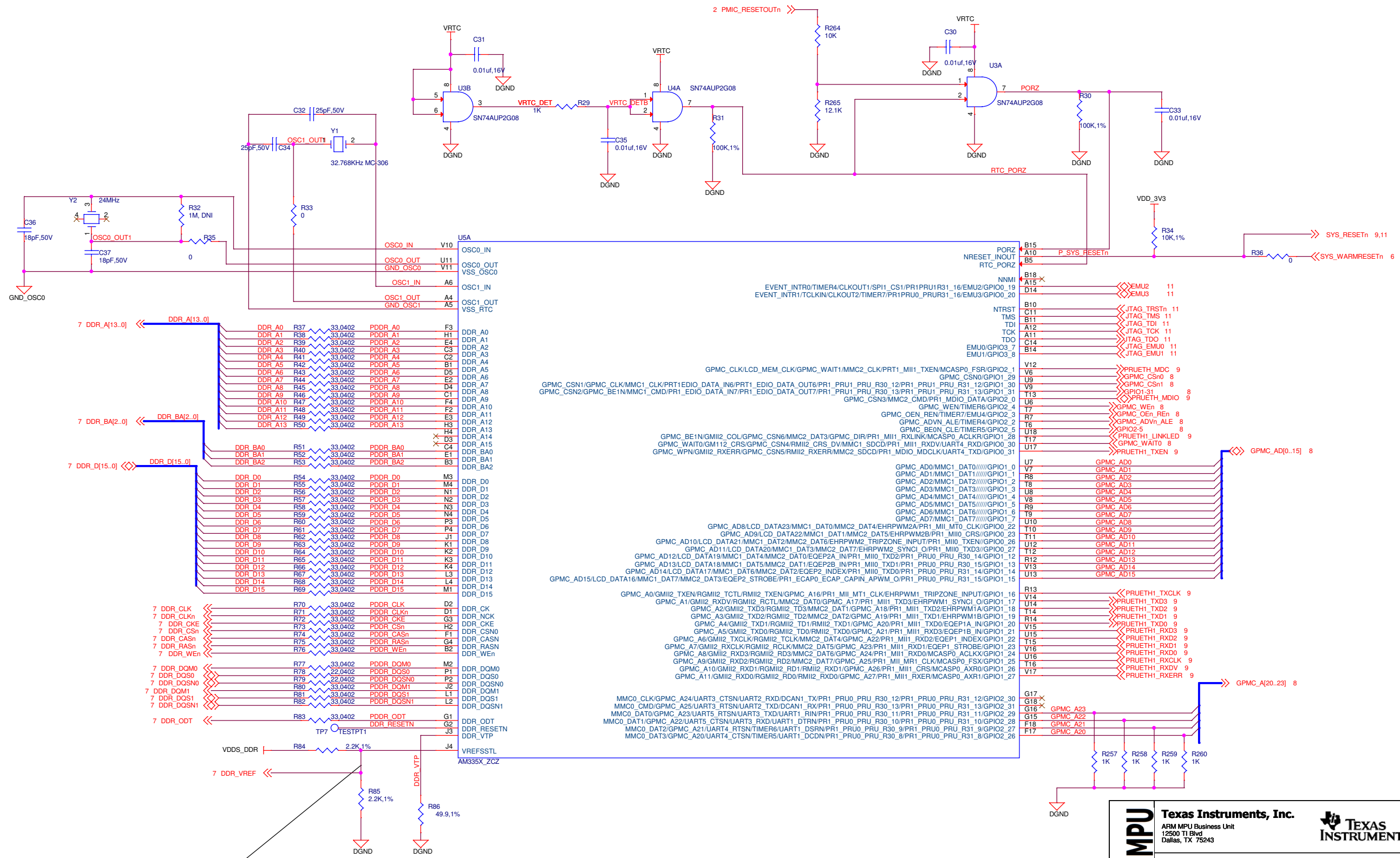


**Only J1 or J14 will be installed. Need dual foot print.**

**Power Management IC**



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- 1) LOCATE THESE RESISTORS AS CLOSE TO EACH OTHER AS POSSIBLE.
- 2) KEEP CONNECTIONS ON THE RESISTORS TO GND AND VDD5\_DDR AS SHORT AS POSSIBLE.
- 3) PLACE THE RESISTOR PAIR EQUAL DISTANCE BETWEEN PROCESSOR AND THE TWO DDR MEMORY DEVICES.
- 4) ISOLATE THIS TRACE FROM OTHER SIGNALS.

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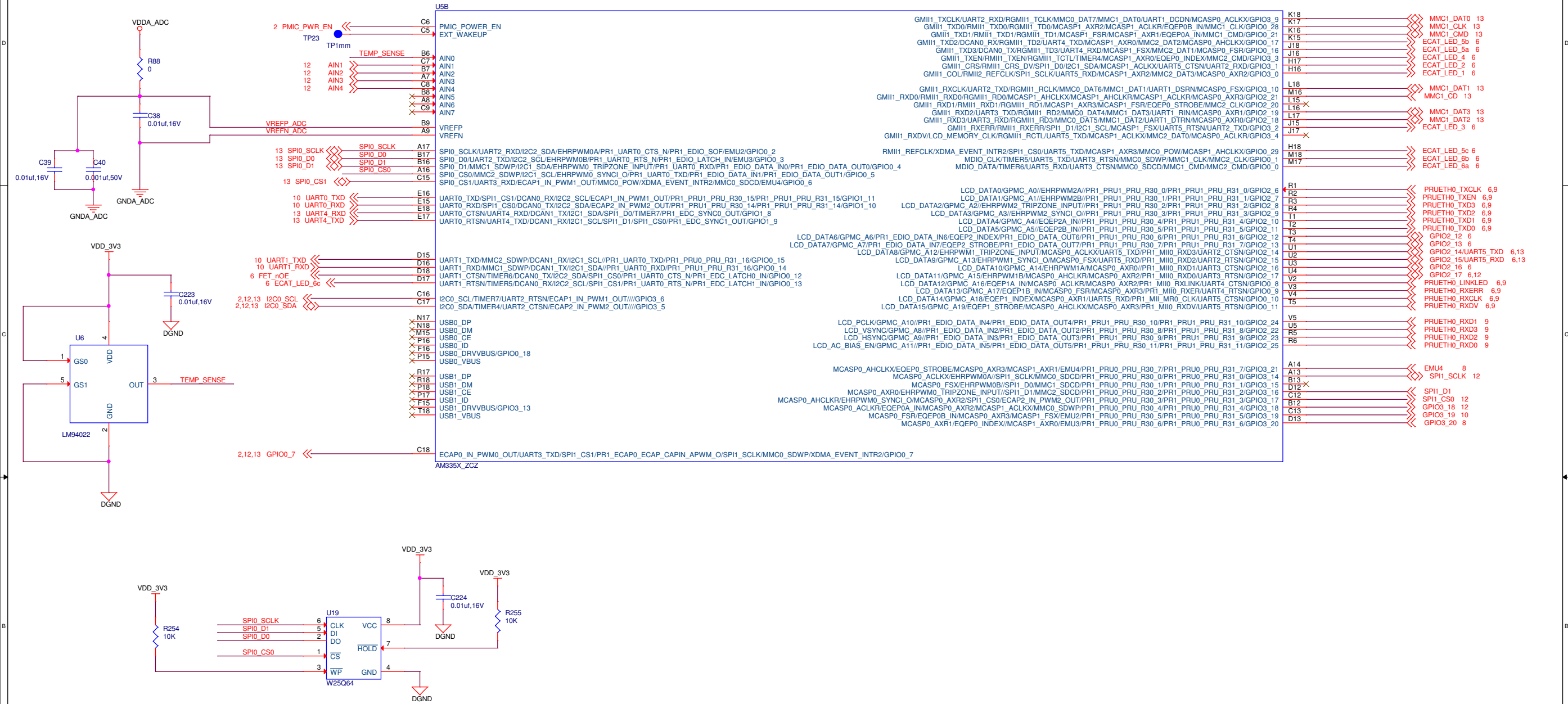
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 ARM MPU Business Unit  
 12500 TI Blvd  
 Dallas, TX 75243

**AM335X 15X15 ICE BOARD**  
 AM335X 1 OF 3

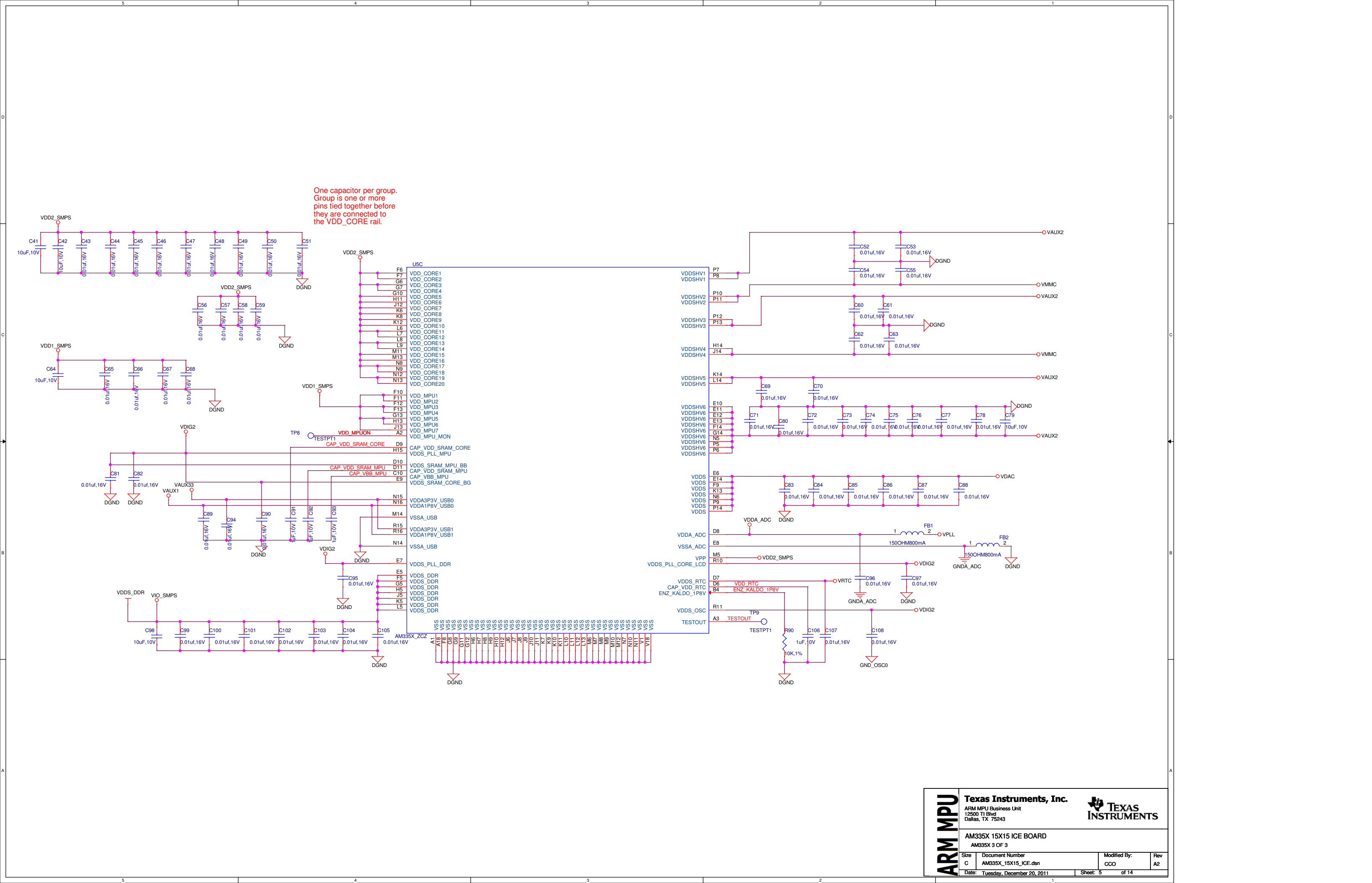
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- 1) AIN signals need to be routed over a plane connected to GND\_ADC.
- 2) Signal should be as short as possible.
- 3) All signals need to be isolated from other signals on the board.



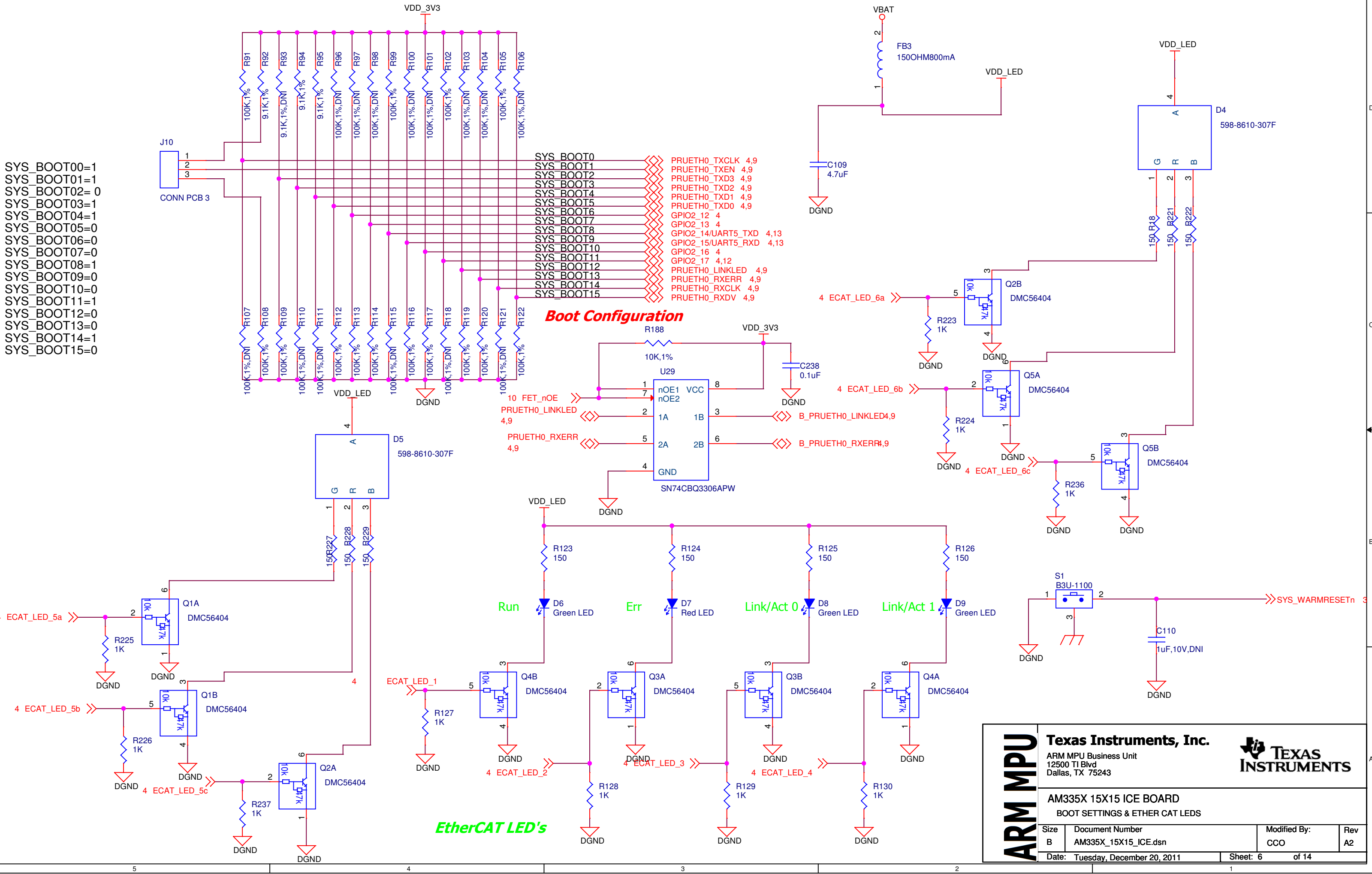
<b>ARM MPU</b>	<b>Texas Instruments, Inc.</b>			<b>TEXAS INSTRUMENTS</b>
	ARM MPU Business Unit 12500 TI Blvd Dallas, TX 75243			
	AM335X 15X15 ICE BOARD AM335X 2 OF 3			
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One capacitor per group.  
Group is one or more  
pins tied together before  
they are connected to  
the VDD\_CORE rail.

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	<b>TEXAS INSTRUMENTS</b>		
	<b>AM335X 15X15 ICE BOARD</b> AM335X 3 OF 3		
	Size: C	Document Number: AM335X_15X15_ICE.dsn	Modified By: CCO
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SYS\_BOOT0=1  
 SYS\_BOOT01=1  
 SYS\_BOOT02=0  
 SYS\_BOOT03=1  
 SYS\_BOOT04=1  
 SYS\_BOOT05=0  
 SYS\_BOOT06=0  
 SYS\_BOOT07=0  
 SYS\_BOOT08=1  
 SYS\_BOOT09=0  
 SYS\_BOOT10=0  
 SYS\_BOOT11=1  
 SYS\_BOOT12=0  
 SYS\_BOOT13=0  
 SYS\_BOOT14=1  
 SYS\_BOOT15=0

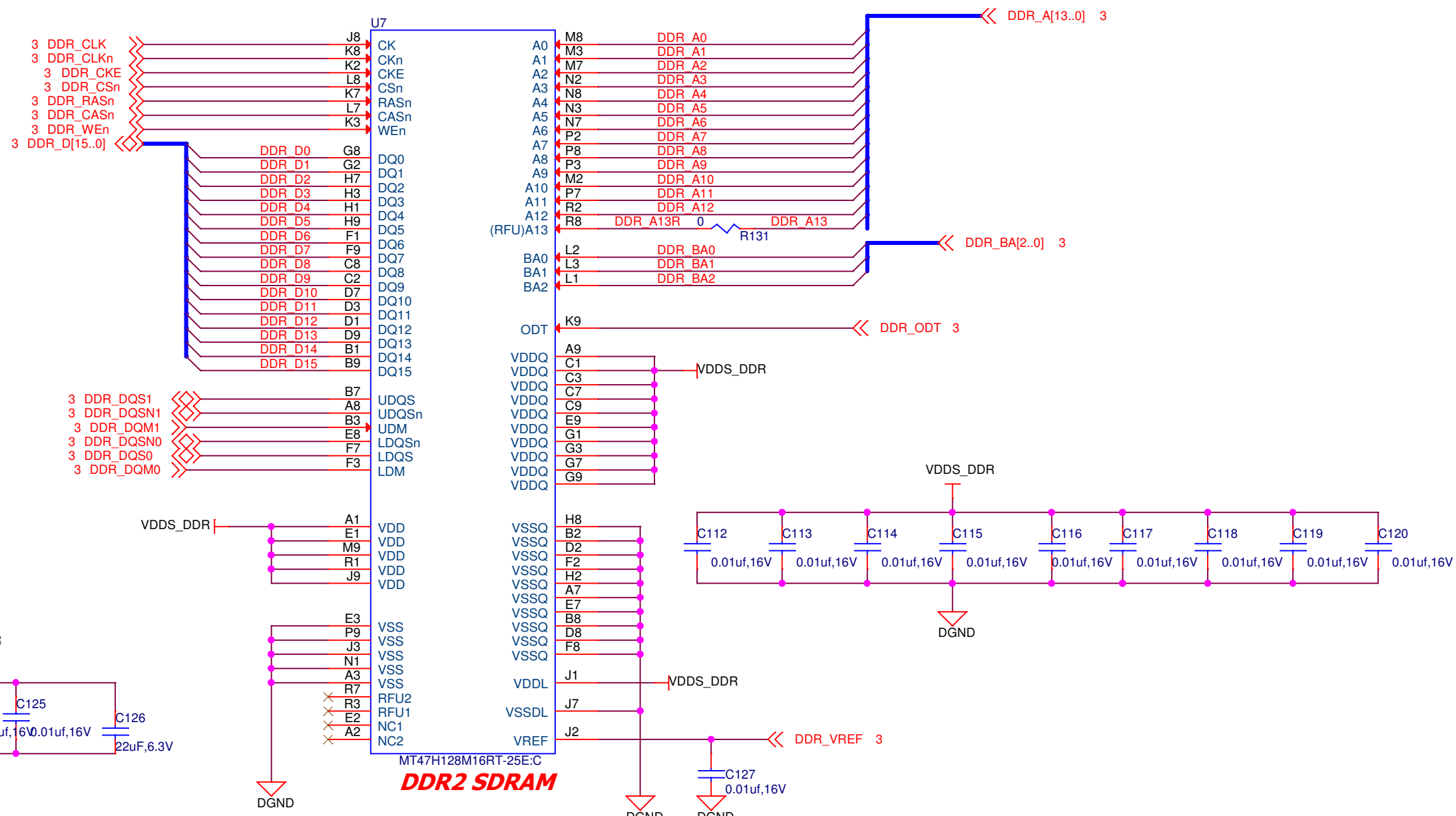


**Boot Configuration**

- PRUETH0\_TXCLK 4,9
- PRUETH0\_TXEN 4,9
- PRUETH0\_TXD3 4,9
- PRUETH0\_TXD2 4,9
- PRUETH0\_TXD1 4,9
- PRUETH0\_TXD0 4,9
- GPIO2\_12 4
- GPIO2\_13 4
- GPIO2\_14/UART5\_TXD 4,13
- GPIO2\_15/UART5\_RXD 4,13
- GPIO2\_16 4
- GPIO2\_17 4,12
- PRUETH0\_LINKLED 4,9
- PRUETH0\_RXERR 4,9
- PRUETH0\_RXCLK 4,9
- PRUETH0\_RXDV 4,9

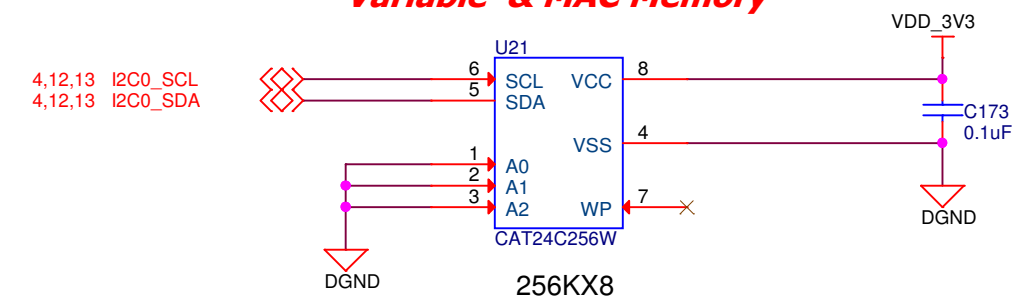
**EtherCAT LED's**

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	<b>AM335X 15X15 ICE BOARD</b>			
BOOT SETTINGS & ETHER CAT LEDS				
Size B	Document Number AM335X_15X15_ICE.dsn	Modified By: CCO	Rev A2	
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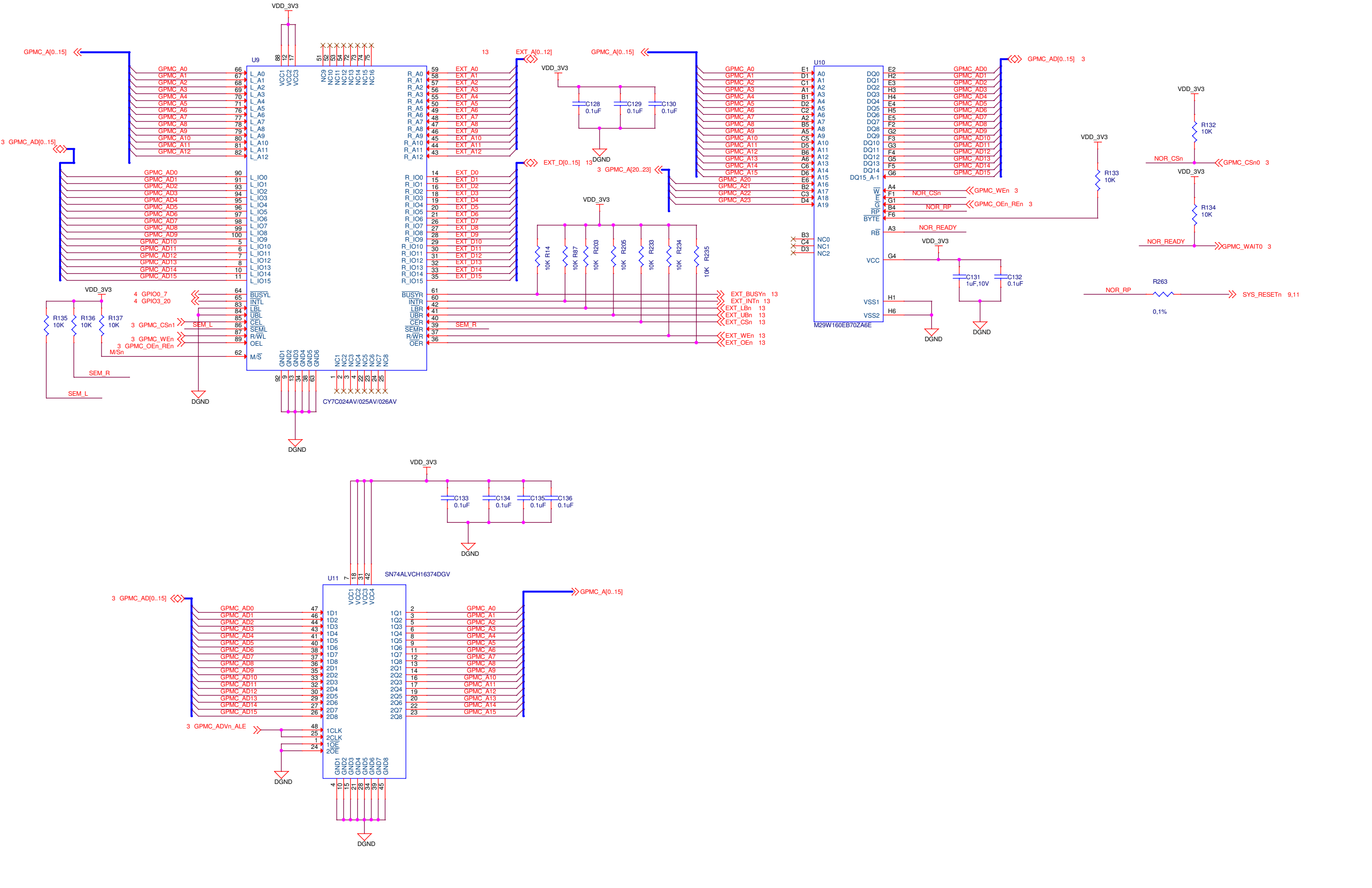
**MT47H128M16RT-25E:C  
DDR2 SDRAM**

**Variable & MAC Memory**

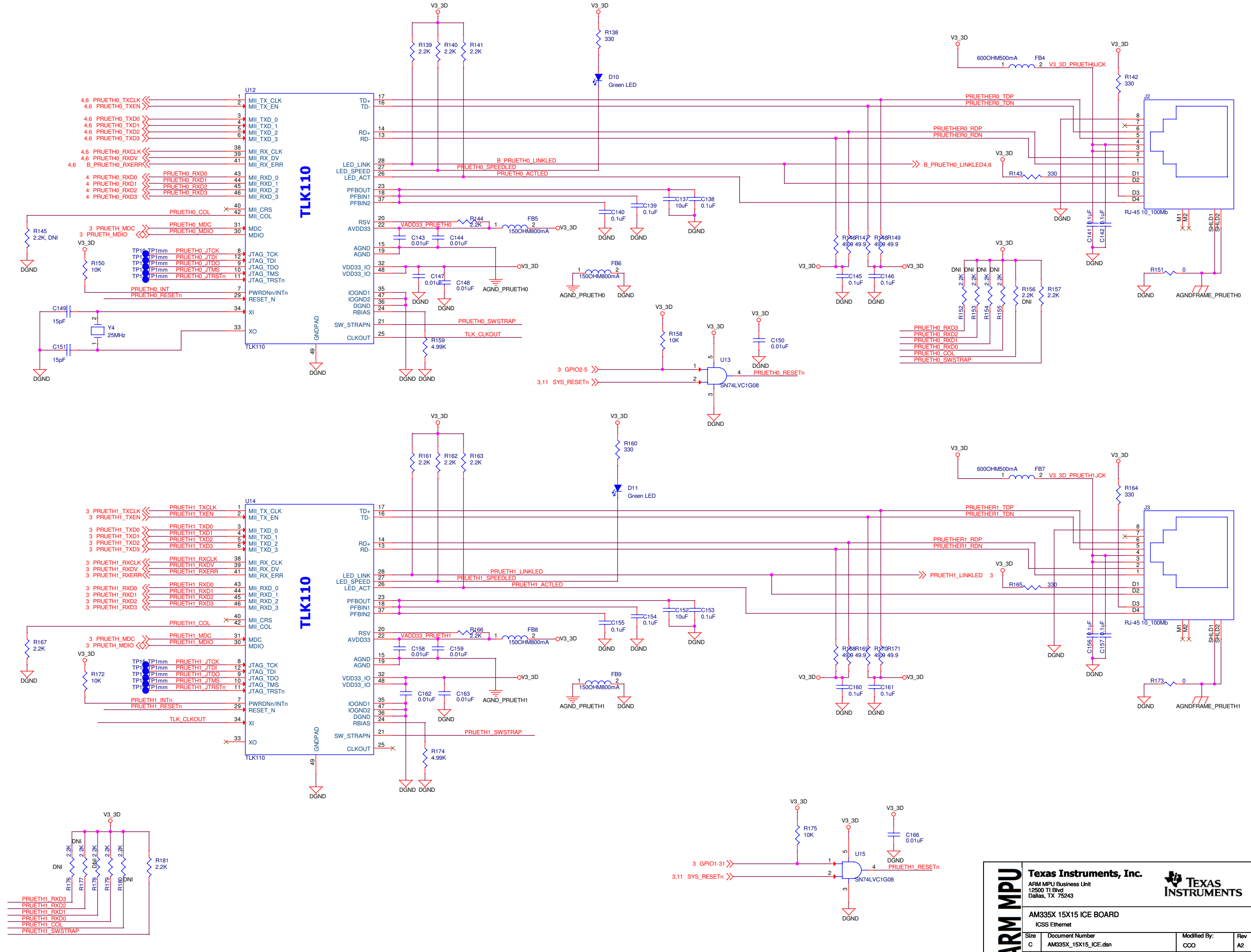


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	<b>AM335X 15X15 ICE BOARD</b>			
	DDR2 INTERFACE			
Size B	Document Number AM335X_15X15_ICE.dsn	Modified By: CCO	Rev A2	
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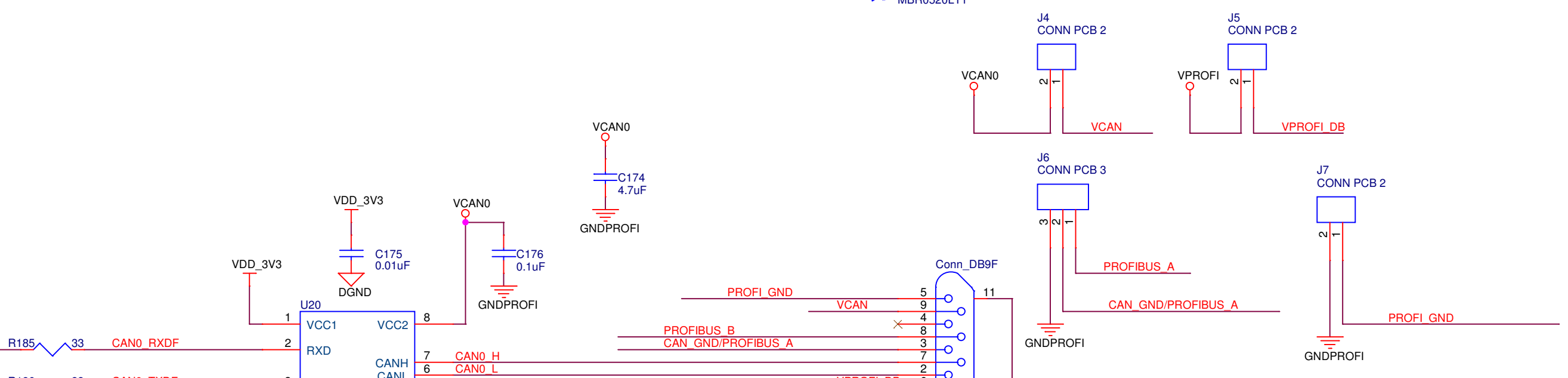
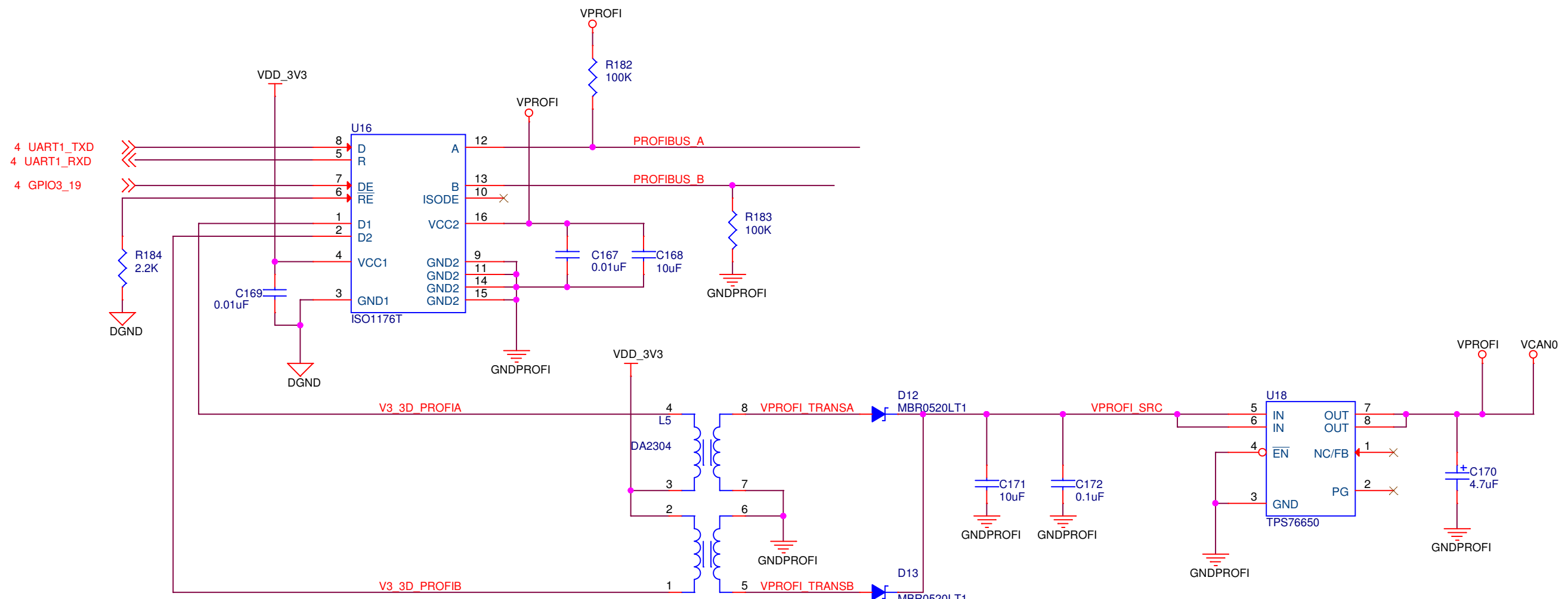








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	<b>AM335X 15X15 ICE BOARD</b>		
	ICSS Ethernet		
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**TEXAS INSTRUMENTS**

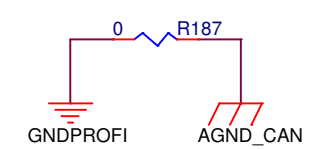
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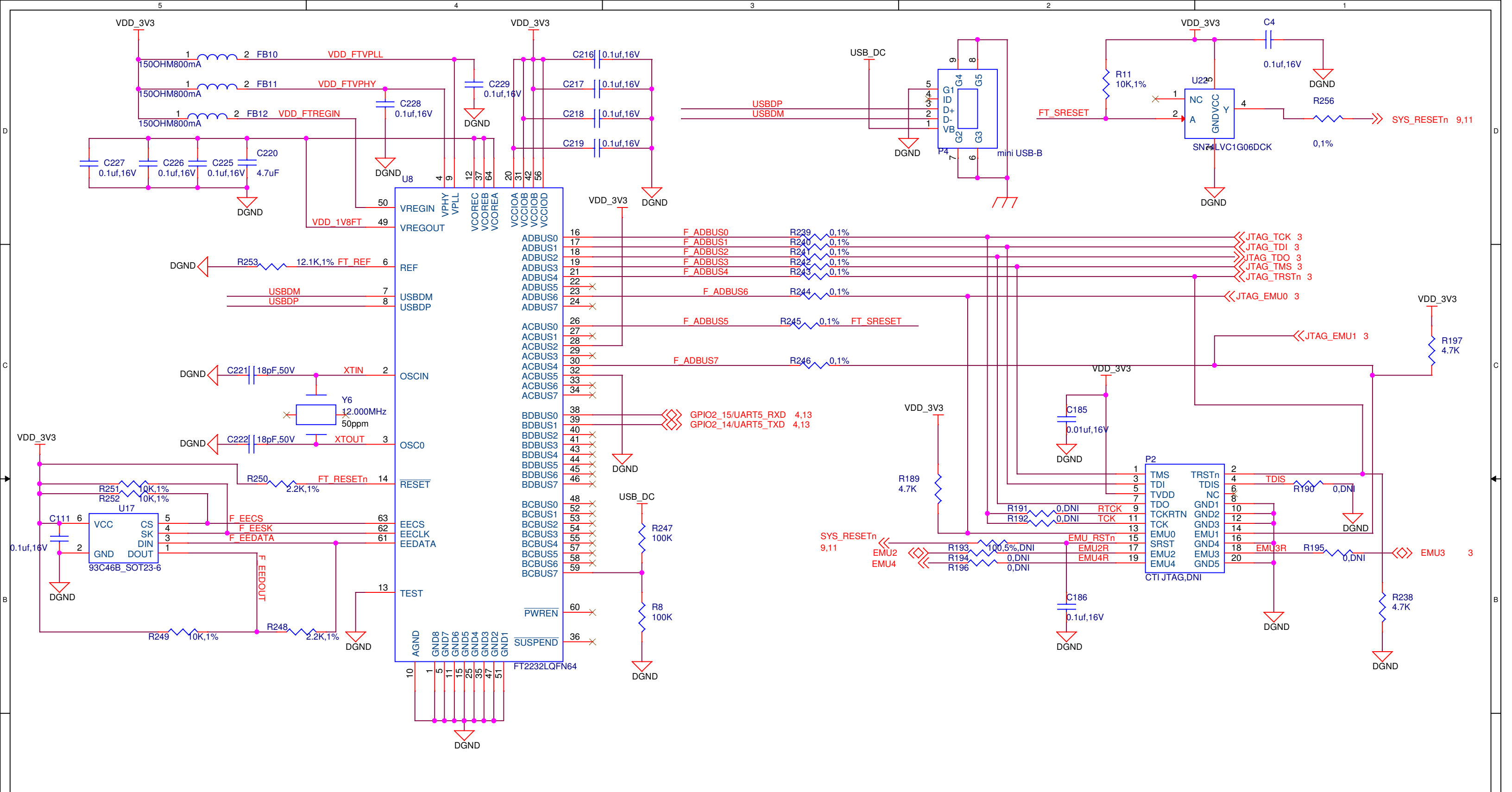
**AM335X 15X15 ICE BOARD**  
 CAN & PROFFI BUS

Size	Document Number	Modified By:	Rev
B	AM335X_15X15_ICE.dsn	CCO	A2

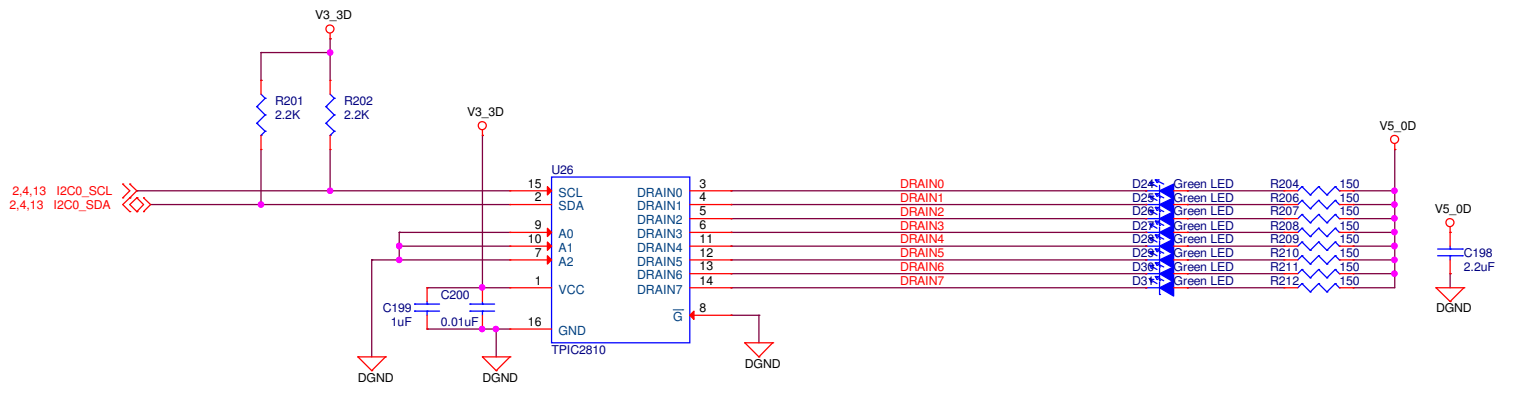
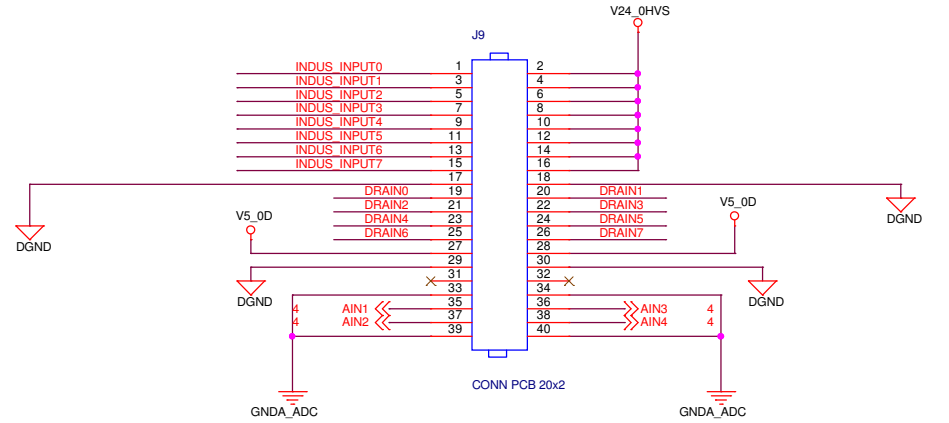
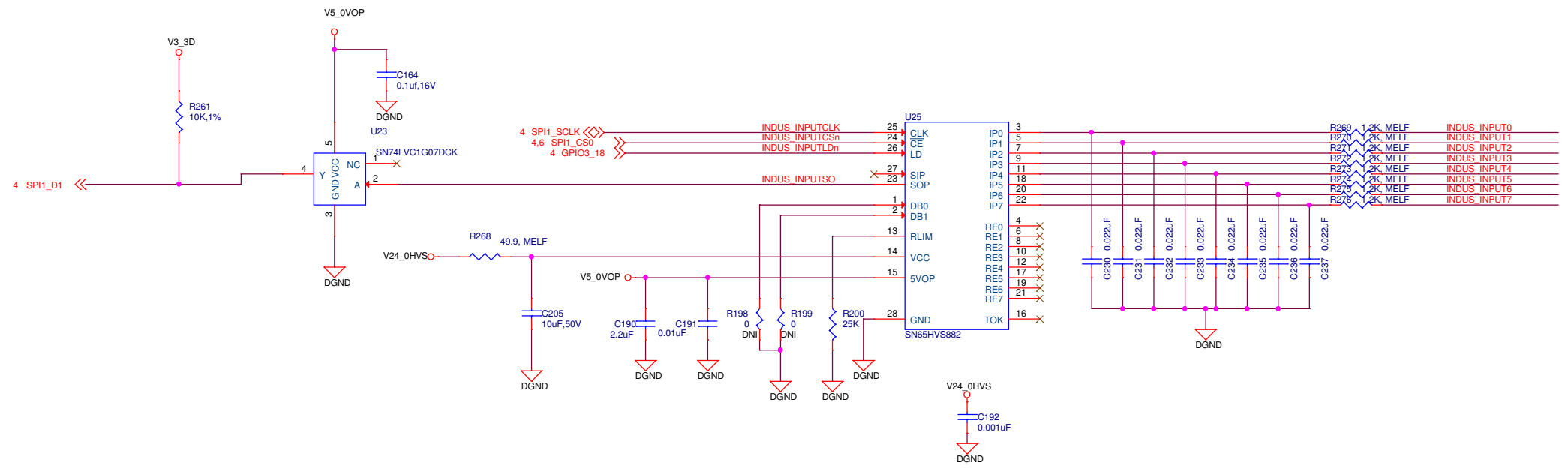
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	<b>AM335X 15X15 ICE BOARD</b> JTAG & DEBUG UART			
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	B	AM335X_15X15_ICE.dsn	CCO	A2
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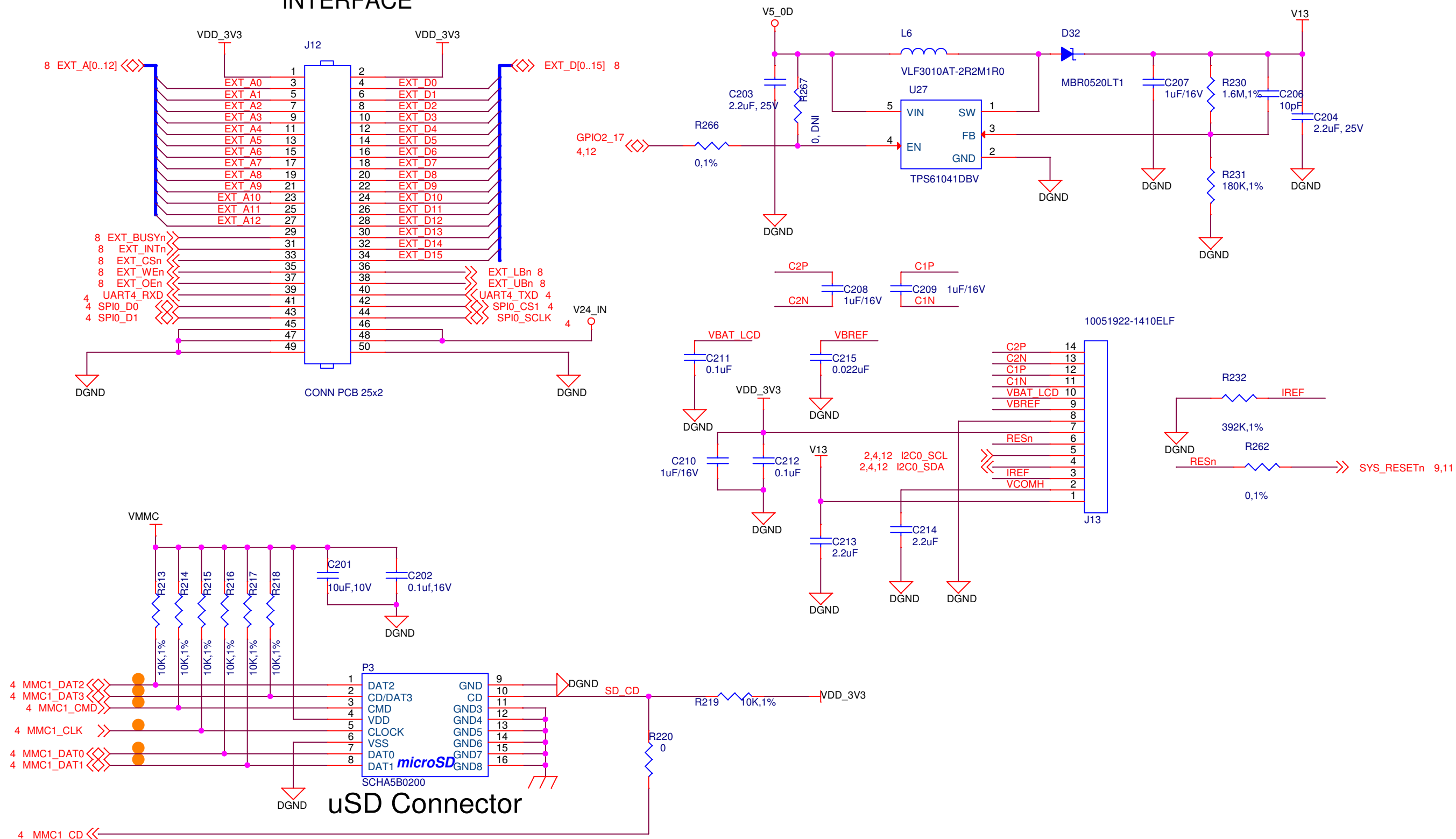
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**AM335X 15X15 ICE BOARD**  
Industrial Inputs/Outputs

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# HOST INTERFACE



● Signals below the orange dot are to be matched to each other @ +/- 10mils. Run over ground and keep isolated from other signals as much as possible.

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	EXPANSION			
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