

To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Phase-out/Discontinued

PNP SILICON EPITAXIAL TRANSISTOR
FOR HIGH-SPEED SWITCHING

The 2SA1395 is a mold power transistor developed for high-speed switching, and is ideal for use as a driver in devices such as switching regulators, DC/DC converters, and high-frequency power amplifiers.

FEATURES

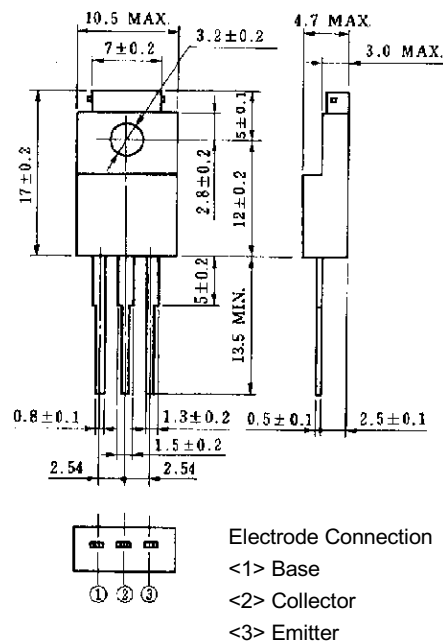
- Mold package that does not require an insulating board or insulation bushing
- Low collector saturation voltage: $V_{CE(sat)} = -0.6 \text{ V MAX. (@-1 A)}$
- Fast switching speed: $t_r = 0.5 \mu\text{s MAX. (@-1 A)}$
- Complementary transistor: 2SC3567

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	-100	V
Collector to emitter voltage	V_{CEO}	-100	V
Emitter to base voltage	V_{EBO}	-7.0	V
Collector current (DC)	$I_{C(DC)}$	-2.0	A
Collector current (pulse)	$I_{C(pulse)^*}$	-4.0	A
Base current (DC)	$I_{B(DC)}$	-1.0	A
Total power dissipation	$P_T (T_c = 25^\circ\text{C})$	15	W
Total power dissipation	$P_T (T_a = 25^\circ\text{C})$	2.0	W
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

* $PW \leq 300 \mu\text{s}$, duty cycle $\leq 10\%$

PACKAGE DRAWING (UNIT: mm)



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ELECTRICAL CHARACTERISTICS (Ta = 25°C)

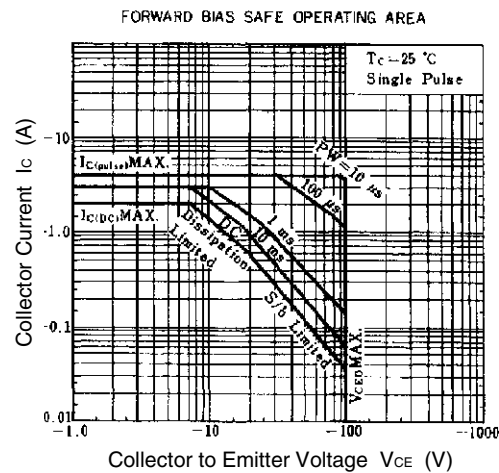
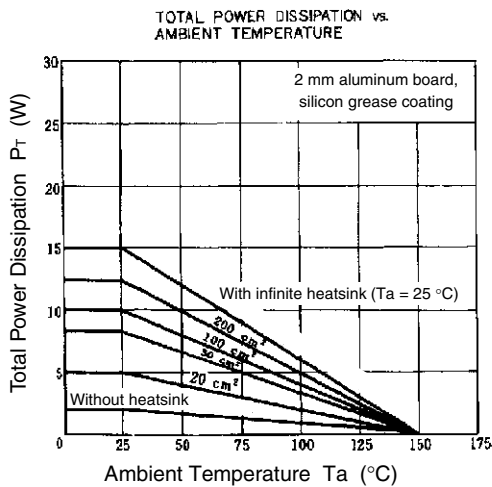
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	V _{CE0(SUS)}	I _c = -1.0 A, I _{B1} = -0.1 A, L = 1 mH	-100			V
Collector to emitter voltage	V _{CEX(SUS)1}	I _c = -1.0 A, I _{B1} = -I _{B2} = -0.1 A, V _{BE(OFF)} = 5.0 V, L = 180 μH, clamped	-100			V
Collector to emitter voltage	V _{CEX(SUS)2}	I _c = -2.0 A, I _{B1} = -0.2 A, I _{B2} = 0.1 A, V _{BE(OFF)} = 5.0 V, L = 180 μH, clamped	-100			V
Collector cutoff current	I _{CBO}	V _{CB} = -100 V, I _E = 0			-10	μA
Collector cutoff current	I _{CER}	V _{CE} = -100 V, R _{BE} = 51 Ω, Ta = 125°C			-1.0	mA
Collector cutoff current	I _{CX1}	V _{CE} = -100 V, V _{BE(OFF)} = 5.0 V			-10	μA
Collector cutoff current	I _{CX2}	V _{CE} = -100 V, V _{BE(OFF)} = 5.0 V, Ta = 125°C			-1.0	mA
Emitter cutoff current	I _{EBO}	V _{EB} = -5.0 V, I _c = 0			-10	μA
DC current gain	h _{FE1} *	V _{CE} = -5.0 V, I _c = -0.1 A	40			
DC current gain	h _{FE2} *	V _{CE} = -5.0 V, I _c = -1.0 A	40		200	
Collector saturation voltage	V _{CE(sat)} *	I _c = -1.0 A, I _B = -0.1 A			-0.6	V
Base saturation voltage	V _{BE(sat)} *	I _c = -1.0 A, I _B = -0.1 A			-1.5	V
Turn-on time	t _{on}	I _c = -1.0 A, R _L = 50 Ω, I _{B1} = -I _{B2} = -0.1 A, V _{CC} ≅ -50 V Refer to the test circuit.			0.5	μs
Storage time	t _{stg}				1.5	μs
Fall time	t _f				0.5	μs

* Pulse test PW ≤ 350 μs, duty cycle ≤ 2%

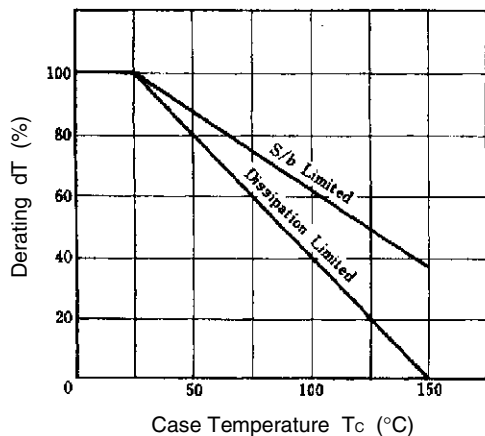
h_{FE} CLASSIFICATION

Marking	M	L	K
h _{FE2}	40 to 80	60 to 120	100 to 200

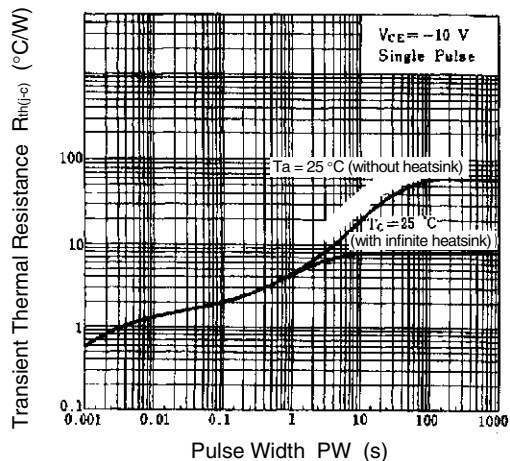
TYPICAL CHARACTERISTICS (Ta = 25 °C)



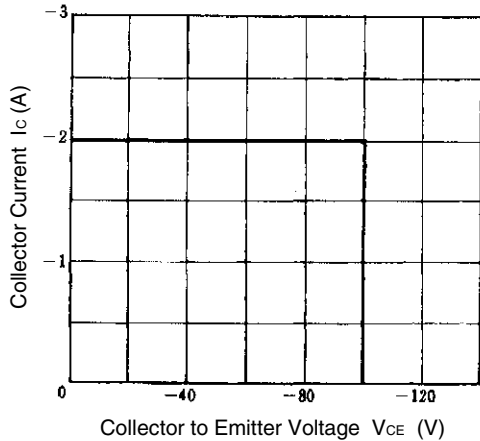
DERATING CURVE OF SAFE OPERATING AREA



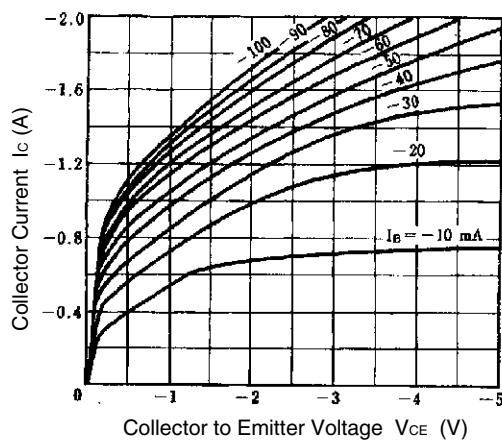
TRANSIENT THERMAL RESISTANCE



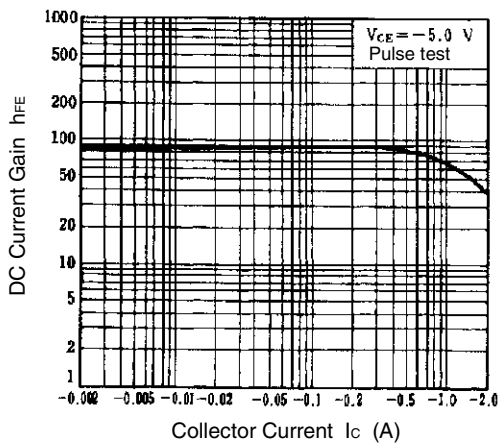
REVERSE BIAS SAFE OPERATING AREA



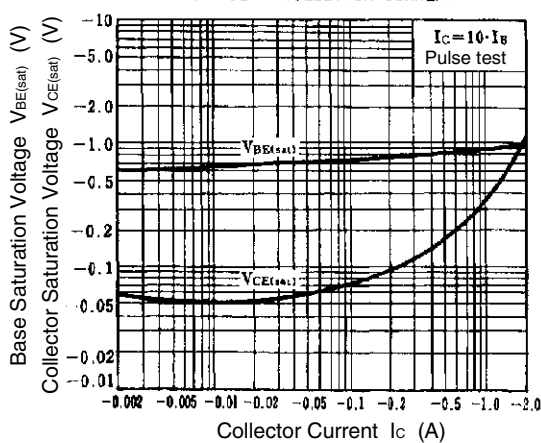
COLLECTOR CURRENT vs COLLECTOR TO EMITTER VOLTAGE

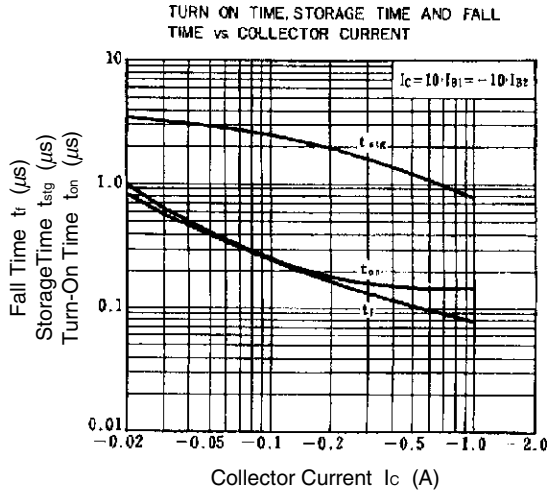


DC CURRENT GAIN vs COLLECTOR CURRENT

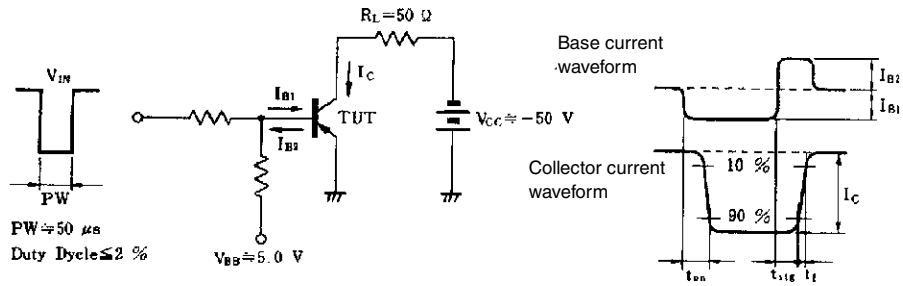


COLLECTOR AND BASE SATURATION VOLTAGE vs COLLECTOR CURRENT





SWITCHING TIME (t_{on} , t_{stg} , t_f) TEST CIRCUIT



[MEMO]

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