

1.9A / 1.2MHz Boost DC to DC Converter

DESCRIPTION

The TS1935B is a current mode step up converter intended for small, low power applications. The converter input voltage ranging from 2.6V to 5.5V. The Output voltage can be set up to 27V. The frequency is 1.2MHz allows the use of small external inductors and capacitors and provides fast transient response. Internal soft start results in small inrush current and extends battery life. Internal power MOSFET with very low RDS (ON) provides high efficiency. The TS1935B automatically transits from PWM to PFM during light load condition further increasing efficiency. The converter also provides protection functions such as under-voltage lockout, current limit and thermal shutdown.

FEATURES

- 2.6V to 5.5 V operating input voltage range
- Adjustable output voltage range up to 27V
- 1.2MHz Fixed Switching Frequency
- Internal soft-start function
- Current limit and Thermal shutdown protection
- Under voltage Lockout
- $\leq 1\mu\text{A}$ Shutdown Current
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-Free according to IEC 61249-2-21

APPLICATION

- White LED current source
- Portable electronics
- Local Boost Regulator



SOT-25

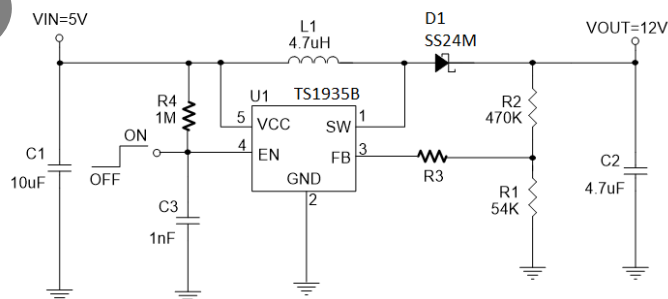


Pin Definition:

1. SW
2. Ground
3. FB
4. EN
5. V_{CC}

Notes: MSL 3 (Moisture Sensitivity Level) per J-STD-020

TYPICAL APPLICATION CIRCUIT



$$V_{OUT} = 1.238V \times \left(1 + \frac{R_2}{R_1}\right)$$

R2 Suggest 390K~820K

V _{IN}	V _{OUT}	R3
2.6~3.6V	5V	120kΩ
2.6~5.3V	7V	82kΩ
2.6~5.5V	7.5~27V	0Ω

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise specified) ^(Note 1)			
PARAMETER	SYMBOL	LIMIT	UNIT
Input Voltage	V_{IN}	GND - 0.3 to GND + 6.5	V
EN, V_{FB} Voltage	V_{EN}, V_{FB}	GND - 0.3 to $V_{CC} + 0.3$	V
SW Voltage	V_{SW}	30	V
Internal Power Dissipation	P_D	$(T_J - T_A)/R_{\theta JA}$	mW
Lead Solder Temperature (260°C)		5	S
Ambient Temperature Range	T_A	-40 to +85	°C
Junction Temperature Range	T_J	-40 to +125	°C
Storage Temperature Range	T_{STG}	-40 to +150	°C

Note: Stress above the listed absolute maximum rating may cause permanent damage to the device

THERMAL PERFORMANCE ^(Note 3)			
PARAMETER	SYMBOL	LIMIT	UNIT
Thermal Resistance - Junction to Case	$R_{\theta JC}$	110	°C/W
Thermal Resistance - Junction to Ambient	$R_{\theta JA}$	250	°C/W

Note: $R_{\theta JA}$ is measured with the PCB copper area of approximately 1 in² (Multi-layer).

RECOMMENDED OPERATING CONDITION ($T_A = 25^\circ\text{C}$ unless otherwise specified) ^(Note 4)			
PARAMETER	SYMBOL	LIMIT	UNIT
Power supply pin	V_{CC}	38	V
DMG voltage to GND	V_{DMG}	-0.3 to 38	V
OUT voltage to GND	V_{OUT}	-0.3 to 38	V
CS voltage to GND	V_{CS}	-0.3 to 5	V
COM voltage to GND	V_{COM}	-0.3 to V_{CC}	V
Operating Junction Temperature Range	T_J	-40 to +125	°C
Operating Ambient Temperature Range	T_{OPA}	-40 to +85	°C

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Input Voltage range	V_{CC}		2.6	--	5.5	V
Under Voltage Lockout	UVLO	Rising	--	2.35	2.60	V
UVLO Hysteresis			--	-130	--	mV
Step-Up Voltage Adjust Range	V_{OUT}		3	--	27	V
Operating quiescent current	I_{CCQ}	$I_{OUT} = 0\text{mA}, V_{FB} = 1.5\text{V}$	--	150	250	μA
Shutdown current	I_{SD}	$V_{EN} = 0\text{V}$	--	0.1	1	μA
Feedback Voltage	V_{FB}		1.213	1.238	1.263	V
FB Input Leakage Current	I_{FB-LKG}	$V_{FB} = 1.3\text{V}$	-100	0.01	+100	nA
Line Regulation		$V_{IN} = 2.5$ to 5.5V $I_{OUT} = 20\text{mA}$	--	0.2	--	%
Load Regulation		$V_{IN} = 5\text{V}$ $I_{OUT} = 1\text{mA}$ to 400mA	-	0.2	--	%
Switching frequency	F_{OSC}		900	1200	1500	kHz
Maximum Duty	D_{MAX}		82	87	-	%

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise specified)						
PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
N-channel MOSFET current limit	I_{LIM}	Duty=50%	--	1.9	--	A
MOSFET on-resistance ^(Note)	$R_{DS(on)}$	$V_{CC}=3V, I_{SW}=1A$	--	650	-	mΩ
		$V_{CC}=5V, I_{SW}=1A$	--	500	--	
SW Leakage Current	I_{SWL}	$V_{LX} = 27V, V_{FB} = 1.5V$	--	--	1	μA
EN high-level input voltage	V_{IH}		1.0	--	--	V
EN low-level input voltage	V_{IL}		--	--	0.4	V
EN Hysteresis	hys		--	200	-	mV
EN input leakage current	I_{EN-LKG}	$V_{EN}=GND$ or V_{CC}	--	0.01	0.1	μA
Thermal Shutdown	T_{DS}		--	150	--	°C
Thermal Shutdown Hysteresis	T_{SH}		--	35	--	

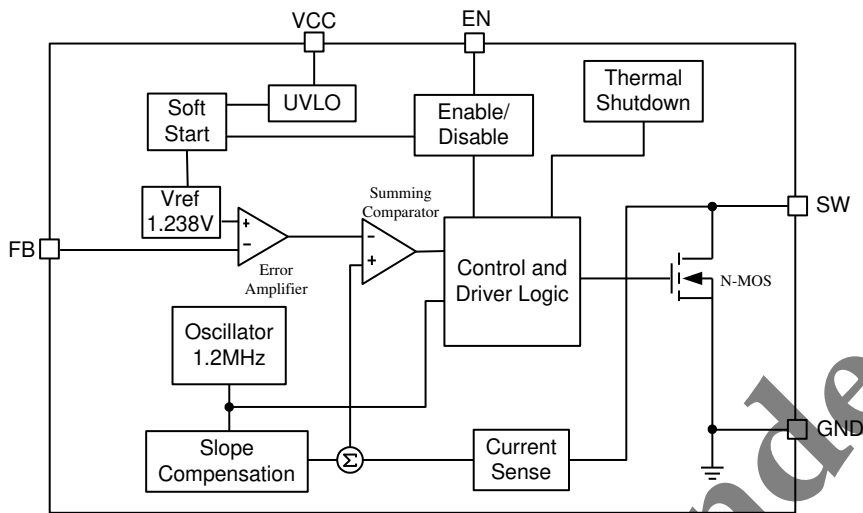
Note: Guaranteed by design

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TS1935BCX5 RFG	SOT-25	3,000pcs / 7" Reel

Not Recommended

FUNCTION BLOCK



PIN DESCRIPTION

PIN NO.	NAME	FUNCTION
1	SW	Power Switch Output. SW is the drain of the internal MOSFET switch. Connect the power inductor and output rectifier to SW. SW can swing between GND and 27V.
2	GND	Ground. Tie directly to ground plan.
3	FB	Feedback Input. FB voltage is 1.238V. Connect a resistor divider to FB.
4	EN	Regulator On/Off Control Input. A high input at EN turns on the converter, and a low input turns it off. When not used, connect EN to the input source for automatic startup. The EN pin cannot be left floating.
5	VCC	Input Supply Pin. Must be locally bypassed.

APPLICATION INFORMATION

Setting the Output Voltage

Application circuit item shows the basic application circuit with TS1935BCX5 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 1.238V \times \left(1 + \frac{R2}{R1}\right)$$

For most applications, R2 is a suggested a value by 390k~820kΩ. Place the resistor-divider as close to the IC as possible to reduce the noise sensitivity.

Under Voltage Lockout (UVLO)

To avoid mis-operation of the device at low input voltages an under voltage lockout is included that disables the device, if the input voltage falls below (2.35V-130mV).

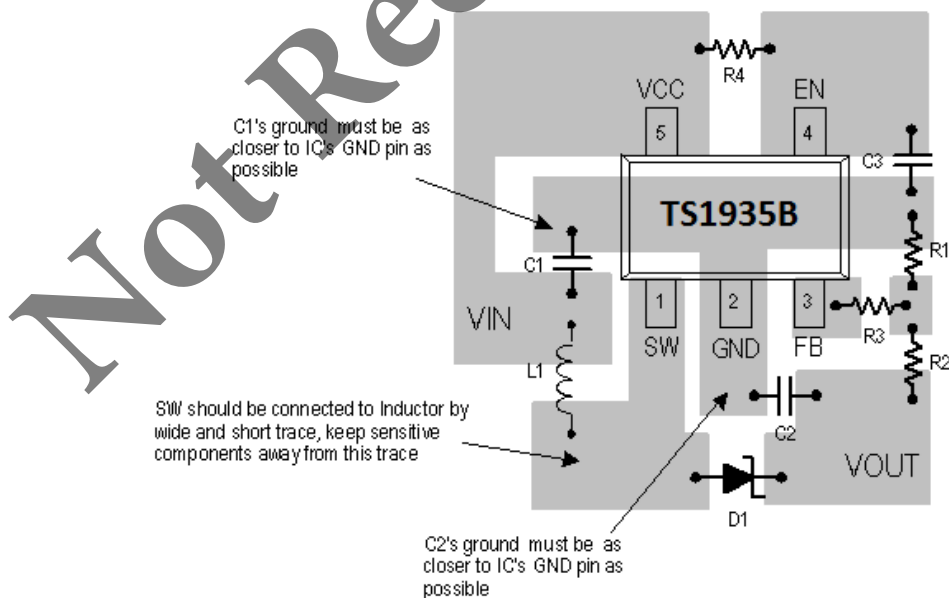
Input Capacitor Selection

The input capacitor reduces the surge current drawn from the input and switching noise from the device. The input capacitor impedance at the switching frequency shall be less than input source impedance to prevent high frequency switching current passing to the input. A low ESR input capacitor sized for maximum RMS current must be used. Ceramic capacitors with X5R or X7R dielectrics are highly recommended because of their low ESR and small temperature coefficients. A 10μF ceramic capacitor for most applications is sufficient. For a lower output power requirement application, this value can be decreased.

Output Capacitor Selection

The output capacitor is required to keep the output voltage ripple small and to ensure regulation loop stability. The output capacitor must have low impedance at the switching frequency. Ceramic capacitors with X5R or X7R dielectrics are recommended due to their low ESR and high ripple current. A 4.7uF ceramic capacitors works for most of the applications. Higher capacitor values can be used to improve the load transient response.

Layout Guide



ELECTRICAL CHARACTERISTICS CURVE

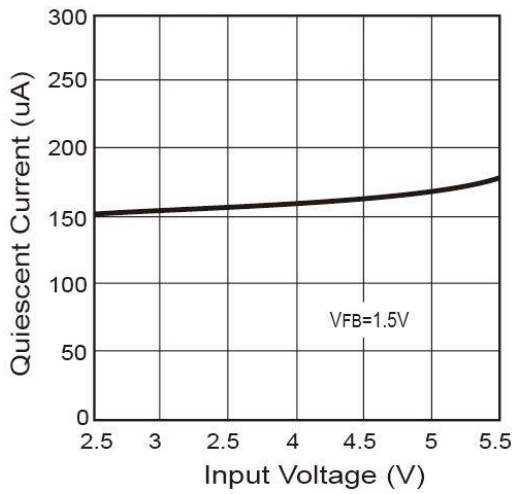


Figure 1. Quiescent Current vs. Input Voltage

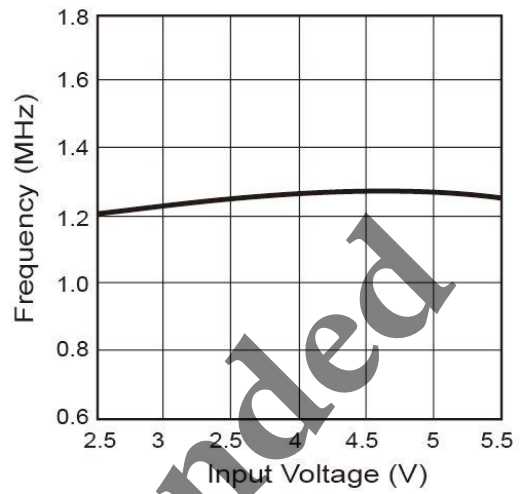


Figure 2. Frequency vs. Input Voltage

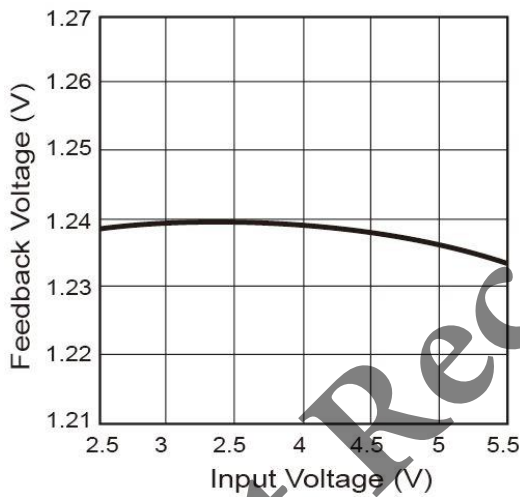


Figure 3. FB Voltage vs. Input Voltage

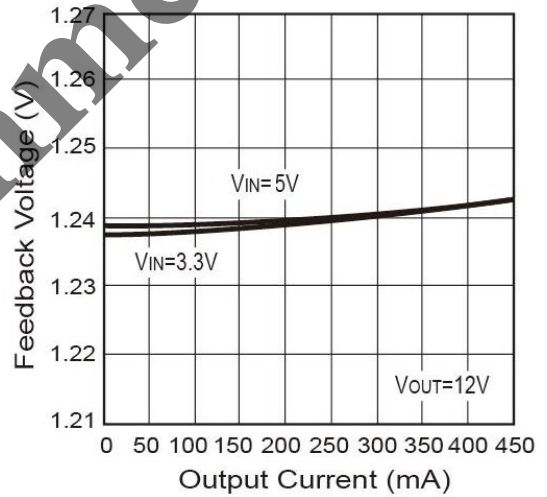


Figure 4. FB Voltage vs. Output Current

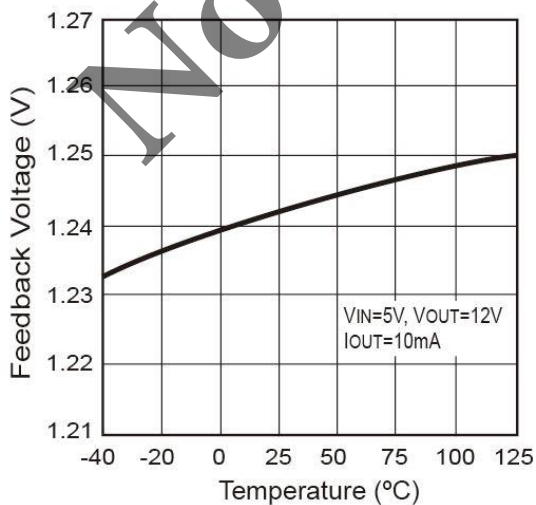


Figure 5. FB Voltage vs. Temperature

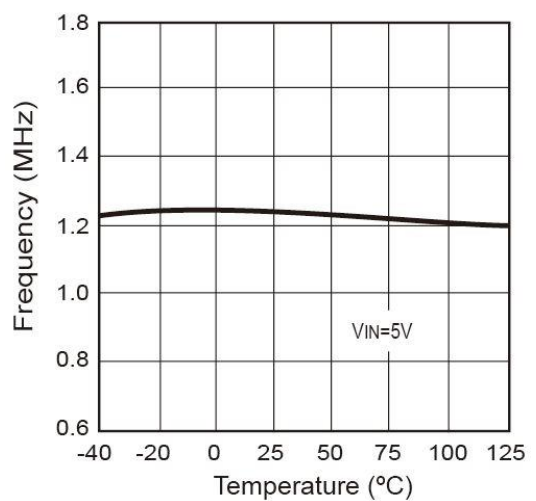


Figure 6. Frequency vs. Temperature

ELECTRICAL CHARACTERISTICS CURVE (CONTINUE)

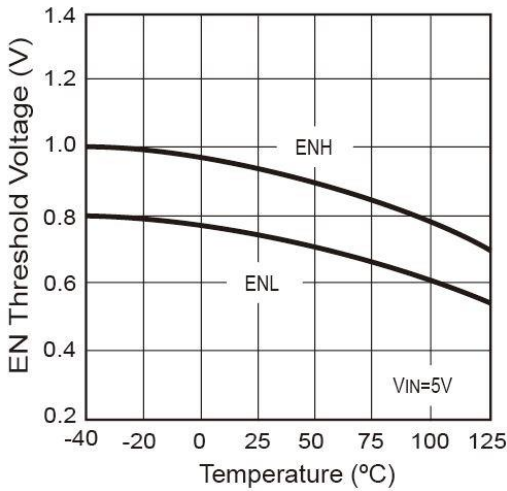


Figure 7. Threshold Voltage vs. Temperature

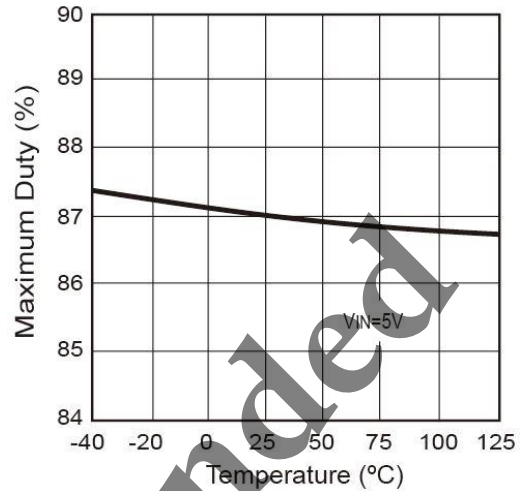


Figure 8. Max. Duty vs. Temperature

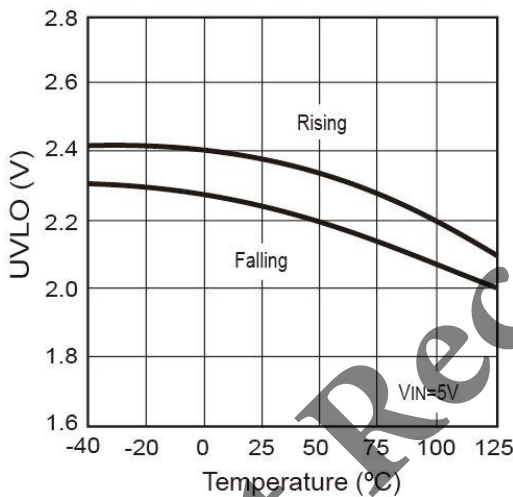


Figure 9. UVLO vs. Temperature

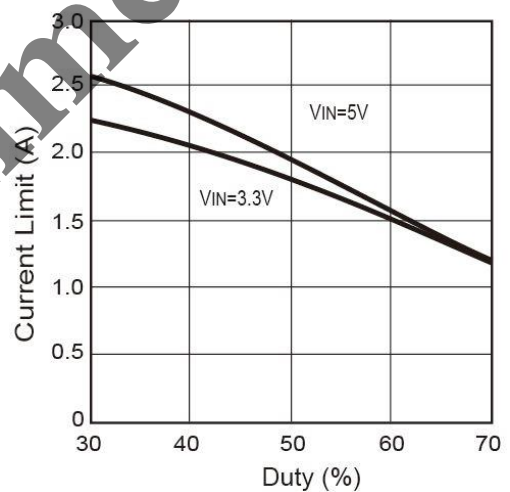


Figure 10. Duty vs. Current Limit

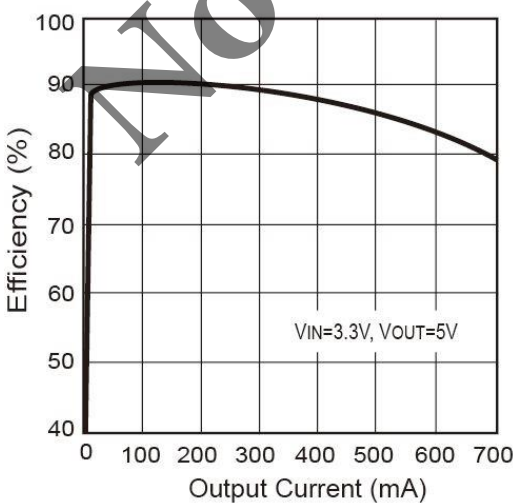


Figure 11. Efficiency vs. Output Current

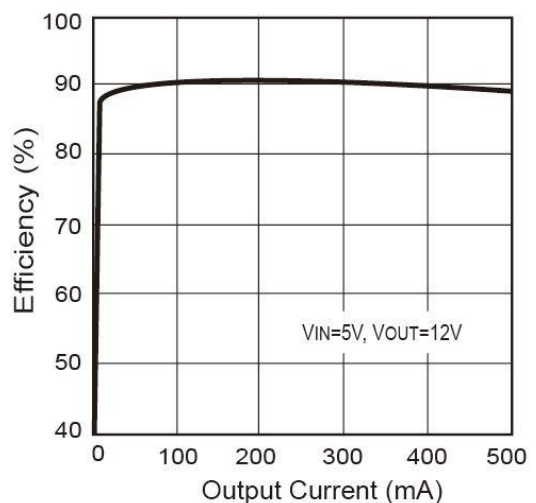
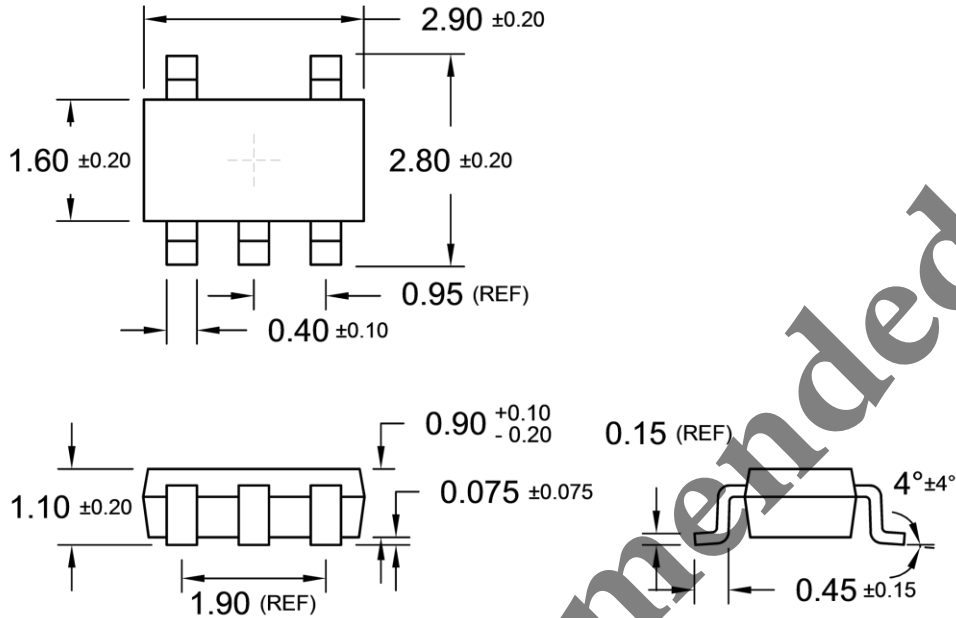


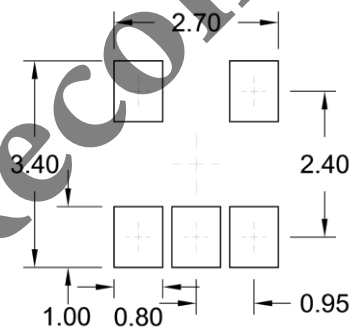
Figure 12. Efficiency vs. Output Current

PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

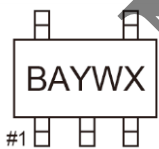
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SUGGESTED PAD LAYOUT (Unit: Millimeters)



MARKING DIAGRAM



- BA** = Device Code
- Y** = Year Code (3=2013, 4=2014.....)
- W** = Week Code
- WW: 01~26 (A~Z)
- 27~52 (a~z)
- X** = Internal ID Code

Not Recommended

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