

CTLDM8120-M621H

**SURFACE MOUNT  
P-CHANNEL  
ENHANCEMENT-MODE  
SILICON MOSFET**

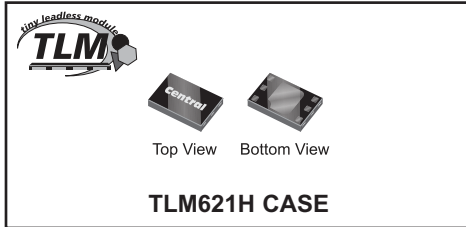


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**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CTLDM8120-M621H is a very low profile (0.4mm) P-Channel enhancement-mode MOSFET in a small, thermally efficient, 1.5mm x 2mm TLM™ package.

**MARKING CODE: CNF**



• Device is **Halogen Free** by design

**APPLICATIONS:**

- Load / Power Switches
- Power Supply Converter Circuits
- Battery Powered Portable Equipment

**MAXIMUM RATINGS:** (T<sub>A</sub>=25°C)

Drain-Source Voltage
Gate-Source Voltage
Continuous Drain Current (Steady State)
Continuous Drain Current, t≤5.0s
Continuous Source Current (Body Diode)
Maximum Pulsed Drain Current, tp=10μs
Maximum Pulsed Source Current, tp=10μs
Power Dissipation (Note 1)
Operating and Storage Junction Temperature
Thermal Resistance (Note 1)

**FEATURES:**

- Low r<sub>DS(ON)</sub> (0.24Ω MAX @ V<sub>DS</sub>=1.8V)
- High Current (I<sub>D</sub>=0.95A)
- Logic Level Compatible
- Small, 1.5 x 2.0 x 0.4mm Ultra Low Height Profile TLM™

SYMBOL		UNITS
V <sub>DS</sub>	20	V
V <sub>GS</sub>	8.0	V
I <sub>D</sub>	860	mA
I <sub>D</sub>	950	mA
I <sub>S</sub>	360	mA
I <sub>DM</sub>	4.0	A
I <sub>SM</sub>	4.0	A
P <sub>D</sub>	1.6	W
T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
θ <sub>JA</sub>	75	°C/W

**ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>GSSF</sub> , I <sub>GSSR</sub>	V <sub>GS</sub> =8.0V, V <sub>DS</sub> =0		1.0	50	nA
I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0		5.0	500	nA
BV <sub>DSS</sub>	V <sub>GS</sub> =0, I <sub>D</sub> =250μA	20	24		V
V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.45	0.76	1.0	V
V <sub>SD</sub>	V <sub>GS</sub> =0, I <sub>S</sub> =360mA			0.9	V
r <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.95A		85	150	mΩ
r <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =0.77A		85	142	mΩ
r <sub>DS(ON)</sub>	V <sub>GS</sub> =2.5V, I <sub>D</sub> =0.67A		130	200	mΩ
r <sub>DS(ON)</sub>	V <sub>GS</sub> =1.8V, I <sub>D</sub> =0.2A		190	240	mΩ
Q <sub>g(tot)</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.0A		3.56		nC
Q <sub>gs</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.0A		0.36		nC
Q <sub>gd</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =1.0A		1.52		nC

Notes: (1) Mounted on a 4-layer JEDEC test board with one thermal vias connecting the exposed thermal pad to the first buried plane. PCB was constructed as per JEDEC standards JESD51-5 and JESD51-7.

R2 (2-August 2011)

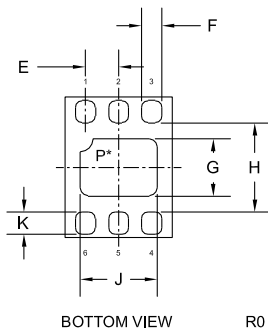
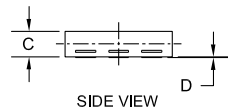
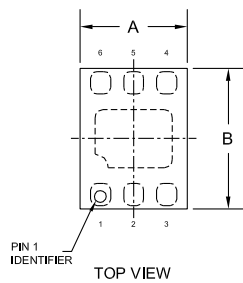
**CTLDM8120-M621H**  
**SURFACE MOUNT**  
**P-CHANNEL**  
**ENHANCEMENT-MODE**  
**SILICON MOSFET**



**ELECTRICAL CHARACTERISTICS - Continued:** ( $T_A=25^\circ\text{C}$  unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$g_{FS}$	$V_{DS}=10\text{V}, I_D=810\text{mA}$	2.0			S
$C_{rss}$	$V_{DS}=16\text{V}, V_{GS}=0, f=1.0\text{MHz}$		80		pF
$C_{iss}$	$V_{DS}=16\text{V}, V_{GS}=0, f=1.0\text{MHz}$		200		pF
$C_{oss}$	$V_{DS}=16\text{V}, V_{GS}=0, f=1.0\text{MHz}$		60		pF
$t_{on}$	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=950\text{mA}, R_G=6.0\Omega$		20		ns
$t_{off}$	$V_{DD}=10\text{V}, V_{GS}=4.5\text{V}, I_D=950\text{mA}, R_G=6.0\Omega$		25		ns

**TLM621H CASE - MECHANICAL OUTLINE**

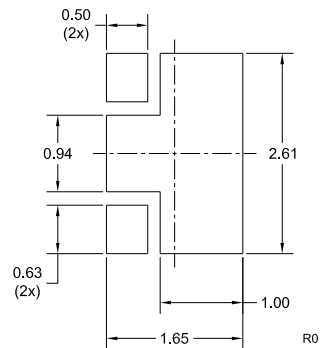


\*Exposed pad P internally connected to pins 2, 3, 4, and 5.

SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.065	1.35	1.65
B	0.073	0.085	1.85	2.15
C	0.012	0.016	0.30	0.40
D	0.000	0.002	0.00	0.05
E	0.020		0.50	
F	0.008	0.012	0.20	0.30
G	0.027	0.035	0.69	0.89
H	0.053	0.057	1.35	1.45
J	0.039	0.047	0.99	1.19
K	0.011	0.015	0.28	0.38

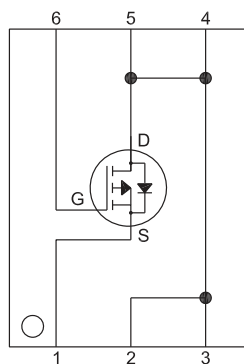
TLM621H (REV:R0)

**OPTIONAL MOUNTING PADS**  
(Dimensions in mm)



For standard mounting refer to TLM621H Package Details

**PIN CONFIGURATION**



**LEAD CODE:**

- 1) Source
- 2) Drain
- 3) Drain
- 4) Drain
- 5) Drain
- 6) Gate

**MARKING CODE: CNF**

R2 (2-August 2011)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

#### Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.  
145 Adams Avenue  
Hauppauge, NY 11788 USA  
Main Tel: (631) 435-1110  
Main Fax: (631) 435-1824  
Support Team Fax: (631) 435-3388  
[www.centrasemi.com](http://www.centrasemi.com)

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# Product End of Life Notification

<b>PDN ID:</b>	PDN01096
<b>Notification Date:</b>	8/07/18
<b>Last Buy Date:</b>	2/07/19
<b>Last Shipment Date</b>	8/07/19

Summary: All devices in the TLM621 and TLM621H packages are discontinued and now classified as End of Life (EOL).

Although Central Semiconductor Corp. makes every effort to continue to produce devices that have been proclaimed EOL (End of Life) by other manufacturers, it is an accepted industry practice to discontinue certain devices when customer demand falls below a minimum level of sustainability. Accordingly, the following product(s) have been transitioned to End of Life status as part of Central's ongoing Product Management Process. Any replacement products are noted below. The effective date for placing last purchase orders will be six (6) months from the date of this notice and twelve (12) months from the notice date for final shipments, and minimum order quantities may apply. The last purchase and shipment dates may be extended if inventory is available.

<u>Central Part Number</u>	<u>Replacement</u>
CTLDM7002A-M621 BK	N/A, Stock Only
CTLDM7002A-M621 TR	N/A, Stock Only
CTLDM7003-M621 BK	N/A, Stock Only
CTLDM7003-M621 TR	N/A, Stock Only
CTLDM7120-M621H BK	N/A, Stock Only
CTLDM7120-M621H TR	N/A, Stock Only
CTLDM8002A-M621 BK	N/A, Stock Only
CTLDM8002A-M621 TR	N/A, Stock Only
CTLDM8002A-M621H BK	N/A, Stock Only
CTLDM8002A-M621H TR	N/A, Stock Only
CTLDM8120-M621H BK	N/A, Stock Only
CTLDM8120-M621H TR	N/A, Stock Only
CTLSH05-40M621 BK	N/A, Stock Only
CTLSH05-40M621 TR	N/A, Stock Only
CTLSH1-40M621H BK	N/A, Stock Only
CTLSH1-40M621H TR	N/A, Stock Only
CTLT3410-M621 BK	N/A, Stock Only
CTLT3410-M621 TR	N/A, Stock Only
CTLT7410-M621 TR	N/A, Stock Only

Central would be happy to assist you by providing additional information or technical data to help locate an alternate source if we have no replacement available. Please email your requests to [engineering@centrasemi.com](mailto:engineering@centrasemi.com).

**DISCLAIMER:** This End of Life (EOL) notification is in accordance with JEDEC standard JESD48 - Product Discontinuance. Central Semiconductor Corp. will make every effort to offer life-time buy (LTB) opportunities and/or offer replacement devices to existing customers for discontinued devices, however, one or both may not be possible for all devices. Please contact your local Central Semiconductor sales representative for LTB opportunities/additional information.