

To our customers,

---

## Old Company Name in Catalogs and Other Documents

---

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

## Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.
2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.
4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.
6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.
  - “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.
  - “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.
  - “Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.
8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

# BIPOLAR ANALOG INTEGRATED CIRCUIT

# $\mu$ PC4570

## ULTRA LOW-NOISE, WIDEBAND, DUAL OPERATIONAL AMPLIFIER

### DESCRIPTION

The  $\mu$ PC4570 is an ultra low-noise, wideband high slew-rate, dual operational amplifier. Input equivalent noise is three times better than the conventional 4558 type op-amps. The gain bandwidth products and the slew-rate are seven times better than 4558. In spite of fast AC performance, the  $\mu$ PC4570 is extremely stable under voltage-follower circuit conditions. Supply current is also improved compared with conventional wideband op-amps. The  $\mu$ PC4570 is an excellent choice for pre-amplifiers and active filters in audio, instrumentation, and communication circuits.

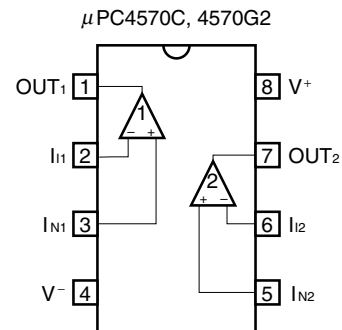
### FEATURES

- Ultra low noise:  $e_n = 4.5 \text{ nV}/\sqrt{\text{Hz}}$
- High slew rate:  $7 \text{ V}/\mu\text{s}$
- High gain bandwidth product:  $\text{GBW} = 15 \text{ MHz}$  at  $100 \text{ kHz}$
- Internal frequency compensation

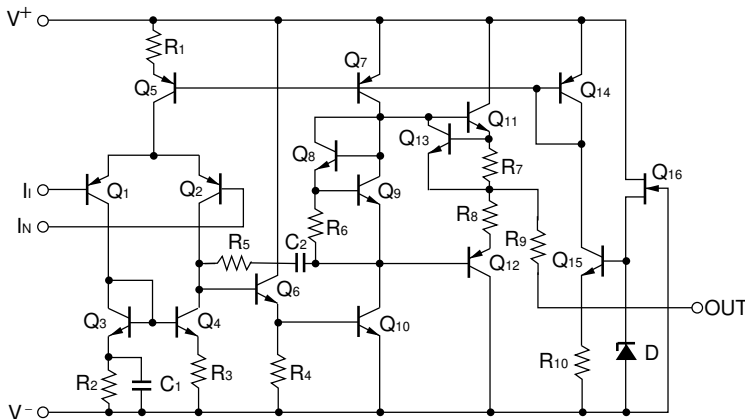
### <R> ORDERING INFORMATION

Part Number	Package
$\mu$ PC4570C	8-pin plastic DIP (7.62 mm (300))
$\mu$ PC4570G2	8-pin plastic SOP (5.72 mm (225))
$\mu$ PC4570G2(5)	8-pin plastic SOP (5.72 mm (225))

### <R> PIN CONFIGURATION (Top View)



### EQUIVALENT CIRCUIT (1/2 Circuit)



The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.  
 Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.

<R> **ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25°C)**

Parameter	Symbol	Ratings	Unit
Voltage between V <sup>+</sup> and V <sup>-</sup> <sup>Note1</sup>	V <sup>+</sup> - V <sup>-</sup>	-0.3 to +36	V
Differential Input Voltage	V <sub>ID</sub>	±30	V
Input Voltage <sup>Note2</sup>	V <sub>I</sub>	V <sup>-</sup> - 0.3 to V <sup>+</sup> + 0.3	V
Output Voltage <sup>Note3</sup>	V <sub>O</sub>	V <sup>-</sup> - 0.3 to V <sup>+</sup> + 0.3	V
Power Dissipation	C Package <sup>Note4</sup>	P <sub>T</sub>	350
	G2 Package <sup>Note5</sup>		440
Output Short Circuit Duration <sup>Note6</sup>	t <sub>s</sub>	10	sec
Operating Ambient Temperature	T <sub>A</sub>	-20 to +80	°C
Storage Temperature	T <sub>stg</sub>	-55 to +125	°C

**Notes 1.** Reverse connection of supply voltage can cause destruction.

2. The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
3. This specification is the voltage which should be allowed to supply to the output terminal from external without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.
4. Thermal derating factor is -5.0 mW/°C when operating ambient temperature is higher than 55°C.
5. Thermal derating factor is -4.4 mW/°C when operating ambient temperature is higher than 25°C.
6. Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

**RECOMMENDED OPERATING CONDITIONS**

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sup>±</sup>	±4		±16	V
Output Current	I <sub>o</sub>			±10	mA
Source Resistance	R <sub>s</sub>			50	kΩ
Capacitive Load (A <sub>v</sub> = +1)	C <sub>L</sub>			100	pF

<R> μPC4570C, μPC4570G2

**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, V<sup>±</sup> = ±15 V)**

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> ≤ 50 Ω		±0.3	±5	mV
Input Offset Current <sup>Note7</sup>	I <sub>IO</sub>			±10	±100	nA
Input Bias Current <sup>Note7</sup>	I <sub>B</sub>			100	400	nA
Large Signal Voltage Gain	A <sub>V</sub>	R <sub>L</sub> ≥ 2 kΩ, V <sub>O</sub> = ±10 V	30,000	300,000		
Supply Current <sup>Note8</sup>	I <sub>CC</sub>	I <sub>O</sub> = 0 A		5	8	mA
Common Mode Rejection Ratio	CMR		80	100		dB
Supply Voltage Rejection Ratio	SVR		80	100		dB
Output Voltage Swing	V <sub>OM</sub>	R <sub>L</sub> ≥ 10 kΩ	±12	±13.4		V
		R <sub>L</sub> ≥ 2 kΩ	±10	±12.8		V
Common Mode Input Voltage Range	V <sub>ICM</sub>		±12	±14		V
Slew Rate	SR	R <sub>L</sub> ≥ 2 kΩ	5	7		V/μs
Gain Bandwidth Product	GBW	f <sub>o</sub> = 100 kHz	10	15		MHz
Unity Gain Frequency	f <sub>unity</sub>	open loop		7		MHz
Phase Margin	φ <sub>unity</sub>	open loop		50		degree
Total Harmonic Distortion	THD	V <sub>O</sub> = 3 V <sub>r.m.s.</sub> , f = 20 Hz to 20 kHz (Figure1)		0.002		%
Input Equivalent Noise Voltage	V <sub>n</sub>	RIAA (Figure2)		0.9		μV <sub>r.m.s.</sub>
		FLAT+JIS A, R <sub>S</sub> = 100 Ω (Figure3)		0.53	0.65	μV <sub>r.m.s.</sub>
Input Equivalent Noise Voltage Density	e <sub>n</sub>	f <sub>o</sub> = 10 Hz, R <sub>S</sub> = 100 Ω		5.5		nV/√Hz
		f <sub>o</sub> = 1 kHz, R <sub>S</sub> = 100 Ω		4.5		nV/√Hz
Input Equivalent Noise Current Density	i <sub>n</sub>	f <sub>o</sub> = 1 kHz		0.7		pA/√Hz
Channel Separation		f = 20 Hz to 20 kHz		120		dB

**Notes 7.** Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage

**8.** This current flows irrespective of the existence of use.

μPC4570G2(5)

ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C, V<sup>±</sup> = ±15 V)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> ≤ 50 Ω		±0.3	±1	mV
Input Offset Current <sup>Note7</sup>	I <sub>IO</sub>			±10	±50	nA
Input Bias Current <sup>Note7</sup>	I <sub>B</sub>			100	200	nA
Large Signal Voltage Gain	A <sub>V</sub>	R <sub>L</sub> ≥ 2 kΩ, V <sub>O</sub> = ±10 V	50,000	300,000		
Supply Current <sup>Note8</sup>	I <sub>CC</sub>	I <sub>O</sub> = 0 A		5	7	mA
Common Mode Rejection Ratio	CMR		85	100		dB
Supply Voltage Rejection Ratio	SVR		85	100		dB
Output Voltage Swing	V <sub>om</sub>	R <sub>L</sub> ≥ 10 kΩ	±13	±13.4		V
		R <sub>L</sub> ≥ 2 kΩ	±12	±12.8		V
Common Mode Input Voltage Range	V <sub>ICM</sub>		±13.5	±14		V
Slew Rate	SR	R <sub>L</sub> ≥ 2 kΩ	5	7		V/μs
Gain Bandwidth Product	GBW	f <sub>o</sub> = 100 kHz	10	15		MHz
Unity Gain Frequency	f <sub>unity</sub>	open loop		7		MHz
Phase Margin	φ <sub>unity</sub>	open loop		50		degree
Total Harmonic Distortion	THD	V <sub>O</sub> = 3 V <sub>r.m.s.</sub> , f = 20 Hz to 20 kHz (Figure1)		0.002		%
Input Equivalent Noise Voltage	V <sub>n</sub>	RIAA (Figure2)		0.9		μV <sub>r.m.s.</sub>
		FLAT+JIS A, R <sub>S</sub> = 100 Ω (Figure3)		0.53	0.65	μV <sub>r.m.s.</sub>
Input Equivalent Noise Voltage Density	e <sub>n</sub>	f <sub>o</sub> = 10 Hz, R <sub>S</sub> = 100 Ω		5.5		nV/√Hz
		f <sub>o</sub> = 1 kHz, R <sub>S</sub> = 100 Ω		4.5		nV/√Hz
Input Equivalent Noise Current Density	i <sub>n</sub>	f <sub>o</sub> = 1 kHz		0.7		pA/√Hz
Channel Separation		f = 20 Hz to 20 kHz		120		dB

**Notes 7.** Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage

**8.** This current flows irrespective of the existence of use.

MEASUREMENT CIRCUIT

Figure1 Total Harmonic Distortion Measurement Circuit

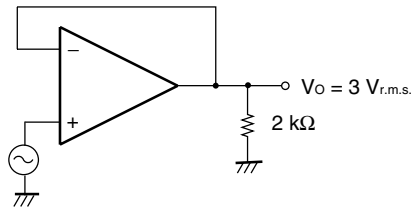


Figure2 Noise Measurement Circuit (RIAA)

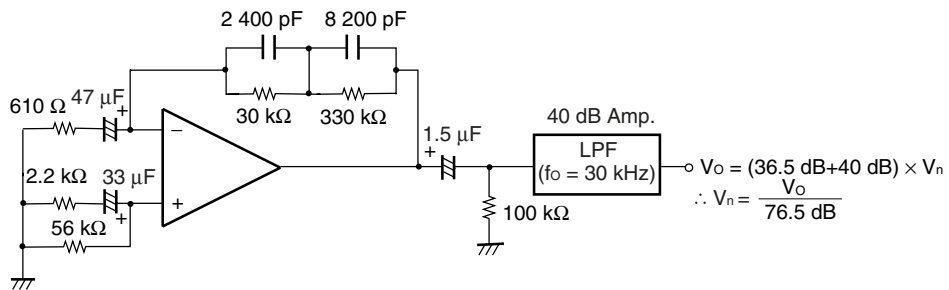
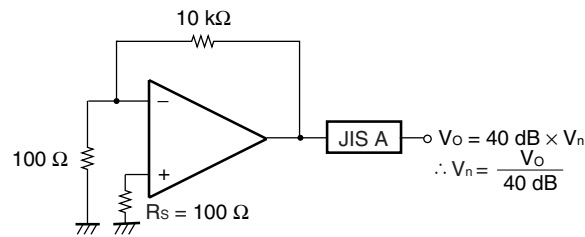
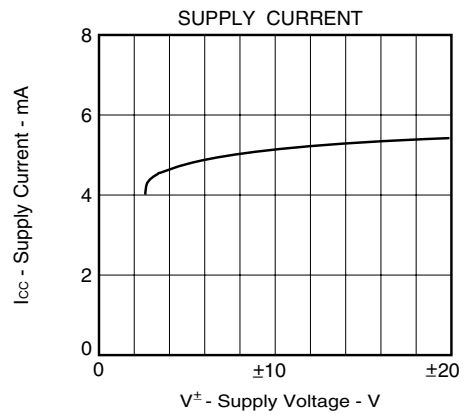
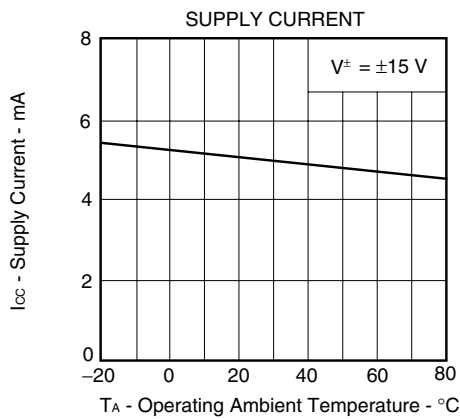
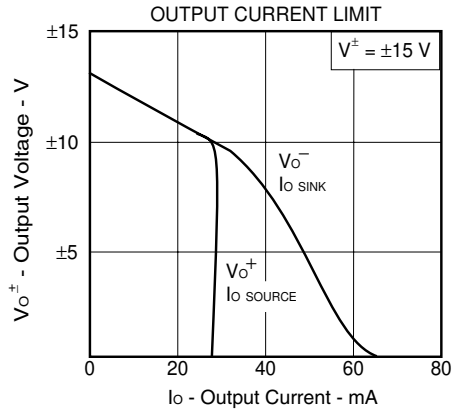
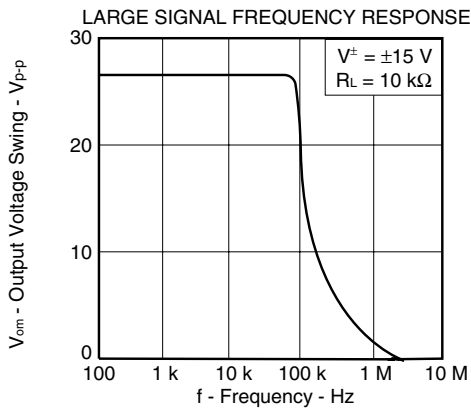
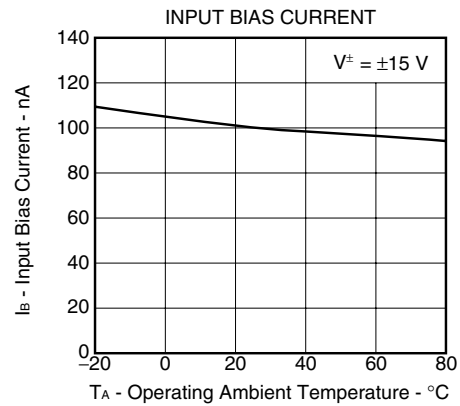
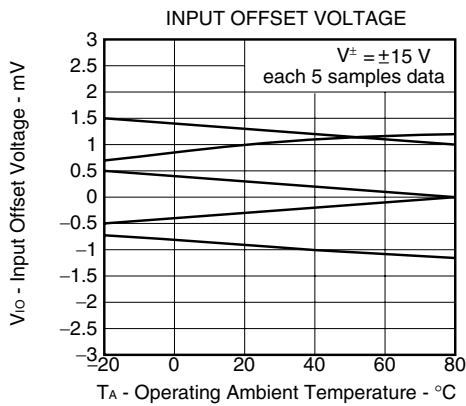
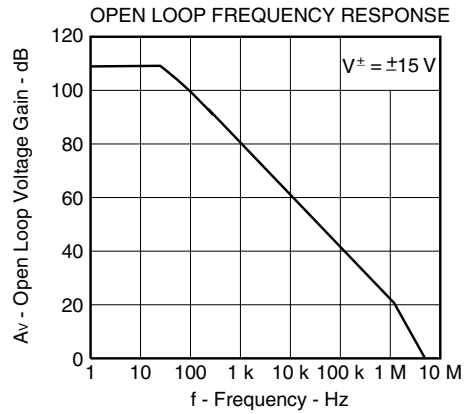
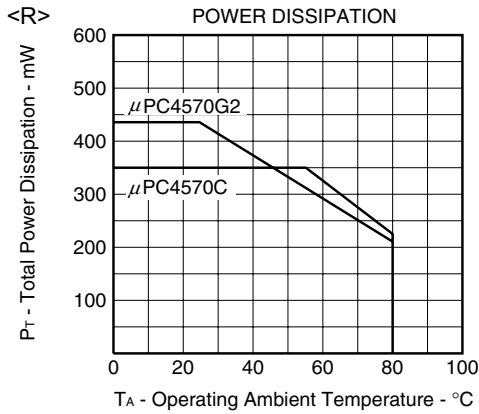


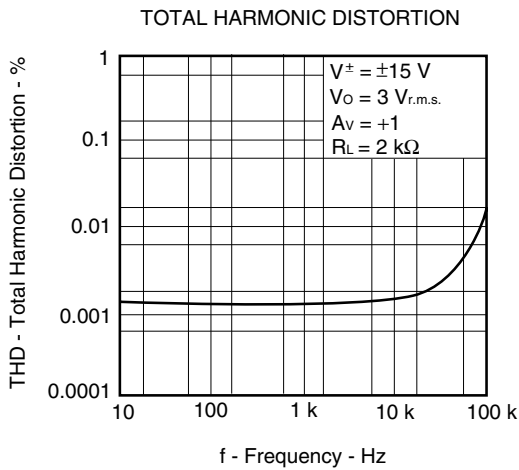
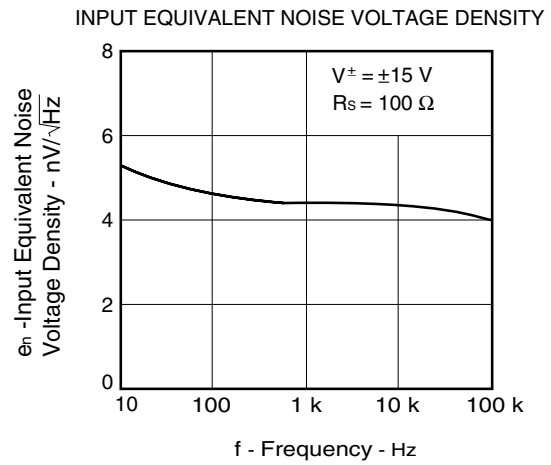
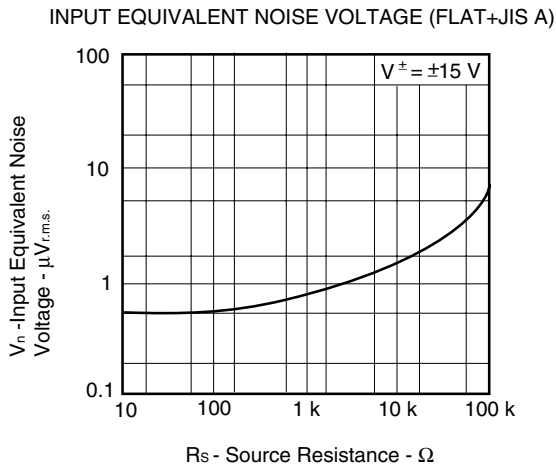
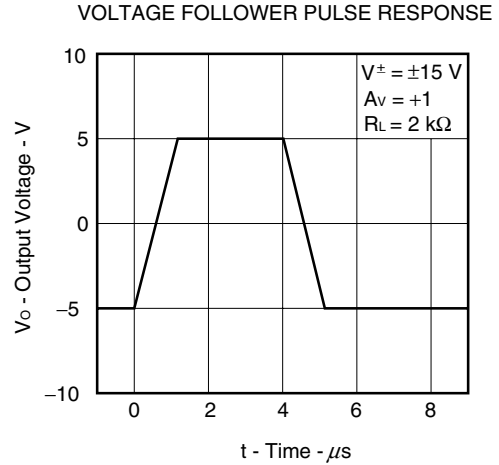
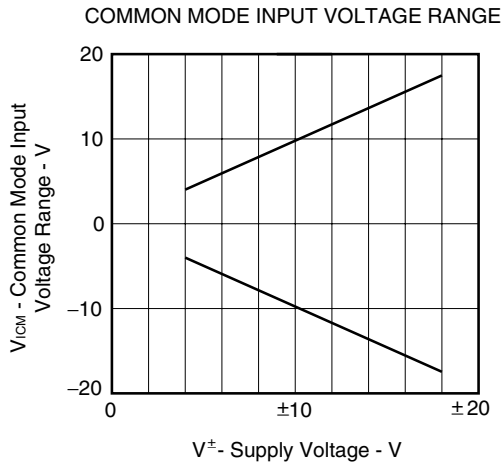
Figure3 Noise Measurement Circuit (FLAT+JIS A)



TYPICAL PERFORMANCE CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ , TYP.)

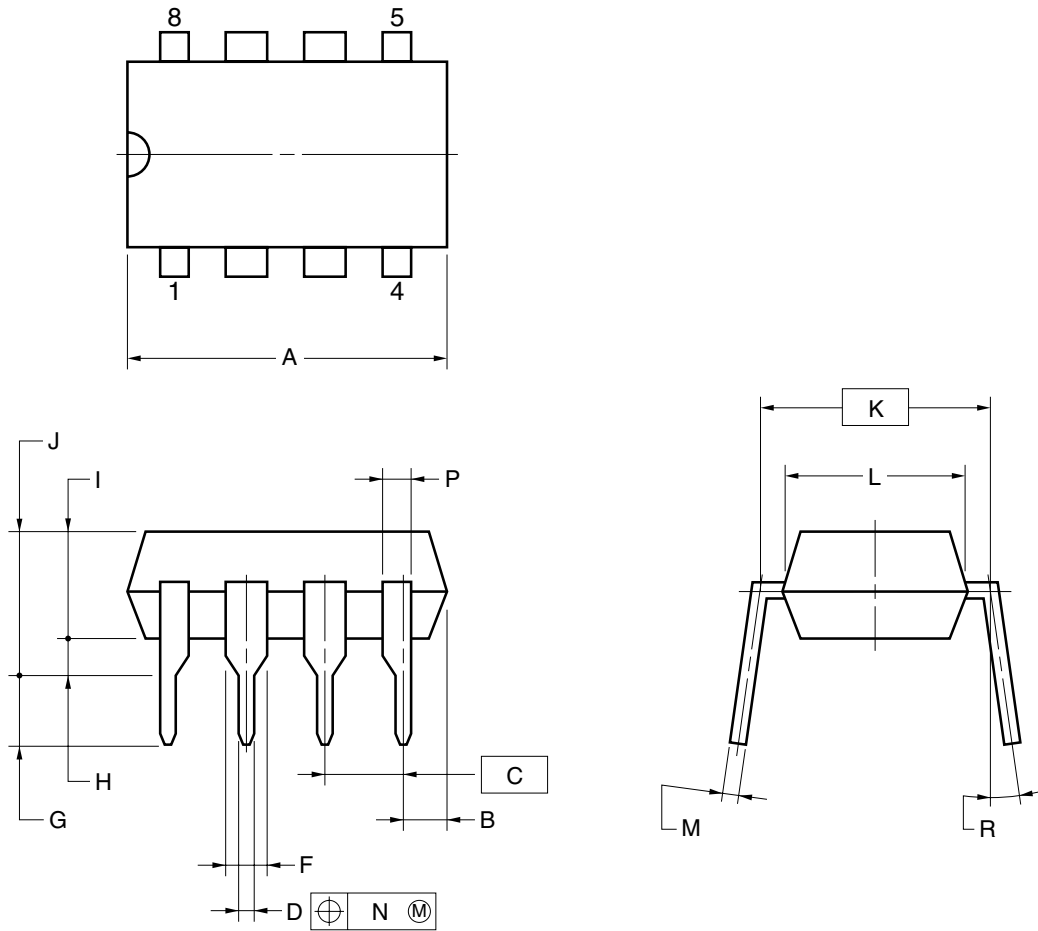






<R> PACKAGE DRAWINGS (Unit: mm)

8-PIN PLASTIC DIP (7.62mm(300))



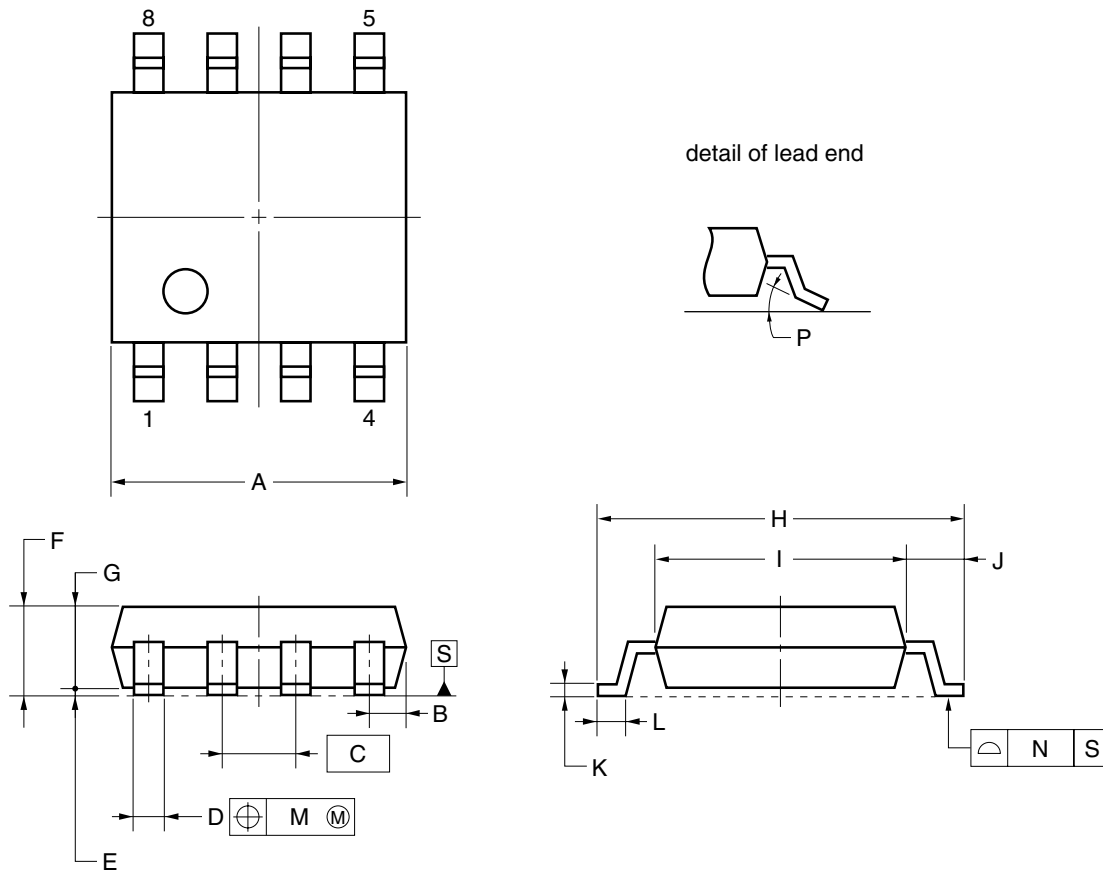
NOTES

1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
2. Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS
A	10.16 MAX.
B	1.27 MAX.
C	2.54 (T.P.)
D	0.50±0.10
F	1.4 MIN.
G	3.2±0.3
H	0.51 MIN.
I	4.31 MAX.
J	5.08 MAX.
K	7.62 (T.P.)
L	6.4
M	0.25 <sup>+0.10</sup> <sub>-0.05</sub>
N	0.25
P	0.9 MIN.
R	0~15°

P8C-100-300B,C-2

8-PIN PLASTIC SOP (5.72 mm (225))



**NOTE**

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	5.2 <sup>+0.17</sup> / <sub>-0.20</sub>
B	0.78 MAX.
C	1.27 (T.P.)
D	0.42 <sup>+0.08</sup> / <sub>-0.07</sub>
E	0.1±0.1
F	1.59±0.21
G	1.49
H	6.5±0.3
I	4.4±0.15
J	1.1±0.2
K	0.17 <sup>+0.08</sup> / <sub>-0.07</sub>
L	0.6±0.2
M	0.12
N	0.10
P	3° <sup>+7°</sup> / <sub>-3°</sub>

S8GM-50-225B-6

<R> **RECOMMENDED SOLDERING CONDITIONS**

The μPC4570 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

**Semiconductor Device Mount Manual (<http://www.necel.com/pkg/en/mount/index.html>)**

**Type of Surface Mount Device**

**μPC4570G2, μPC4570G2(5): 8-pin plastic SOP (5.72 mm (225))**

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 230°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 1 time.	IR30-00-1
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 1 time.	VP15-00-1
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	—

**Caution** Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

**Type of Through-hole Device**

**μPC4570C: 8-pin plastic DIP (7.62 mm (300))**

Process	Conditions
Wave Soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (per each lead).

**Caution** For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

<R> **REFERENCE DOCUMENTS**

QUALITY GRADES ON NEC SEMICONDUCTOR DEVICES	C11531E
SEMICONDUCTOR DEVICE MOUNT MANUAL	<a href="http://www.necel.com/pkg/en/mount/index.html">http://www.necel.com/pkg/en/mount/index.html</a>
NEC SEMICONDUCTOR DEVICE RELIABILITY/	IEI-1212
QUALITY CONTROL SYSTEM- STANDARD LINEAR IC	

- **The information in this document is current as of December, 2007. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets or data books, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.**
- No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Electronics. NEC Electronics assumes no responsibility for any errors that may appear in this document.
- NEC Electronics does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from the use of NEC Electronics products listed in this document or any other liability arising from the use of such products. No license, express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Electronics or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software and information in the design of a customer's equipment shall be done under the full responsibility of the customer. NEC Electronics assumes no responsibility for any losses incurred by customers or third parties arising from the use of these circuits, software and information.
- While NEC Electronics endeavors to enhance the quality, reliability and safety of NEC Electronics products, customers agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property or injury (including death) to persons arising from defects in NEC Electronics products, customers must incorporate sufficient safety measures in their design, such as redundancy, fire-containment and anti-failure features.
- NEC Electronics products are classified into the following three quality grades: "Standard", "Special" and "Specific".

The "Specific" quality grade applies only to NEC Electronics products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of an NEC Electronics product depend on its quality grade, as indicated below. Customers must check the quality grade of each NEC Electronics product before using it in a particular application.

"Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots.

"Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).

"Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

(Note)

- (1) "NEC Electronics" as used in this statement means NEC Electronics Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC Electronics products" means any product developed or manufactured by or for NEC Electronics (as defined above).