

SIDC03D60C8

Fast switching diode chip in Emitter Controlled 3 -Technology

Features:

- 600V Emitter Controlled 3 technology 70 µm chip
- soft, fast switching
- low reverse recovery charge
- · small temperature coefficient

Recommended storage environment

This chip is used for:

- Power module
- Discrete components



Applications:

- Drives
- · White goods
- Resonant applications

Chip Type	V_{R}	I F	Die Size	Package
SIDC03D60C8	600V	10A	1.82 x 1.82 mm ²	sawn on foil

Mechanical Parameters Raster size 1.82 x 1.82 $\,\mathrm{mm}^2$ Area total 3.312 Anode pad size 1.39 x 1.39 **Thickness** 70 μm Wafer size 200 mm 8499 Max. possible chips per wafer Passivation frontside Photoimide 3200 nm AlSiCu Pad metal Ni Ag -system Backside metal suitable for epoxy and soft solder die bonding Die bond Electrically conductive glue or solder Wire bond Al, ≤500µm Reject ink dot size Ø 0.65mm; max 1.2mm Store in original container, in dry nitrogen, in dark

environment, < 6 month at an ambient temperature of 23°C



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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}	<i>T</i> _{vj} = 25 ℃	600	V
Continuous forward current	I _F	<i>T</i> _{vj} < 150℃	1)	A
Maximum repetitive forward current	I _{FRM}	<i>T</i> _{vj} < 150℃	20	
Junction temperature range	$T_{\rm vj}$		-40+175	°C
Operating junction temperature	T _{vj}		-40+150	°C
Dynamic ruggedness ²⁾	P _{max}	$I_{\text{Fmax}} = 20\text{A}, \ V_{\text{Rmax}} = 600\text{V}, \ T_{\text{vj}} \le 150^{\circ}\text{C}$	tbd	kW

¹⁾ depending on thermal properties of assembly

Static Characteristics (tested on wafer), $T_{vj} = 25 \text{ }^{\circ}\text{C}$

Parameter	Symbol	Conditions	Value			Unit
raiailletei	Syllibol	Conditions	min.		max.	Onne
Reverse leakage current	I_{R}	V _R =600V			27	μA
Cathode-Anode breakdown Voltage	V_{BR}	I _R =0.25mA	600			V
Diode forward voltage	V_{F}	/ _F =10A	1.25	1.6	1.95	V

Further Electrical Characteristics

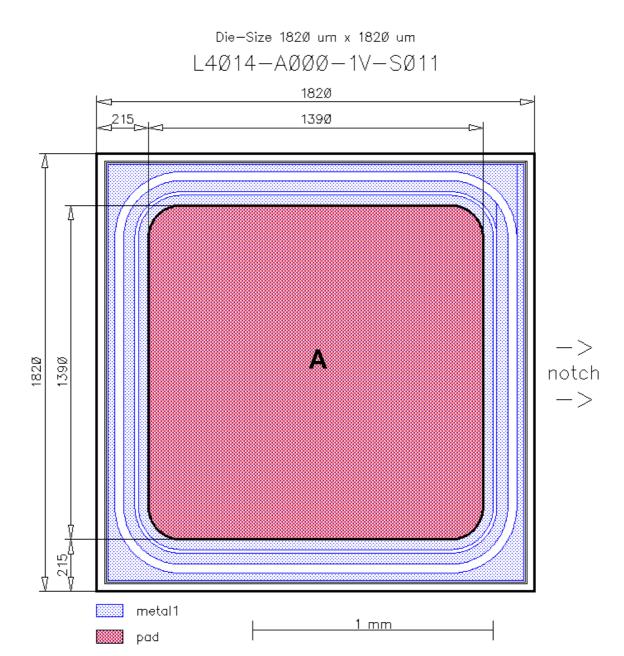
Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

²⁾ not subject to production test - verified by design/characterisation





Chip Drawing



A: Anode pad



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Description
AQL 0,65 for visual inspection according to failure catalogue
Electrostatic Discharge Sensitive Device according to MIL-STD 883

Revision History

Version	Subjects (major changes since last revision)	Date

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