



FEATURES

400W output power
12V main output
5V standby output of 15W
1U height: 2.15"x13.67"x1.58"
8.6 Watts per cubic inch density
Efficiency 85% at full load, 100Vac and 50°C
N+1 redundancy capable, including hot plugging (up to 4 in parallel)
Active current sharing on 12V main output, ORing FET
 Overvoltage, overcurrent, overtemperature protection
Internal cooling fan (variable speed)
PSMI and SMbus / I2C interface with bicolor LED status indicators
RoHS compliant

54mm 1U Front End AC-DC Power Supply Converter

PRODUCT OVERVIEW

The D1U2-W-400-12-HA4C is a 400 watt, power factor corrected front end supply with a 12V main output and a 5V (15W) standby. It features active current sharing and up to 4 supplies maybe operated in parallel. The supply may be hot plugged, it recovers from overtemperature faults, and has status LEDs on the front panel in addition to logic and PSMI status signals. The supply comes in a low profile 1U package and has >8W/cubic inch power density, making it ideal for delivering reliable, efficient power to servers, workstations, storage systems and other 12V distributed power systems.

ORDERING GUIDE

Part Number	Power Output High Line AC	Power Output Low Line AC	Main Output	Standby Output	Airflow
D1U2-W-400-12-HA4C	400W	400W	12V	5V	Back to front

INPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Nom.	Max.	Units
Voltage Operating Range		90	115/230	264	Vac
Frequency		47	50/60	63	Hz
Turn-on Voltage	Ramp up	85			Vac
Turn-off Voltage	Ramp down			85	vac
Maximum current at Vin=100Vac	400W			5	Arms
Inrush Current	Cold start between 0 to 200msec			30	Apk
Power Factor	At 230Vac, full load		0.99		
	35% load	80			
Efficiency (100Vac) including fan load	50% load	85			%
	100% load	85			

OUTPUT VOLTAGE CHARACTERISTICS

Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units
	Voltage Set Point			12.0		Vdo
	Line and Load Regulation		11.8		12.2	Vdc
12V	Ripple Voltage & Noise ¹	20MHz Bandwidth			120	mV p-p
	Output Current (230Vac)		0		33.3	А
	Load Capacitance		0		15,000	μF
	Voltage Set Point			5.0		Vdc
	Line and Load Regulation		4.85		5.15	Vuc
5VSB	Ripple Voltage & Noise ¹	20MHz Bandwidth			50	mV p-p
	Output Current		0		3	А
	Load Capacitance		0		500	μF

Ripple and noise are measured with 0.1 μ F of ceramic capacitance and 10 μ F of tantalum capacitance on each of the power supply outputs. A short coaxial cable with 50 Ω scope termination is used.



Available now at www.murata-ps.com/en/3d/acdc.html





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D1U2-W-400-12-HA4C

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OUTPUT CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Output Rise Monotonicity	No voltage excursion							
Startup Time	AC ramp up		1.5	2.0	S			
Transiant Despanse	12V, 30-70% load step, 1A/µs di/dt			3				
Transient Response	5VSB, 30-70% load step, 0.1A/µs di/dt			3	%			
Current sharing accuracy (up to 4 in parallel)	At 100% load			±10	70			
Hot Swap Transients	All outputs within regulation							
Holdup Time		20			ms			
ENVIRONMENTAL CHARACTERISTICS								
Parameter	Conditions	Min.	Тур.	Max.	Units			
Storage Temperature Range	-40 70							
Derating Temperature Range		0		50	°C			
Dperating Humidity	Noncondensing	5		90				
Storage Humidity		5		95	%			
Altitude (without derating at 55°C)				3,000	m			
Shock	30G non operating							
Operational Vibration	0.5G, 5 – 500 Hz							
ИТВЕ	Per Telcordia SR332M1C1 @25°C	300K			hrs			
	CSA/UL 60950-1-07-2nd Ed.							
Cofety Approvala	IEC 60950-1:2005 (2nd Edition) w Am. 1:20	009						
Safety Approvals	EN 60950-1:2006 +A11:2009 +A1:2010	EN 60950-1:2006 +A11:2009 +A1:2010						
	CE Marking per LVD DIRECTIVE 2006/95/EC)						
nput Fuse	Power Supply has internal 10A/250V fast b	low fuse on the AC	C line input					
Switching Frequency	90KHz for Boost PFC Converter							
	200KHz for Main Output Converter							
Weight	2.28lbs (1.034kg)							

PROTECT	ION CHARACTERISTICS					
Output Voltage	Parameter	Conditions	Min.	Тур.	Max.	Units
	Overtemperature (intake)	Autorestart	65	70	75	°C
12V	Overvoltage	Latching	14.0		14.5	V
IZV	Overcurrent	Hiccup	115		130	%
5VSB	Overvoltage	Latching	5.7		5.9	V
0V2D	Overcurrent	Autorecovery	4.4		6.0	A

ISOLATION CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Inculation Sofety Pating / Test Voltage	Input to Output - Reinforced	3000			Vrms
Insulation Safety Rating / Test Voltage	Input to Chassis - Basic	1500			Vrms
Isolation	Output to Chassis	500			Vrms

CONTROL SIGNALS	
Condition	LED Status
Standby - ON; Main output - OFF; AC PRESENT	Blinking green
Standby - ON; Main output - ON	Solid green
Main/standby output overcurrent, undervoltage, overvoltage warning	Blinking yellow
FAN_FAULT; overtemperature; main/standby output overcurrent, undervoltage, overvoltage fault	Yellow

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D1U2-W-400-12-HA4C

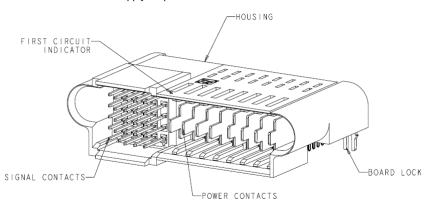
54mm 1U Front End AC-DC Power Supply Converter

EMISSIONS AND IMMUNITY		
Characteristic	Standard	Compliance
Input Current Harmonics	IEC/EN 61000-3-2	Complies
Voltage Fluctuation and Flicker	IEC/EN 61000-3-3	Complies
Conducted Emissions	FCC 47 CFR Part 15/CISPR 22/EN55022	Class B
ESD Immunity	IEC/EN 61000-4-2	Level 3 criteria A
Radiated Field Immunity	IEC/EN 61000-4-3	Level 3 criteria B
Electrical Fast Transients/Burst Immunity	IEC/EN 61000-4-4	Level 3 criteria A
Surge Immunity	IEC/EN 61000-4-5	Level 3 criteria A
Radiated Field Conducted Immunity	IEC/EN 61000-4-6	Level 3 criteria A
Magnetic Field Immunity	IEC/EN 61000-4-8	3 A/m criteria B
		230Vin, 100% load, Phase 0°, Dip 100% Duration 10ms (A)
Voltage dips, interruptions	IEC/EN 61000-4-11	230Vin, 50% load, Phase 0°, Dip 100% Duration 20ms (VSB:A, V1:A)
		230Vin, 100% load, Phase 0°, Dip 100% Duration > 20ms (VSB, V1:B)

DC OUTPUT CONNECTOR AND SIGNALS

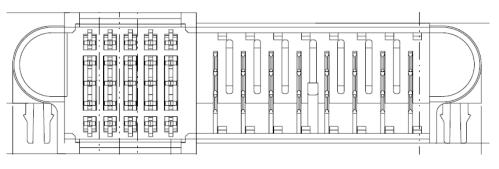
The DC Output Connector is a TYCO MINIPAK HDL Connector **TYCO P/N: 1926734-1**. Mating pin sequencing shall be 12V_RTN first, 12V second, signals third and PSKILL_L signal last. PSKILL_L is the last to mate and first to break and is used as a power supply output enable for the 12V rail.

Mating Part: TYCO P/N 1-1926739-8



Power Supply Output Connector Isometric and Front Views

Front Connector View Looking at Blades and Pins (view looking in at rear of power supply)





54mm 1U Front End AC-DC Power Supply Converter

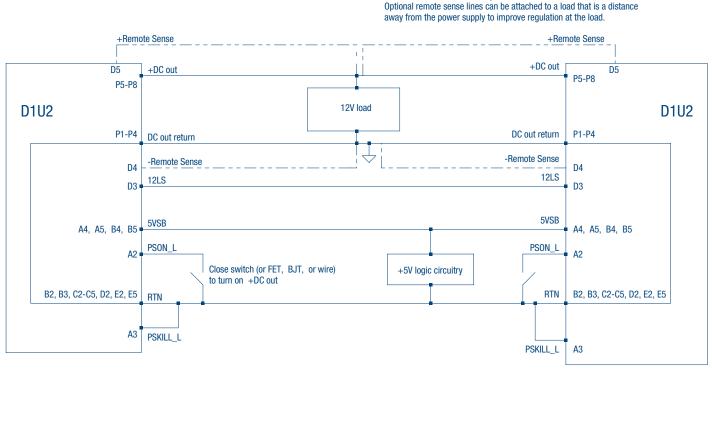
	to mating face	e of 25S8P M	INIPAK HD	L Plug								
Columr	1:											
Row: 1	2	3	4	5	1	2	3	4	5	6	7	8
e												
. –	_	+										
d												
с												
b												
а												
ER SUPPLY OUTF wer Blade Numb		OR POWER Signal	BLADE AN	ID SIGNAL		ATION nction		Sid	gnal Directio	n	Blade	Sequencing
	۶r	-						SI	-	n		
P1, P2, P3, P4		RTN				Return			Output		Level 3 UPM PWF	
P5, P6, P7, P8		12V	1			Output			Output Level 2 UPM F			
A1 A2		SMB_ALERT_	_L		12C Serial				0			
		PSUN L				Bus Interrup Supply ON	ot		Output Input		Leve	el 2 Signal
A3		PSON_L PSKILL L			Power	Supply ON			Input			
		PSON_L PSKILL_L 5VSB			Power St.	Supply ON Ipply Enable			Input Input		Lev	el 1 Signal
A3 A4, A5 B1		PSKILL_L 5VSB			Power Su Power Su 5V Stand	Supply ON	!		Input		Lev	
A4, A5		PSKILL_L			Power Su Power Su 5V Stand I2C Seria	Supply ON Ipply Enable dby Voltage	!	E	Input Input Output Bi-directional		Leve	el 1 Signal
A4, A5 B1 B2, B3 B4, B5		PSKILL_L 5VSB SMB_SCL RTN 5VSB			Power S Power Su 5V Stand I2C Seria Gr	Supply ON Ipply Enable dby Voltage Il Bus Clock	!		Input Input Output		Leve	el 1 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA			Power S Power Su 5V Stand I2C Seria Gr	Supply ON ipply Enable dby Voltage al Bus Clock ound dby Voltage	1		Input Input Output Bi-directional		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5		PSKILL_L 5VSB SMB_SCL RTN 5VSB			Power S Power SL 5V Stand 12C Seria Gr 5V Stand 2C Serial Bus Gr	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound	ress		Input Input Output Si-directional Output Output		Leve Leve Leve	el 1 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1			Power S Power S 5V Stand I2C Seria Gr 5V Stand 2C Serial Bus	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound	ress		Input Input Output di-directional Output di-directional Output Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN			Power St Power St 5V Stand 12C Seria Gr 5V Stand 2C Serial But Gr 2C Serial But	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound	ress		Input Input Output Si-directional Output Output		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS		1:	Power St Power St 5V Stand I2C Seria Gr 5V Stand CS Serial Bus Gr 2C Serial Bus Gr 12V Curre	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin	ress it A1 e	E	Input Input Output di-directional Output di-directional Output Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN		1:	Power St Power St 5V Stand I2C Seria Gr 5V Stand 2C Serial Bus Gr 2C Serial Bus Gr Gr	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin	ress it A1 e	E	Input Input Output Bi-directional Output Bi-directional Output Input Output Bi-directional		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3 D3 D4 D5		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS 12V_RS- 12V_RS+		;	Power St Power St 5V Stand 12C Seria Gr 5V Stand 2C Serial Bus Gr 2C Serial Bus Gr 12V Curre 12V Remote 12V Remote	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin Sense Nega Sense Posi	ress it A1 e tive tive	E	Input Input Output ii-directional Output ii-directional Output Input ii-directional Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3 D3 D4 D5 E1		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS 12V_RS- 12V_RS- 12V_RS+ SMB_A0		;	Power St Power St 5V Stand 12C Seria Gr 5V Stand 2C Serial Bus Gr 2C Serial Bus Gr 12V Curre 12V Curre	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin Sense Nega Sense Posi	ress it A1 e tive tive	E	Input Input Output Bi-directional Output Bi-directional Output Input Output Bi-directional		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3 D3 D4 D5		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS 12V_RS- 12V_RS- 12V_RS+ SMB_A0 RTN		;	Power St Power St 5V Stand 12C Serial Gr 5V Stand 2C Serial Bus Gr 12V Curre 12V Remote 12V Remote 12V Remote 2C Serial Bus Gr	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin Sense Nega Sense Posi s Address B ound	ress it A1 e tive tive it A0	E	Input Input Output ii-directional Output ii-directional Output Input ii-directional Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3 D3 D4 D5 E1 E2 E3		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS 12V_RS- 12V_RS- 12V_RS+ SMB_A0 RTN PWOK_H		;	Power St Power St 5V Stand 12C Serial SV Stand 2C Serial Bus Gr 12V Curre 12V Remote 12V Remote 12V Remote 2C Serial Bus Gr Power O	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin Sense Nega Sense Posi s Address B ound k Status Bit	ress it A1 e tive tive it A0	E	Input Input Output Dutput Output Gutput Input Output Dutput Dutput Input Input Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal
A4, A5 B1 B2, B3 B4, B5 C1 C2, C3, C4, C5 D1 D2 D3 D4 D5 E1 E2		PSKILL_L 5VSB SMB_SCL RTN 5VSB SMB_SDA RTN SMB_A1 RTN 12LS 12V_RS- 12V_RS- 12V_RS+ SMB_A0 RTN		;	Power SL 5V Stand 5V Stand 5V Stand 2C Serial Bus Gr 2C Serial Bus Gr 12V Curre 12V Remote 12V Remote 2C Serial Bus Cr 2C Serial Bus Gr 7 Power O Fan Fail #1	Supply ON upply Enable dby Voltage al Bus Clock ound dby Voltage s Data / Add ound s Address B ound nt Share Lin Sense Nega Sense Posi s Address B ound	ress it A1 e tive tive it A0	E	Input Input Output ii-directional Output ii-directional Output Input ii-directional Input		Leve Leve Leve	el 1 Signal el 2 Signal el 2 Signal el 2 Signal el 2 Signal



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---- Dotted lines show optional remote sense connections.



CURRENT SHARING NOTES

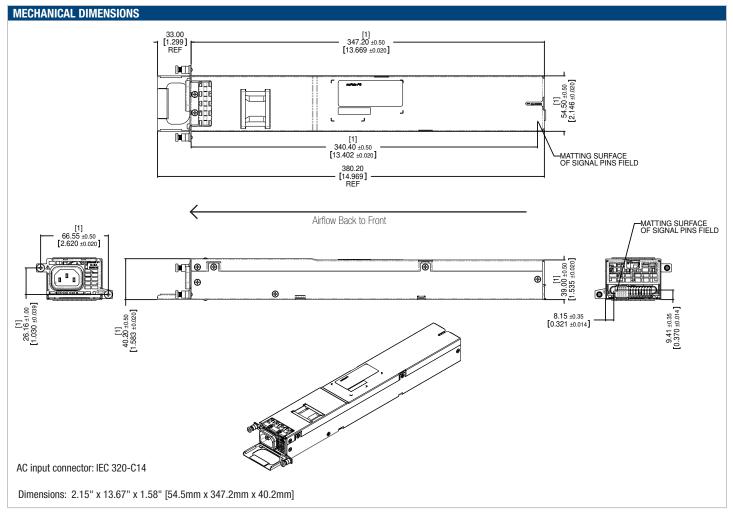
12V Output: Current sharing is achieved using the active current share method. (See wiring diagram section for connection details.) The total combined load must be below 400W at startup. Startup of parallel power supplies is not internally synchronized. It is recommended that the paralleled power supplies be turned on at the same time (with their PSON_L signals). Current sharing can be achieved with or without remote sense connected to the common load.

5VSB Output: 5VSB outputs can be tied together for redundancy but total combined output power must not exceed 15W. The 5VSB output has internal ORing MOSFET for additional redundancy / internal short protection.

Up to four units can be paralleled together. Outputs of AC input units (D1U2-W-400) and DC input units (D1U2-D-400) can be paralleled together. Please consult your Murata sales representative if operation with more than four units in parallel is needed.



54mm 1U Front End AC-DC Power Supply Converter



OPTIONAL ACCESSORIES	
Description	Part Number
12V D1U2 Output Connector Card	D1U2-12-CONC
APPLICATION NOTES	
APPLIGATION NOTES	
Document Number	Description
TBD	D1U2 Output Connector Card
TBD	D1U2 Communication Protocol

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This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy: Refer to: http://www.murata-ps.com/requirements/

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