





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
30V	1.2Ω @ V _{GS} = 4.5V	440mA
	1.5Ω @ V _{GS} = 2.5V	395mA

Features and Benefits

- N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Load Switch

Mechanical Data

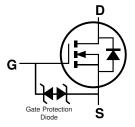
- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208@4
- Weight: 0.001 grams (Approximate)



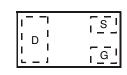




Bottom View



Equivalent Circuit



Top View Pin-Out

Ordering Information (Note 4)

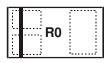
Part Number	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
DMN32D0LFB4-7B	7	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

DMN32D0LFB4-7B



Top View Bar Denotes Gate and Source Side

R0 = Product Type Marking Code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	±10	V
Drain Current (Note 5)	Ι _D	440	mA

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5) @T _A = +25°C	P_{D}	350	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ hetaJA}$	357	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

					I			
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)								
Drain-Source Breakdown Voltage		BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	@ $T_C = +25^{\circ}C$	I _{DSS}	-	_	1	μΑ	$V_{DS} = 30V$, $V_{GS} = 0V$	
Gate-Body Leakage		I_{GSS}	-	_	±10	μΑ	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage		$V_{GS(TH)}$	0.6		1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
				0.6	1.2		$V_{GS} = 4.5V, I_D = 100mA$	
Static Drain-Source On-Resistance		R _{DS(ON)}	_	0.7	1.5	Ω	$V_{GS} = 2.5V, I_D = 20mA$	
				0.8	2.2		$V_{GS} = 1.8V, I_D = 20mA$	
Source-Drain Diode Forward Voltage		V_{SD}	l	0.7	1.4	٧	$V_{GS} = 0V, I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 7)								
Input Capacitance		C_{iss}	l	44.8	_	рF	$V_{DS} = 15V, V_{GS} = 0V$ 	
Output Capacitance		Coss	1	4.6	_	рF		
Reverse Transfer Capacitance		C_{rss}	1	2.5	_	рF		
Gate Resistance		R_{g}	_	81.2	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge		Q_g	I	0.6		nC	V _{GS} = 4.5V, V _{DS} = 15V, I _D = 350mA	
Gate-Source Charge		Q _{gs}	_	0.1	_	nC		
Gate-Drain Charge		Q_{gd}	1	0.2	_	nC		
Turn-On Delay Time		t _{D(ON)}	ı	3.4	_	ns		
Turn-On Rise Time		t_R	1	2.5	_	ns	$V_{DD} = 20V, R_L = 250\Omega, V_{GEN}$ $= 4.5V, R_{GEN} = 6\Omega$	
Turn-Off Delay Time		t _{D(OFF)}	ı	19	_	ns		
Turn-Off Fall Time		tϝ	1	7.9	_	ns		

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to product testing.



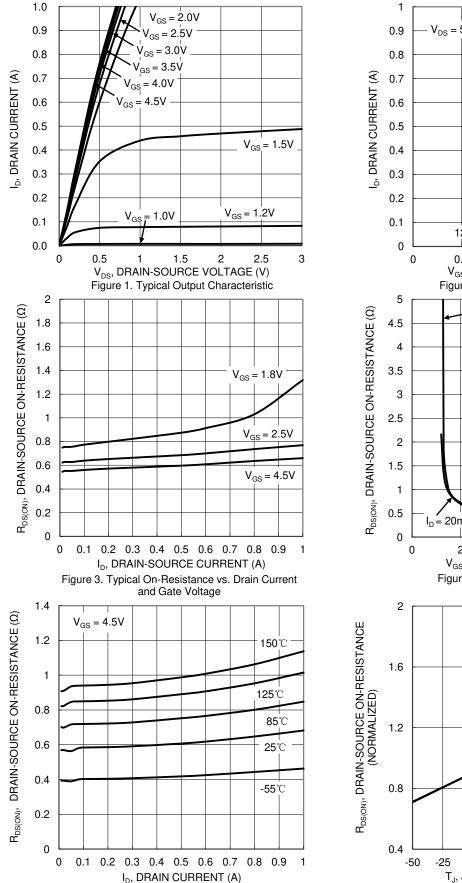
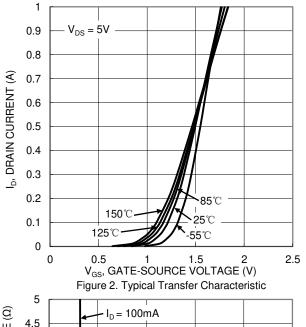
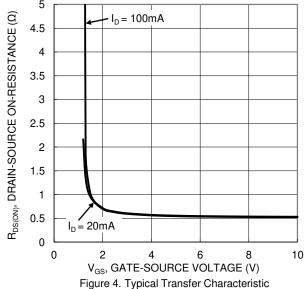


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





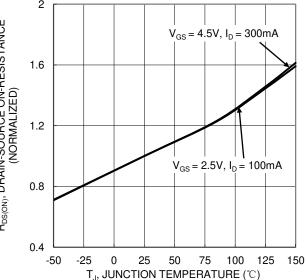


Figure 6. On-Resistance Variation with Temperature





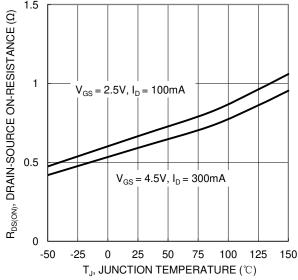
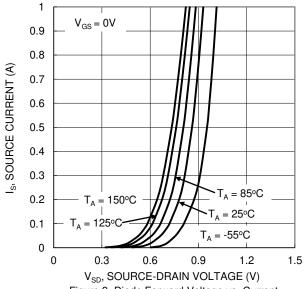
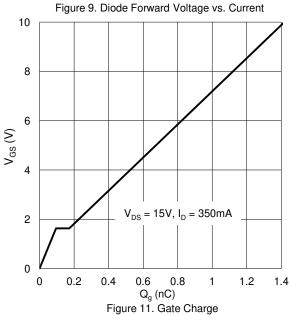


Figure 7. On-Resistance Variation with Temperature





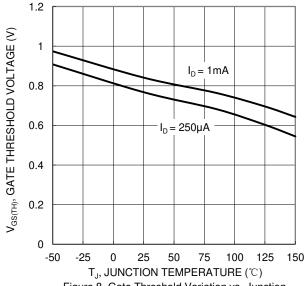
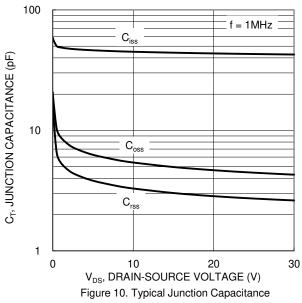


Figure 8. Gate Threshold Variation vs. Junction Temperature



10 $\begin{array}{c} R_{\text{DS(ON)}} \\ \text{Limited} \end{array}$ ID, DRAIN CURRENT (A) = 100ms 0.1 T_{J(Max)} = 150 ℃ T_A = 25°C Single Pulse DUT on 1*MRP Board DC $V_{GS} = 4.5V$ 0.01 0.1 10 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



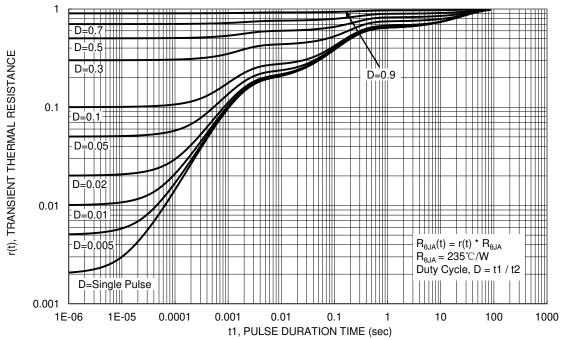


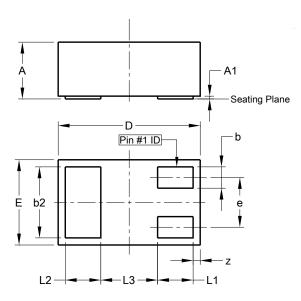
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3

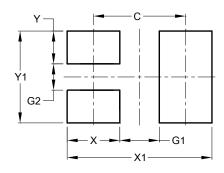


X2-DFN1006-3				
Dim	Min	Max	Тур	
Α	ı	0.40	-	
A 1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.05	1.00	
Е	0.55	0.65	0.60	
е	ı	ı	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	-	-	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Υ	0.25
V1	0.70

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