



DMN2005UFG

20V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>C</sub> = +25°C (Note 9)		
20V	$4.6m\Omega @ V_{GS} = 4.5V$	50A		
	$8.7 m\Omega @ V_{GS} = 2.5 V$	36A		

#### Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

## Applications

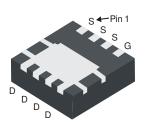
- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

#### **Features and Benefits**

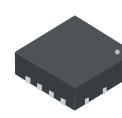
- Low R<sub>DS(ON)</sub> ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- 100% UIS & Rg tested
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMN2005UFGQ)

#### **Mechanical Data**

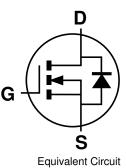
- Case: PowerDI<sup>®</sup>3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)



Bottom View



Top View



#### Ordering Information (Note 4)

Part Number		Case	Packaging		
DMN2005UFG-7		PowerDI3333-8 2,000/Tape & R			
DMN2005UFG-13		PowerDI3333-8	3,000/Tape & Reel		
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (BoHS) 2011/65/EU (BoHS 2) & 2015/863/EU (BoHS 3) compliant					

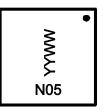
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

#### Marking Information



PowerDI3333-8

N05= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 19 = 2019) WW = Week Code (01 to 53)



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	20	V		
Gate-Source Voltage	V <sub>GSS</sub>	±12	V		
	= 4.5V Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	ID	50 40	A
Continuous Drain Current (Notes 6 & 9) $V_{GS}$ = 4.5V		$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	18 14	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	130	A		
Maximum Continuous Body Diode Forward Current (N	ls	2.6	А		
Avalanche Current , L = 0.2mH	I <sub>AS</sub>	23.9	A		
Repetitive Avalanche Energy, L = 0.2mH	E <sub>AS</sub>	58.4	mJ		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.05	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	120	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	2.27	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		$R_{ heta JA}$	55	°C/W
Thermal Resistance, Junction to Case (Note 6)	$R_{ ext{ heta}JC}$	4.2	°C/W	
Operating and Storage Temperature Range	T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

Notes:

Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

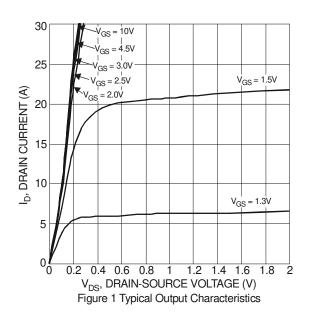


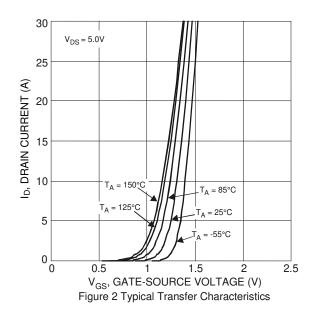
#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	—	10	μΑ	$V_{DS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS		—	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	·		•	•			
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.4	0.7	1.2	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Statia Duaia Cauraa On Daaiatanaa	D	_	4	4.6		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 13.5A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		4.9	8.7	mΩ	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 13.5A	
Diode Forward Voltage	V <sub>SD</sub>		0.8	1.1	V	$V_{GS} = 0V, I_{S} = 27A$	
DYNAMIC CHARACTERISTICS (Note 8)	·		•	•			
Input Capacitance	C <sub>iss</sub>	_	6,495	_	pF		
Output Capacitance	C <sub>oss</sub>	_	546	_	pF	<sup>−</sup> V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, −f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	477	_	pF		
Gate Resistance	Rg		0.7	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	68.8	_	nC		
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	_	164	_	nC		
Gate-Source Charge	Q <sub>qs</sub>		10.4	_	nC	$-V_{DS} = 16V, I_D = 27A$	
Gate-Drain Charge	Q <sub>gd</sub>		17.4	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	12.4	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	25.7	_	ns	V <sub>GS</sub> = 5V, V <sub>DS</sub> = 10V,	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	114	_	ns	$R_G = 4.7\Omega, I_D = 13.5A$	
Turn-Off Fall Time	tF	—	38	_	ns	<u> </u>	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	16.1		ns	$I_F = 13.5A$ , di/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	8.5	_	nC	I <sub>F</sub> = 13.5A, di/dt = 100A/µs	

 Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing. Notes:

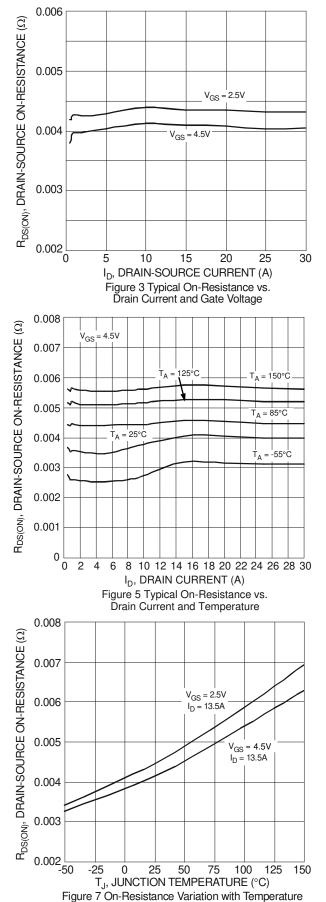
9. Limited by package.

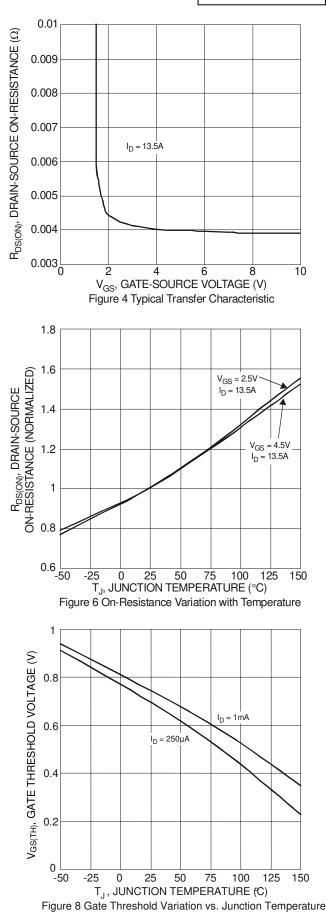






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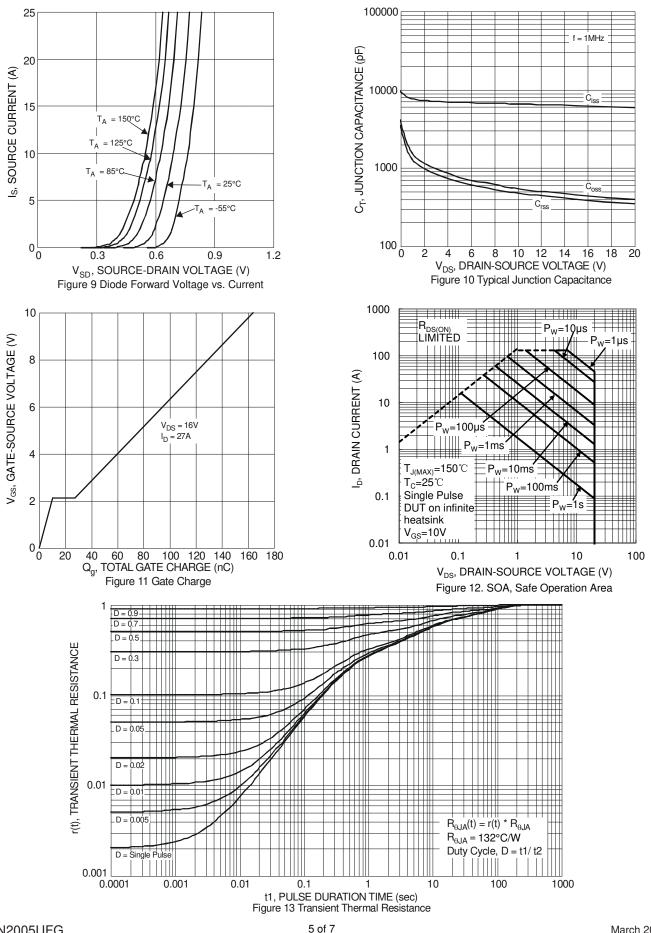




DMN2005UFG Document number: DS36943 Rev. 4 - 2



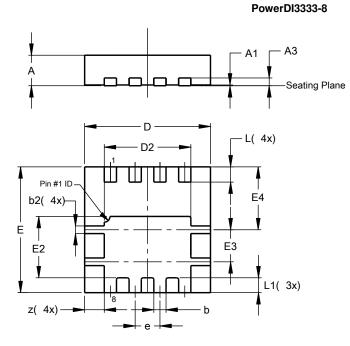
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#### **Package Outline Dimensions**

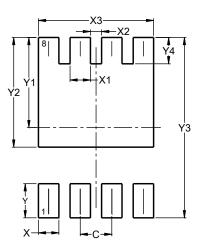
Please see http://www.diodes.com/package-outlines.html for the latest version.



	PowerDI3333-8					
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05	0.02			
A3	-	-	0.203			
b	0.27	0.37	0.32			
b2	0.15	0.25	0.20			
D	3.25	3.35	3.30			
D2	2.22	2.32	2.27			
Е	3.25	3.35	3.30			
E2	1.56	1.66	1.61			
E3	0.79	0.89	0.84			
E4	1.60	1.70	1.65			
е	-	-	0.65			
L	0.35	0.45	0.40			
L1	_	-	0.39			
z	-	-	0.515			
All Dimensions in mm						

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



#### Dimensions Value (in mm) С 0.650 Х 0.420 X1 0.420 X2 0.230 Х3 2.370 V 0.700 Y1 1.850 2.250 Y2 Y3 3.700 0.540 **Y**4

#### PowerDI3333-8



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