



DDTC (R1 = R2 SERIES) EE

NPN PRE-BIASED TRANSISTOR IN SOT523

Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors, R1 = R2
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

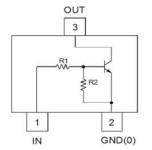
- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 <a>®
- Weight: 0.002 grams (Approximate)

Part Number	R1, R2 (NOM)
DDTC123EE	2.2kΩ
DDTC143EE	4.7kΩ
DDTC114EE	10kΩ
DDTC124EE	22kΩ
DDTC144EE	47kΩ
DDTC115EE	100kΩ

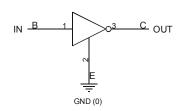








Device Schematic



Equivalent Inverter Circuit

Ordering Information (Note 4)

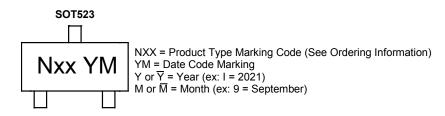
Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DDTC123EE-7-F	Standard	N04	7	8	3000
DDTC143EE-7-F	Standard	N08	7	8	3000
DDTC114EE-7-F	Standard	N13	7	8	3000
DDTC124EE-7-F	Standard	N17	7	8	3000
DDTC144EE-7-F	Standard	N20	7	8	3000
DDTC115EE-7-F	Standard	N24	7	8	3000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



Marking Information



Date Code Key

Year	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code		J	K	L	М	N	0	Р	R	S	Т	U
	ı	ı	1					1		1	1	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Absolute Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Chara	cteristic	Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>	•	V _{CC}	50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	DDTC123EE DDTC143EE DDTC114EE DDTC124EE DDTC144EE DDTC115EE	Vı	-10 to +12 -10 to +30 -10 to +40 -10 to +40 -10 to +40 -10 to +40	٧
Output Current	DDTC123EE DDTC143EE DDTC114EE DDTC124EE DDTC144EE DDTC115EE	lo	100 100 50 30 100 20	mA
Output Current		I _C (Max)	100	mA

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 5 & 6)	P_{D}	150	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{ heta JA}$	833	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 5. Mounted on FR-4 PC Board with minimum recommended pad layout.

6. 150mW per element must not be exceeded.



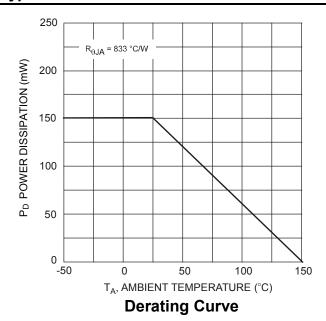
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

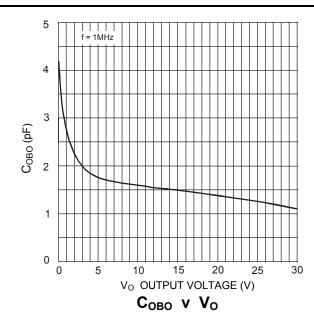
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
		V _{I(off)} (Note 7)	0.5	1.1	_		$V_{CC} = 5V, I_{O} = 100\mu A$
Input Voltage		V _{I(on)} (Note 8)		1.9	3	٧	$\begin{array}{l} V_O = 0.3 V, \ I_O = 20 mA, \ DDTC123EE \\ V_O = 0.3 V, \ I_O = 20 mA, \ DDTC143EE \\ V_O = 0.3 V, \ I_O = 10 mA, \ DDTC114EE \\ V_O = 0.3 V, \ I_O = 5 mA, \ DDTC124EE \\ V_O = 0.3 V, \ I_O = 2 mA, \ DDTC144EE \\ V_O = 0.3 V, \ I_O = 1 mA, \ DDTC115EE \\ \end{array}$
Output Voltage		V _{O(on)}	l	0.1	0.3	٧	$I_O/I_I = 10$ mA/0.5mA, DDTC123EE $I_O/I_I = 10$ mA/0.5mA, DDTC143EE $I_O/I_I = 10$ mA/0.5mA, DDTC114EE $I_O/I_I = 10$ mA/0.5mA, DDTC124EE $I_O/I_I = 10$ mA/0.5mA, DDTC144EE $I_O/I_I = 5$ mA/0.25mA, DDTC115EE
Input Current	DDTC123EE DDTC143EE DDTC114EE DDTC124EE DDTC144EE DDTC115EE	Iı	_	_	3.8 1.8 0.88 0.36 0.18 0.15	mA	V _I = 5V
Output Current		I _{O(off)}	_	_	0.5	μΑ	V _{CC} = 50V, V _I = 0V
DC Current Gain	DDTC123EE DDTC143EE DDTC114EE DDTC124EE DDTC144EE DDTC115EE	Gį	20 20 30 56 68 82	_	_	_	$V_O = 5V$, $I_O = 20$ mA $V_O = 5V$, $I_O = 10$ mA $V_O = 5V$, $I_O = 5$ mA
Input Resistor Tolerance	Input Resistor Tolerance		-30	_	+30	%	_
Resistance Ratio Tolerand	ce	$\Delta R_2/R_1$	0.8	1	1.2	%	_
Transition frequency (Note	9)	f _T		250	_	MHz	V _{CE} = -10V, I _E = 5mA, f = 100MHz

Notes:

- 7. Guarantees that the device will be switched OFF if the Input Voltage is less than 0.5V. 8. Guarantees that the device will be switched ON if the Input Voltage is more than 3V. 9. Transistor only.

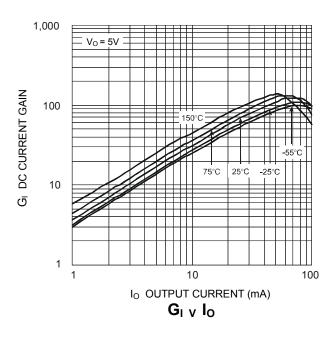
Typical Electrical Characteristics

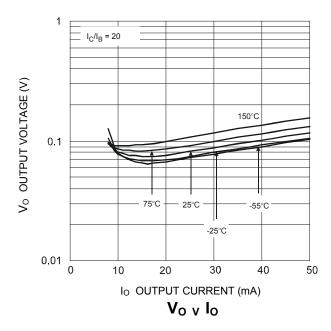


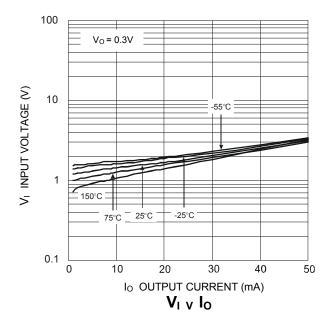




Typical Electrical Characteristics - DDTC123EE

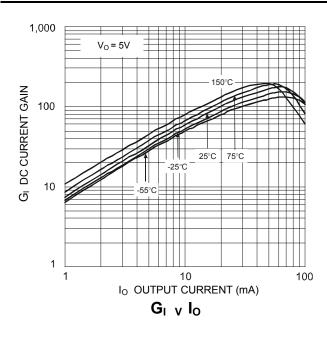


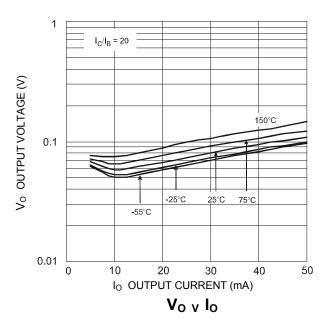


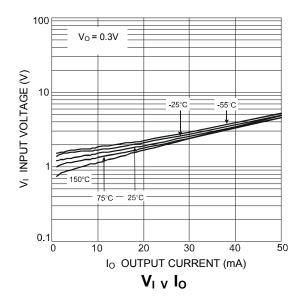




Typical Electrical Characteristics - DDTC143EE

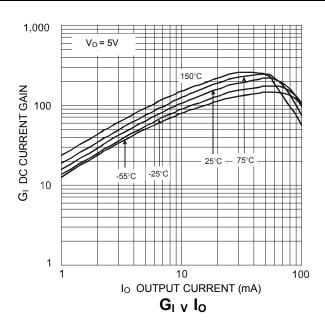


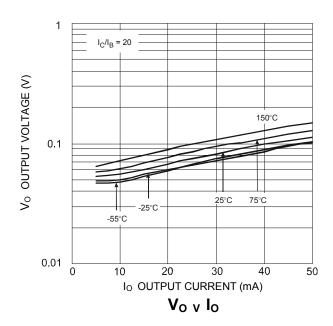


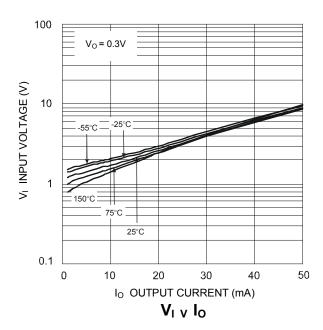




Typical Electrical Characteristics - DDTC114EE

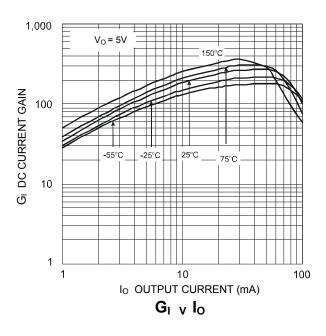


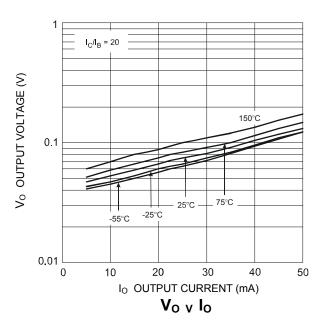


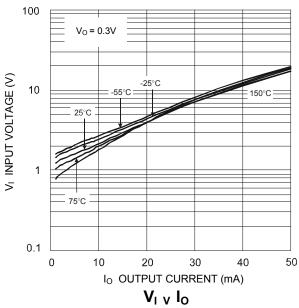




Typical Electrical Characteristics - DDTC124EE

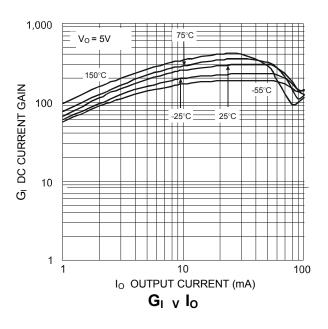


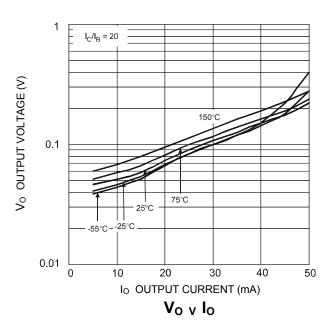


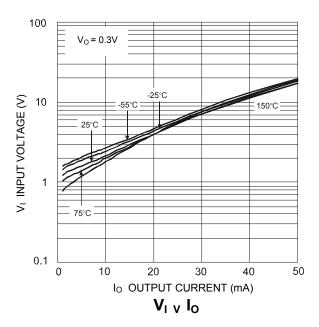




Typical Electrical Characteristics - DDTC144EE





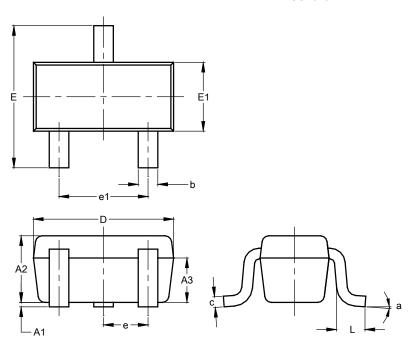




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

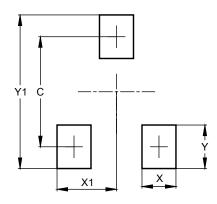


SOT523					
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.60	0.80	0.75		
A 3	0.45	0.65	0.50		
b	0.15	0.30	0.22		
С	0.10	0.20	0.12		
D	1.50	1.70	1.60		
Е	1.45	1.75	1.60		
E1	0.75	0.85	0.80		
е	0.50 BSC				
e1	0.90	1.10	1.00		
L	0.20	0.40	0.33		
а	0°		8°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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