

## High Speed IGBT3 Chip

### Features:

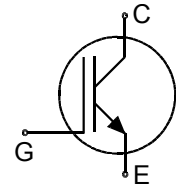
- 650V Trench & Field Stop technology
- high speed switching series third generation
- low  $V_{CE(sat)}$
- low EMI
- low turn-off losses
- positive temperature coefficient
- qualified according to JEDEC for target applications

### Recommended for:

- discrete components and modules

### Applications:

- uninterruptible power supplies
- welding converters
- converters with high switching frequency



Chip Type	$V_{CE}$	$I_{Cn}^{1)}$	Die Size	Package
IGC10T65QE	650V	20A	3.19 x 3.21mm <sup>2</sup>	sawn on foil

<sup>1)</sup> nominal collector current at  $T_c = 100^\circ\text{C}$ , not subject to production test - verified by design/characterization

### Mechanical Parameters

Die size	3.19 x 3.21		mm <sup>2</sup>
Emitter pad size	See chip drawing		
Gate pad size	0.361 x 0.513		
Area total	10.24		
Thickness	70		µm
Wafer size	200		mm
Max.possible chips per wafer	2693		
Passivation frontside	Photoimide		
Pad metal	3200 nm AlSiCu		
Backside metal	Ni Ag –system		
Die bond	Electrically conductive epoxy glue and soft solder		
Wire bond	Al, <500µm		
Reject ink dot size	Ø 0.65mm ; max 1.2mm		
Storage environment	for original and sealed MBB bags	Ambient atmosphere air, Temperature 17°C – 25°C, < 6 month	
	for open MBB bags	Acc. to IEC62258-3: Atmosphere >99% Nitrogen or inert gas, Humidity <25%RH, Temperature 17°C – 25°C, < 6 month	



# IGC10T65QE

## Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-Emitter voltage, $T_{vj} = 25\text{ °C}$	$V_{CE}$	650	V
DC collector current, limited by $T_{vj\text{ max}}$	$I_C$	1)	A
Pulsed collector current, $t_p$ limited by $T_{vj\text{ max}}$ 2)	$I_{C,puls}$	60	A
Gate emitter voltage	$V_{GE}$	$\pm 20$	V
Operating junction temperature	$T_{vj}$	-40 ... +175	°C
Short circuit data 2) 3) $V_{GE} = 15V, V_{CC} = 400V, T_{vj} = 150\text{ °C}$	$t_{SC}$	5	$\mu s$

1) depending on thermal properties of assembly

2) not subject to production test - verified by design/characterization

3) allowed number of short circuits: <1000; time between short circuits: >1s.

## Static Characteristics (tested on wafer), $T_{vj} = 25\text{ °C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-Emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_C=2\text{ mA}$	650			V
Collector-Emitter saturation voltage	$V_{CEsat}$	$V_{GE}=15V, I_C=20A$	1.48	1.95	2.32	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$I_C=0.29mA, V_{GE}=V_{CE}$	4.2	5.1	5.6	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=650V, V_{GE}=0V$			1	$\mu A$
Gate-Emitter leakage current	$I_{GES}$	$V_{CE}=0V, V_{GE}=20V$			150	nA
Integrated gate resistor	$r_G$			none		$\Omega$

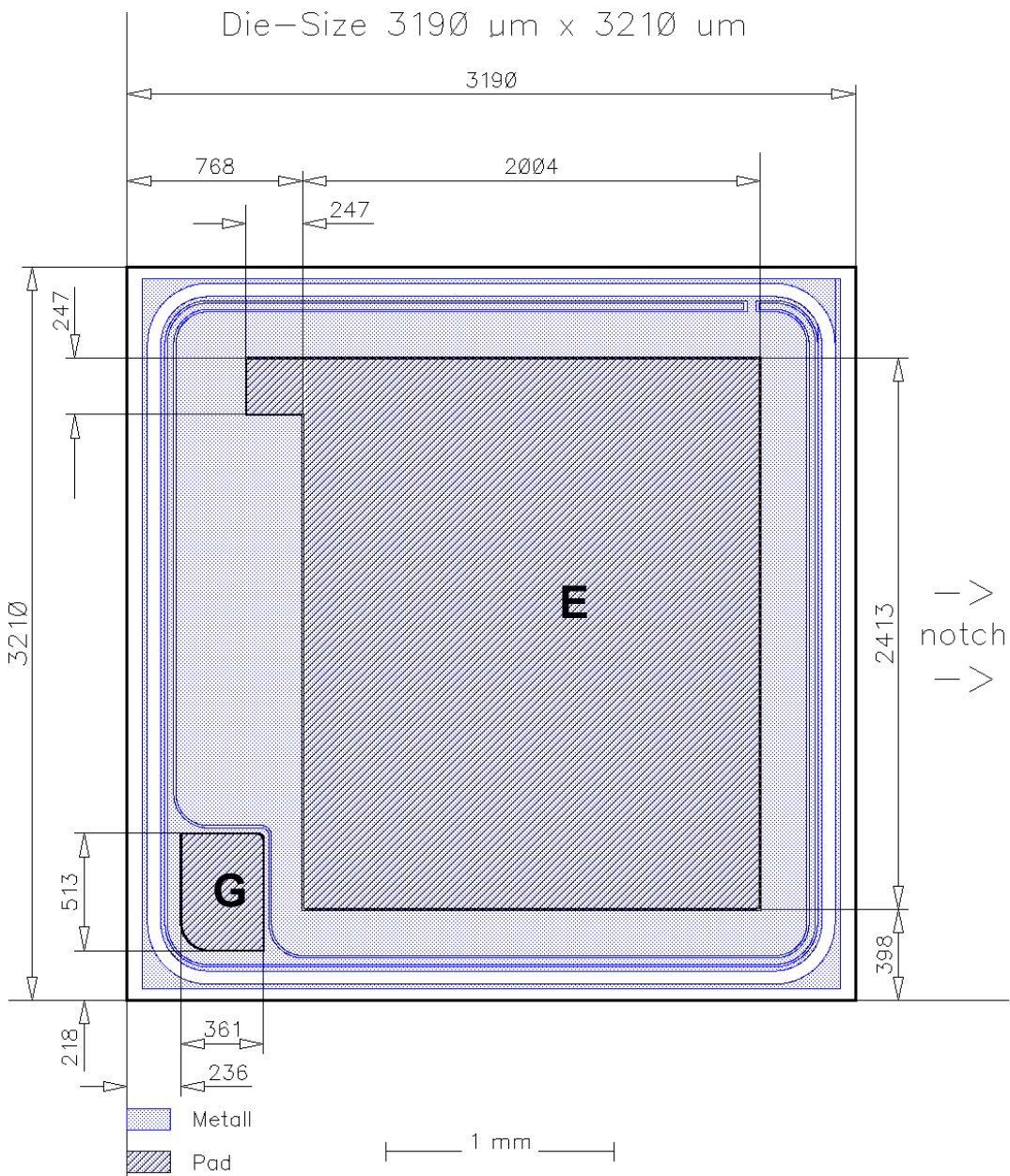
## Electrical Characteristics (not subject to production test - verified by design / characterization)

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Collector-Emitter saturation voltage	$V_{CEsat}$	$V_{GE}=15V, I_C=20A, T_{vj}=175\text{ °C}$		2.5		V
Input capacitance	$C_{ies}$	$V_{CE}=25V, V_{GE}=0V, f=1\text{ MHz}$		1250		pF
Reverse transfer capacitance	$C_{res}$	$T_{vj}=25\text{ °C}$		40		

## Further Electrical Characteristic

Switching characteristics and thermal properties are depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

## Chip Drawing



**E** = Emitter

**G** = Gate



# IGC10T65QE

## Description

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

## Revision History

Version	Subjects (major changes since last revision)	Date

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