

Vishay Siliconix

P-Channel 30 V (D-S) MOSFET



PRODUCT SUMMARY						
V _{DS} (V)	-30					
$R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V	0.0240					
$R_{DS(on)}$ max. (Ω) at $V_{GS} = -4.5 \text{ V}$	0.0350					
Q _g typ. (nC)	15					
I _D (A) ^d	-11.4					
Configuration	Single					

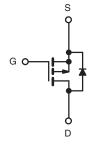
FEATURES

- TrenchFET® power MOSFET
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



APPLICATIONS

- · Load switches
- · Battery switch



P-Channel MOSFET

ORDERING INFORMATION				
Package	SO-8			
Lead (Pb)-free	Si4435DDY-T1-E3			
Lead (Pb)-free and halogen-free	Si4435DDY-T1-GE3			

ABSOLUTE MAXIMUM RATINGS (TA	= 25 °C, unless other	wise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	-30	V	
Gate-source voltage			± 20	V	
	T _C = 25 °C		-11.4		
Continuous durin august (T. 150 °C)	T _C = 70 °C	1 .	-9.1		
Continuous drain current (T _J = 150 °C)	T _A = 25 °C	- I _D	-8.1 ^{a, b}		
	T _A = 70 °C		-6.5 ^{a, b}		
Pulsed drain current	I _{DM}	-50	А		
Continuous source drain diada surrent	T _C = 25 °C	I _S	-4.1		
Continuous source-drain diode current	T _A = 25 °C		-2 a, b		
Avalanche current		I _{AS}	-20		
Single-pulse avalanche energy	L = 0.1 mH	E _{AS}	20	mJ	
	T _C = 25 °C		5		
Maximum power dissipation	T _C = 70 °C		3.2	14/	
	T _A = 25 °C	P _D	2.5 ^{a, b}	- W	
	T _A = 70 °C		1.6 ^{a, b}		
Operating junction and storage temperature range	T _J , T _{stg}	-55 to +150	°C		

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient a, c	t ≤ 10 s	R_{thJA}	38	50	°C/W
Maximum junction-to-foot	Steady state	R_{thJF}	20	25	C/ VV

Notes

- a. Surface mounted on 1" x 1" FR4 board
- b. t = 10 s
- c. Maximum under steady state conditions is 85 °C/W
- d. Based on $T_C = 25\ ^{\circ}C$

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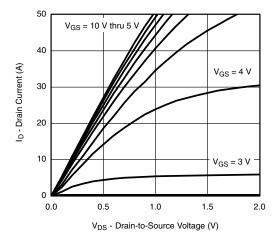
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	/T _J		-31	-	mV/°C	
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$			4.5	-		
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1	-	-3	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zana and a substantial and a s	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V	-	-	-1	μА	
Zero gate voltage drain current		V _{DS} = -30 V, V _{GS} = 0 V, T _J = 55 °C	-	-	-5		
On-state drain current ^a	I _{D(on)}	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	-30	-	-	Α	
Drain activas an atata vasiatanas 3	В	V _{GS} = -10 V, I _D = -9.1 A	-	0.0195	0.0240	Ω	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = -4.5 V, I _D = -6.9 A	-	0.0280	0.0350		
Forward transconductance ^a	9 _{fs}	V _{DS} = -10 V, I _D = -9.1 A	-	23	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	1350	-	pF	
Output capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	215	-		
Reverse transfer capacitance	C _{rss}		-	185	-		
	Q _g —	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -9.1 \text{ A}$	-	32	50		
Total gate charge			-	15	25		
Gate-source charge	Q _{qs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -9.1 \text{ A}$	-	4	-	nC	
Gate-drain charge	Q _{gd}		-	7.5	-		
Gate resistance	R _q	f = 1 MHz	-	5.8	-	Ω	
Turn-on delay time	t _{d(on)}		-	10	15		
Rise time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$	-	8	15		
Turn-off delay time	t _{d(off)}	$I_D\cong$ -1 A, $V_{GEN}=$ -10 V, $R_g=$ 1 Ω	-	45	70		
Fall time	t _f		-	12	25		
Turn-on delay time	t _{d(on)}		-	42	70	ns	
Rise time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$	-	35	60		
Turn-off delay time	t _{d(off)}	$I_D \cong -1$ A, $V_{GEN} = -4.5$ V, $R_g = 1$ Ω	-	40	70		
Fall time	t _f		-	16	30		
Drain-Source Body Diode Characteris	tics						
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-4.1		
Pulse diode forward current	I _{SM}		-	-	-50	A	
Body diode voltage	V _{SD}	I _S = -2 A, V _{GS} = 0 V	-	-0.75	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	34	60	ns	
Body diode reverse recovery charge	Q _{rr}	l _F = -2 A, di/dt = 100 A/μs,	-	22	40	nC	
Reverse recovery fall time	t _a	$T_{J} = 25 ^{\circ}\text{C}$	-	11	-	-	
Reverse recovery rise time	t _b	1	_	23	_	ns	

Notes

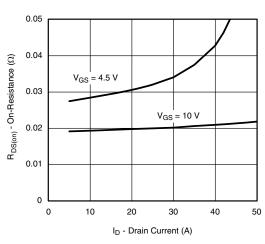
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

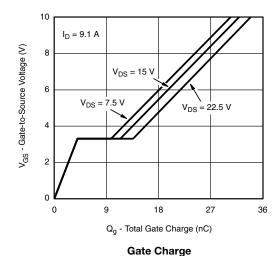


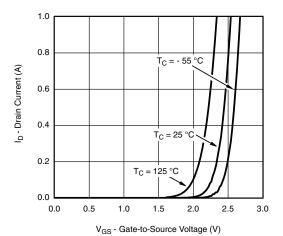


Output Characteristics

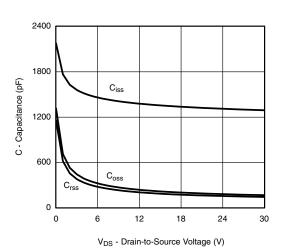


On-Resistance vs. Drain Current

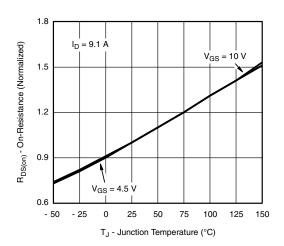




Transfer Characteristics

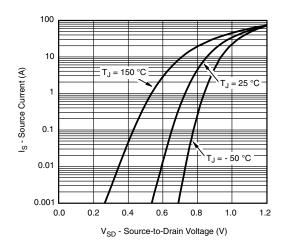


Capacitance

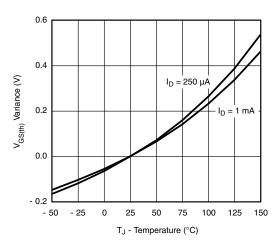


On-Resistance vs. Junction Temperature

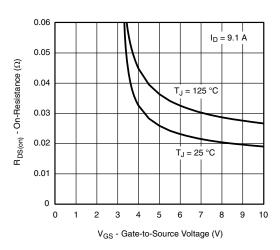




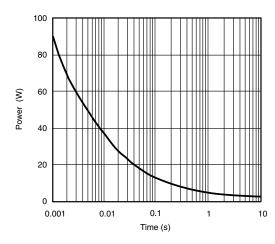
Source-Drain Diode Forward Voltage



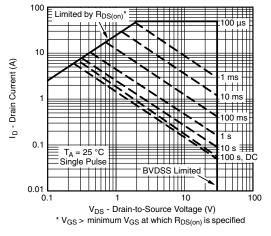
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

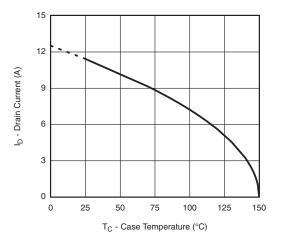


Single Pulse Power, Junction-to-Ambient

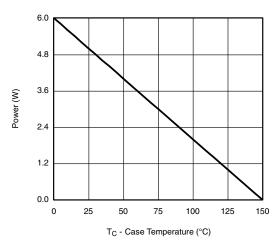


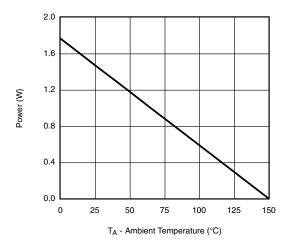
Safe Operating Area





Current Derating a





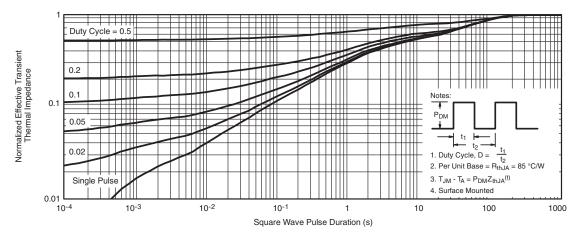
Power, Junction-to-Foot

Power Derating, Junction-to-Ambient

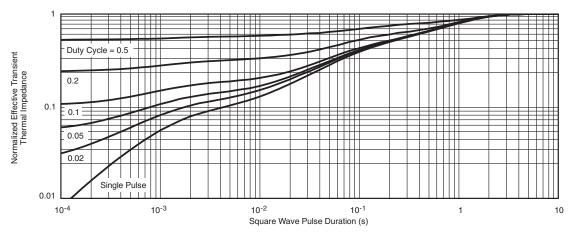
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient



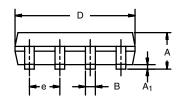
Normalized Thermal Transient Impedance, Junction-to-Foot

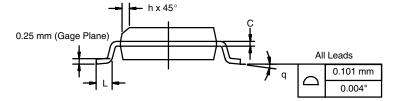
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

APPLICATION NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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