

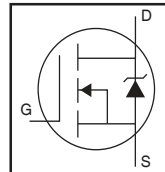
### Features

- Advanced Process Technology
- Logic Level Gate Drive
- 175°C Operating Temperature
- Fast Switching
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified \*

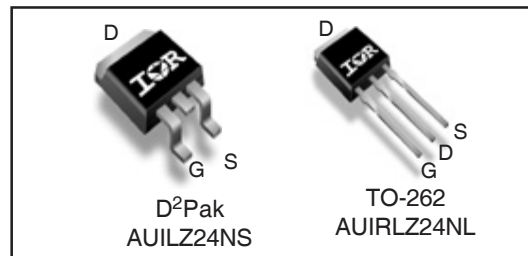
### Description

Specifically designed for Automotive applications, this HEXFET® Power MOSFET utilizes the latest processing techniques to achieve extremely low on-resistance per silicon area. Additional features of this design are a 175°C junction operating temperature, fast switching speed and improved repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in Automotive applications and a wide variety of other applications.

### HEXFET® Power MOSFET



$V_{DSS}$	<b>55V</b>
$R_{DS(on)}$ max.	<b>0.06Ω</b>
$I_D$	<b>18A</b>



Base Part Number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
AUIRLZ24NS	D2-Pak	Tube	50	AUIRLZ24NS
		Tape and Reel Left	800	AUIRLZ24NSTR
AUIRLZ24NL	TO-262	Tube	50	AUIRLZ24NL

### Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature ( $T_A$ ) is 25°C, unless otherwise specified.

	Parameter	Max.	Units
$I_D$ @ $T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ⑤	18	A
$I_D$ @ $T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ⑤	13	
$I_{DM}$	Pulsed Drain Current ①⑤	72	
$P_D$ @ $T_A = 25^\circ\text{C}$	Maximum Power Dissipation	3.8	W
$P_D$ @ $T_C = 25^\circ\text{C}$	Maximum Power Dissipation	45	W
	Linear Derating Factor	0.30	W/°C
$V_{GS}$	Gate-to-Source Voltage	± 16	V
$E_{AS}$	Single Pulse Avalanche Energy ②⑤	68	mJ
$I_{AR}$	Avalanche Current ①	11	A
$E_{AR}$	Repetitive Avalanche Energy ①	4.5	mJ
dv/dt	Peak Diode Recovery ③⑤	5.0	V/ns
$T_J$	Operating Junction and	-55 to + 175	°C
$T_{STG}$	Storage Temperature Range		
	Soldering Temperature, for 10 seconds (1.6mm from case)	300	

### Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	—	3.3	°C/W
$R_{\theta JA}$	Junction-to-Ambient (PCB Mounted, steady-state)**	—	40	

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\*Qualification standards can be found at <http://www.irf.com/>

**Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)**

	Parameter	Min.	Typ.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	55	---	---	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
ΔV <sub>(BR)DSS/ΔT<sub>J</sub></sub>	Breakdown Voltage Temp. Coefficient	---	0.061	---	V/°C	Reference to 25°C, I <sub>D</sub> = 1mA <sup>③</sup>
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance	---	---	0.060	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 11A <sup>④</sup>
		---	---	0.075		V <sub>GS</sub> = 5.0V, I <sub>D</sub> = 11A <sup>④</sup>
		---	---	0.105		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 9.0A <sup>④</sup>
V <sub>GS(th)</sub>	Gate Threshold Voltage	1.0	---	2.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
g <sub>fs</sub>	Forward Transconductance	8.3	---	---	S	V <sub>DS</sub> = 25V, I <sub>D</sub> = 11A <sup>⑤</sup>
I <sub>DSS</sub>	Drain-to-Source Leakage Current	---	---	25	μA	V <sub>DS</sub> = 55V, V <sub>GS</sub> = 0V
		---	---	250		V <sub>DS</sub> = 44V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 150°C
I <sub>GSS</sub>	Gate-to-Source Forward Leakage	---	---	100	nA	V <sub>GS</sub> = 16V
	Gate-to-Source Reverse Leakage	---	---	-100		V <sub>GS</sub> = -16V
Q <sub>g</sub>	Total Gate Charge	---	---	15	nC	I <sub>D</sub> = 11A
Q <sub>gs</sub>	Gate-to-Source Charge	---	---	3.7		V <sub>DS</sub> = 44V
Q <sub>gd</sub>	Gate-to-Drain ("Miller") Charge	---	---	8.5		V <sub>GS</sub> = 5.0V, See Fig. 6 and 13 <sup>④⑤</sup>
t <sub>d(on)</sub>	Turn-On Delay Time	---	7.1	---	ns	V <sub>DD</sub> = 28V
t <sub>r</sub>	Rise Time	---	74	---		I <sub>D</sub> = 11A
t <sub>d(off)</sub>	Turn-Off Delay Time	---	20	---		R <sub>G</sub> = 12Ω, V <sub>GS</sub> = 5.0V
t <sub>f</sub>	Fall Time	---	29	---		R <sub>D</sub> = 2.4Ω, See Fig. 10 <sup>④⑤</sup>
L <sub>S</sub>	Internal Source Inductance	---	7.5	---	nH	Between lead, and center of die contact
C <sub>iss</sub>	Input Capacitance	---	480	---	pF	V <sub>GS</sub> = 0V
C <sub>oss</sub>	Output Capacitance	---	130	---		V <sub>DS</sub> = 25V
C <sub>rss</sub>	Reverse Transfer Capacitance	---	61	---		f = 1.0MHz, See Fig. 5 <sup>⑤</sup>

**Source-Drain Ratings and Characteristics**

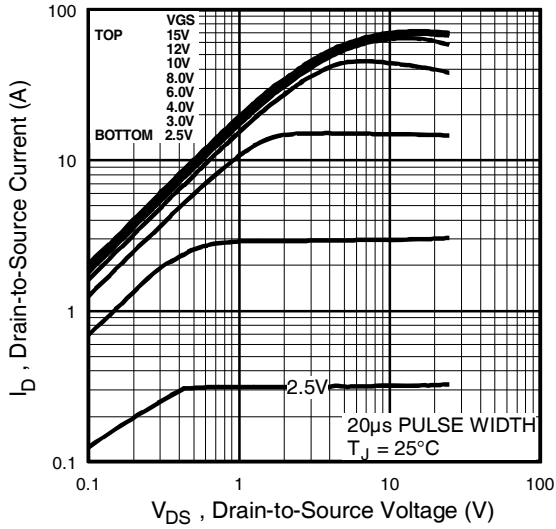
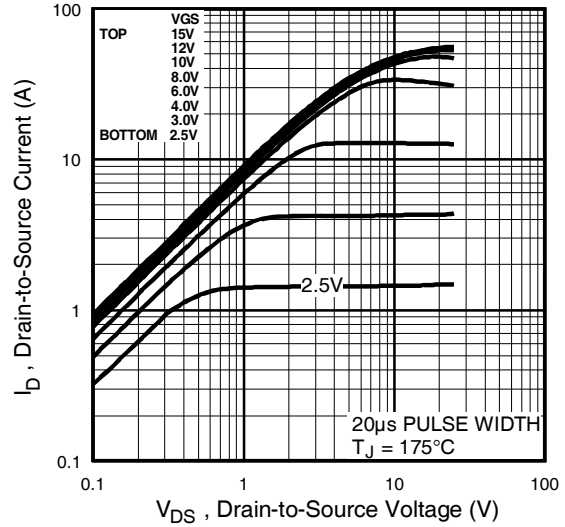
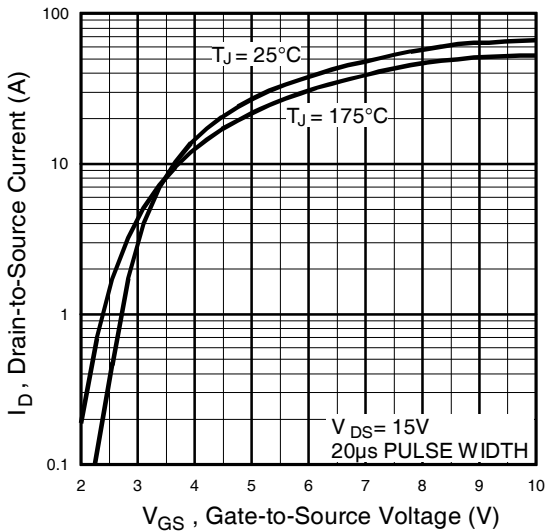
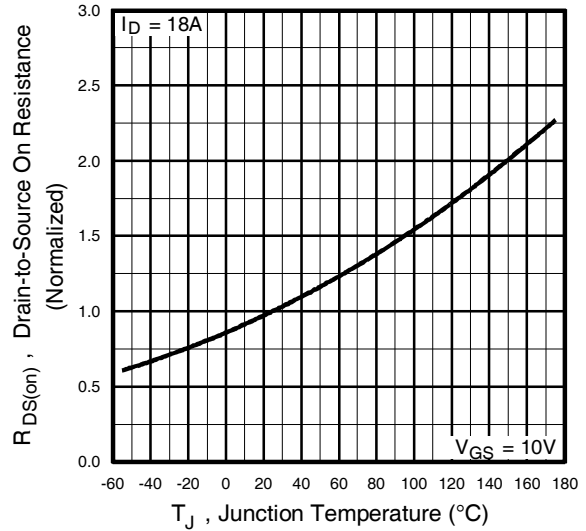
	Parameter	Min.	Typ.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	---	---	18	A	MOSFET symbol showing the integral reverse p-n junction diode.
I <sub>SM</sub>	Pulsed Source Current (Body Diode) <sup>①</sup>	---	---	72		
V <sub>SD</sub>	Diode Forward Voltage	---	---	1.3	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = 11A, V <sub>GS</sub> = 0V <sup>④</sup>
t <sub>rr</sub>	Reverse Recovery Time	---	60	90	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = 11A
Q <sub>rr</sub>	Reverse Recovery Charge	---	130	200	nC	di/dt = 100A/μs <sup>④⑤</sup>
t <sub>on</sub>	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L <sub>S</sub> +L <sub>D</sub> )				

**Notes**

- ① Repetitive rating; pulse width limited by max. junction temperature. ( See fig. 11 )
- ② V<sub>DD</sub> = 25V, starting T<sub>J</sub> = 25°C, L = 790μH, R<sub>G</sub> = 25Ω, I<sub>AS</sub> = 11A. (See Figure 12)
- ③ I<sub>SD</sub> ≤ 11A, di/dt ≤ 290A/μs, V<sub>DD</sub> ≤ V<sub>(BR)DSS</sub>, T<sub>J</sub> ≤ 175°C
- ④ Pulse width ≤ 300μs; duty cycle ≤ 2%.
- ⑤ Uses IRLZ24N data and test conditions.

\*\* When mounted on 1" square PCB ( FR-4 or G-10 Material ).

For recommended footprint and soldering techniques refer to application note #AN-994.


**Fig 1. Typical Output Characteristics**

**Fig 2. Typical Output Characteristics**

**Fig 3. Typical Transfer Characteristics**

**Fig 4. Normalized On-Resistance Vs. Temperature**

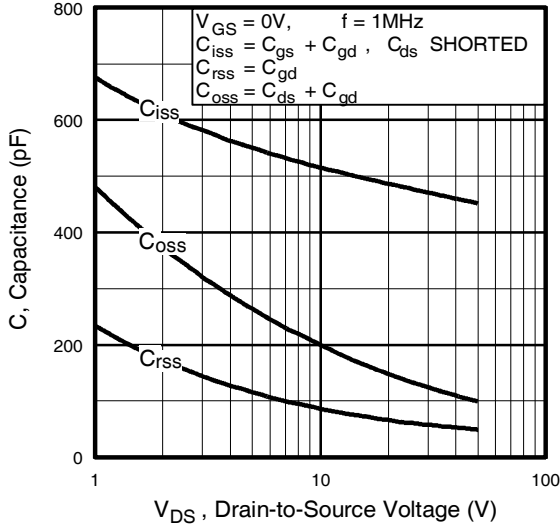


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

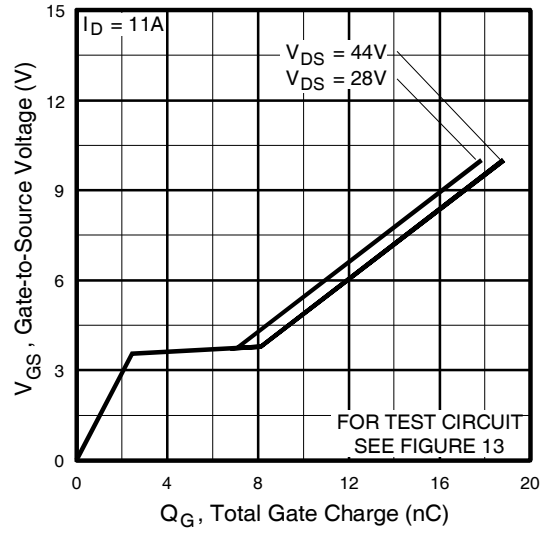


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

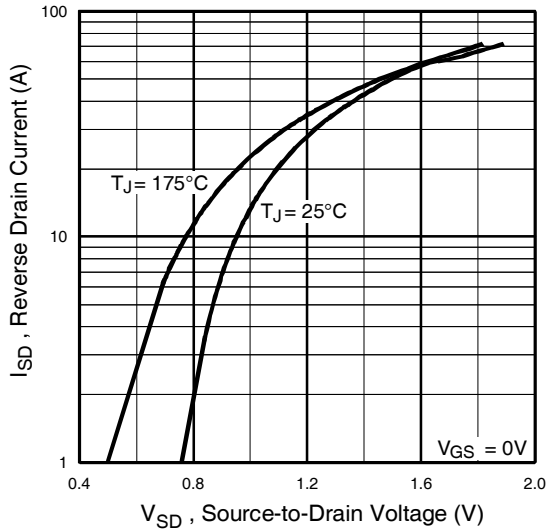


Fig 7. Typical Source-Drain Diode

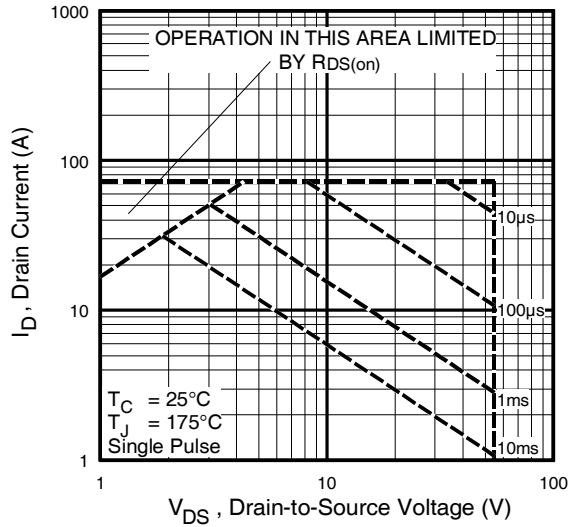
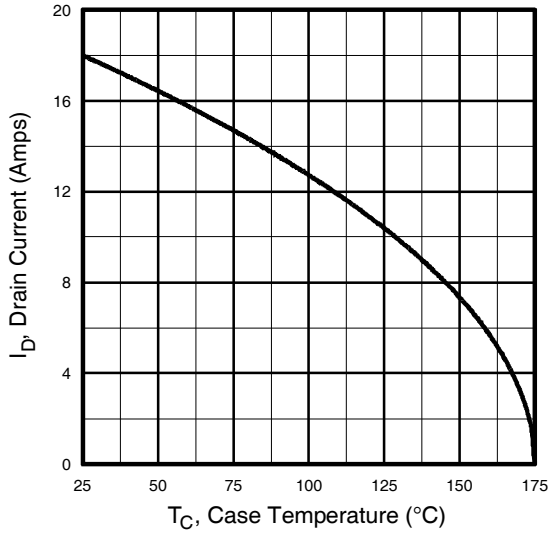
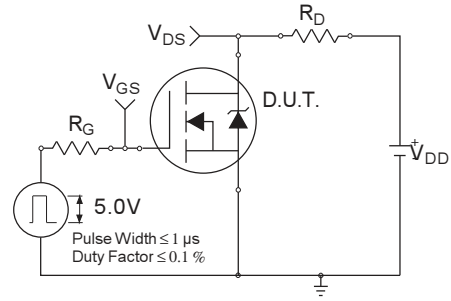


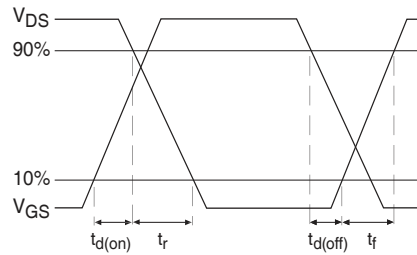
Fig 8. Maximum Safe Operating Area



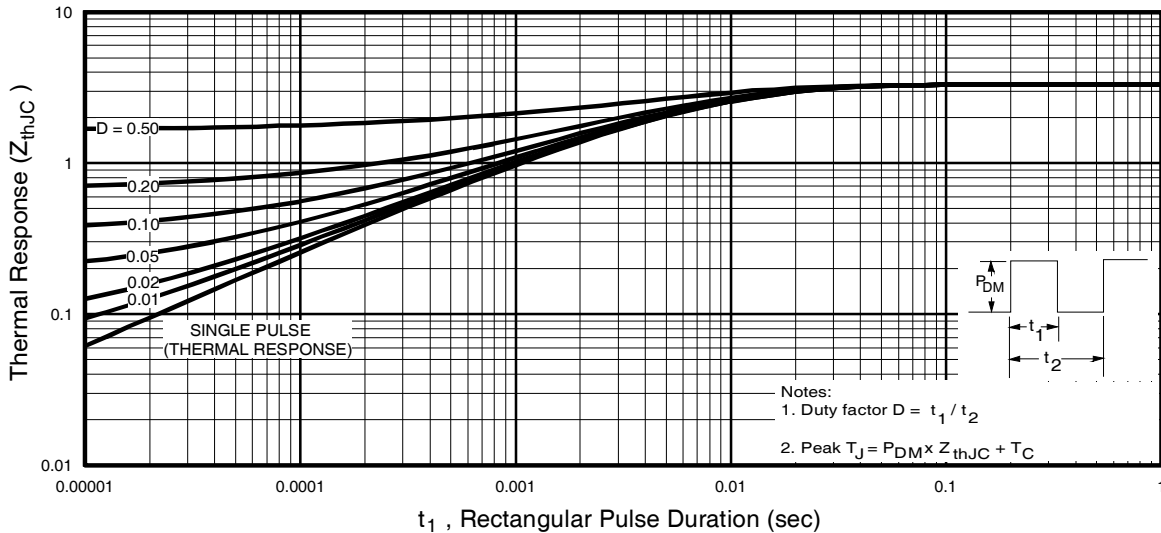
**Fig 9.** Maximum Drain Current Vs. Case Temperature



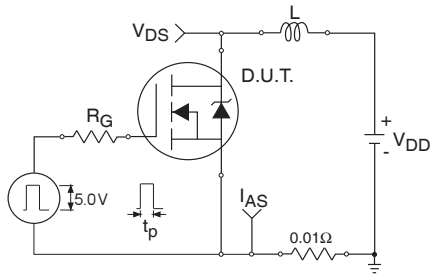
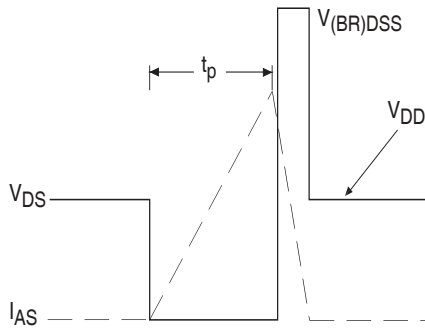
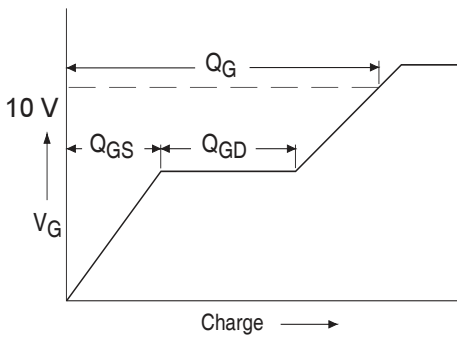
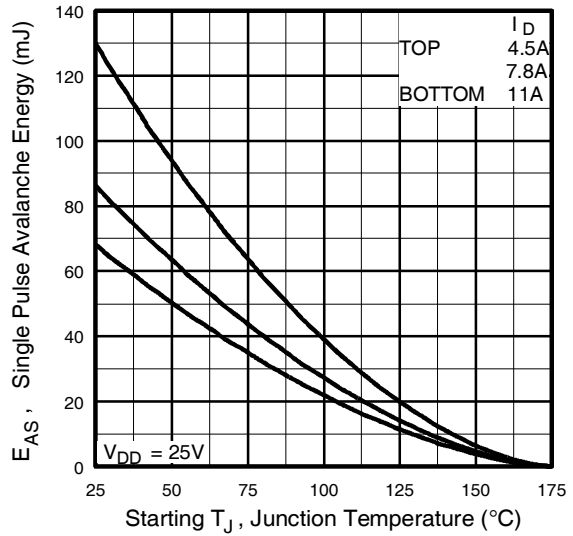
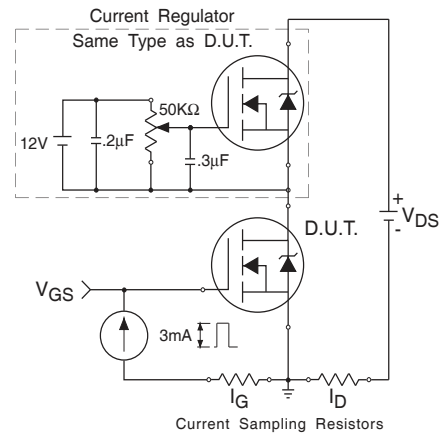
**Fig 10a.** Switching Time Test Circuit



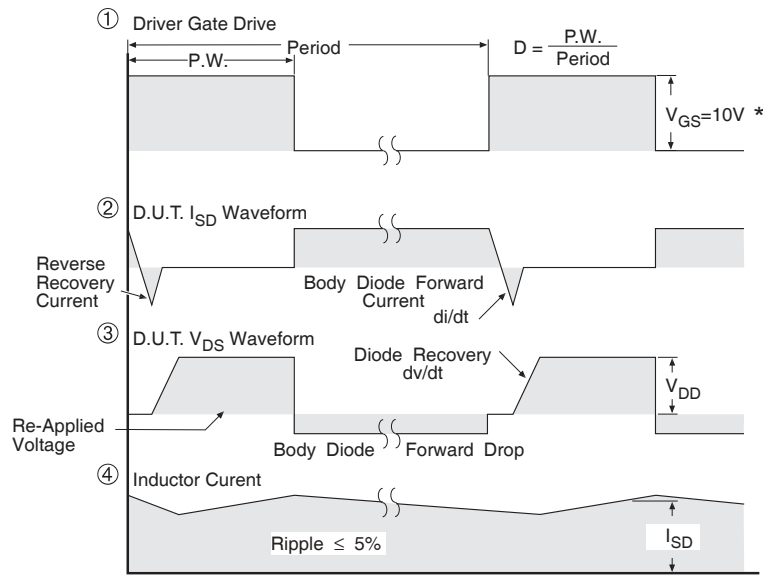
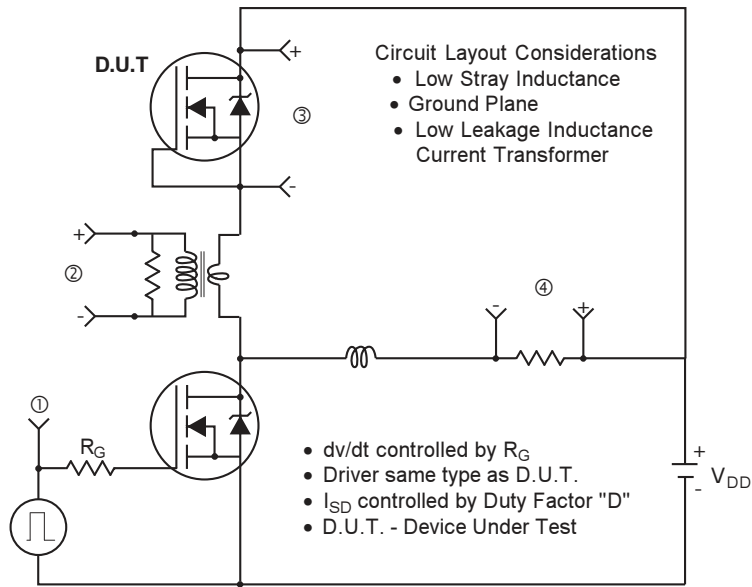
**Fig 10b.** Switching Time Waveforms



**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case


**Fig 12a. Unclamped Inductive Test Circuit**

**Fig 12b. Unclamped Inductive Waveforms**

**Fig 13a. Basic Gate Charge Waveform**

**Fig 12c. Maximum Avalanche Energy Vs. Drain Current**

**Fig 13b. Gate Charge Test Circuit**

### Peak Diode Recovery dv/dt Test Circuit

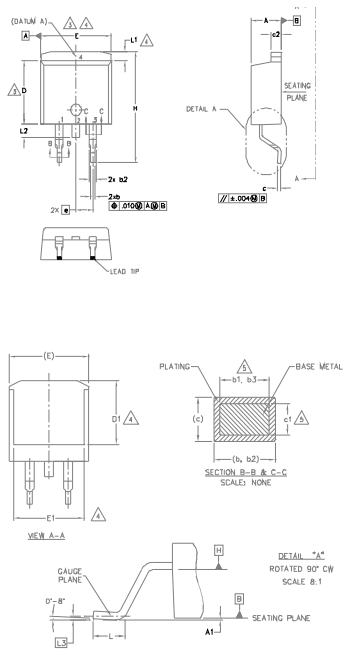


\*  $V_{GS} = 5V$  for Logic Level Devices

**Fig 14.** For N-Channel HEXFETS

## D<sup>2</sup>Pak (TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	
A1	0.00	0.254	.000	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	5
b2	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	5
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	5
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	3
D1	6.86	-	.270	-	4
E	9.65	10.67	.380	.420	3,4
E1	6.22	-	.245	-	4
e	2.54 BSC	-	.100 BSC	-	
H	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1	-	1.68	-	.066	4
L2	-	1.78	-	.070	
L3	0.25 BSC	-	.010 BSC	-	

**NOTES:**

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
- DIMENSION b1, b3 AND c1 APPLY TO BASE METAL ONLY.
- DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- CONTROLLING DIMENSION: INCH.
- OUTLINE CONFORMS TO JEDEC OUTLINE TO-263AB.

**LEAD ASSIGNMENTS**

**DIODES**

- ANODE (TWO DIE) / OPEN (ONE DIE)
- CATHODE
- ANODE

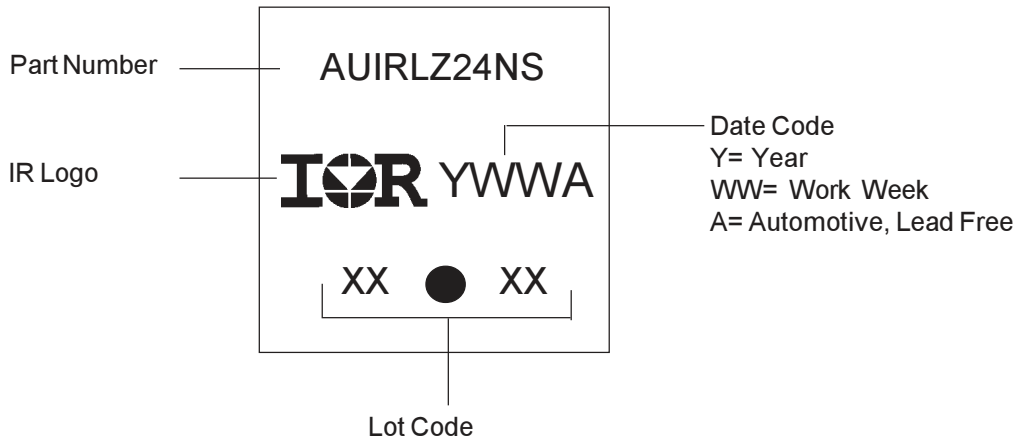
**HEXFET**

- GATE
- DRAIN
- SOURCE

**IGBTs, CoPACK**

- GATE
- COLLECTOR
- EMITTER

## D<sup>2</sup>Pak Part Marking Information



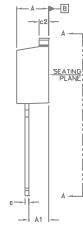
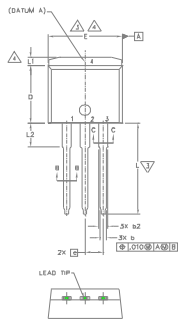
Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>





## TO-262 Package Outline

Dimensions are shown in millimeters (inches)



SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.05	4.83	.160	.190	NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES]. 3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY. 4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1. 5. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY. 6. CONTROLLING DIMENSIONS: INCH. 7. OUTLINE CONFORM TO JEDEC TO-262 EXCEPT A1(max.), b(min.), AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.
A1	2.03	3.02	.080	.119	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	
b2	1.14	1.78	.045	.070	
b3	1.14	1.73	.045	.068	
c	0.38	0.74	.015	.029	
c1	0.38	0.58	.015	.023	
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	
D1	6.86	-	.270	-	
E	9.65	10.67	.380	.420	
E1	6.22	-	.245	-	
e	2.54	BSC	.100	BSC	
L	13.46	14.10	.530	.555	
L1	-	1.65	-	.065	
L2	3.56	3.71	.140	.146	

NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994  
 2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].  
 3. DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.  
 4. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.  
 5. DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.  
 6. CONTROLLING DIMENSIONS: INCH.  
 7. OUTLINE CONFORM TO JEDEC TO-262 EXCEPT A1(max.), b(min.), AND D1(min.) WHERE DIMENSIONS DERIVED THE ACTUAL PACKAGE OUTLINE.

**LEAD ASSIGNMENTS**

**IGBTs, CoPACK**

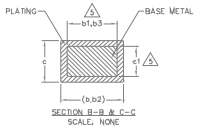
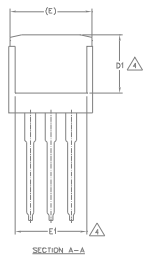
- 1.- GATE
- 2.- COLLECTOR
- 3.- EMITTER
- 4.- COLLECTOR

**HEXFET**

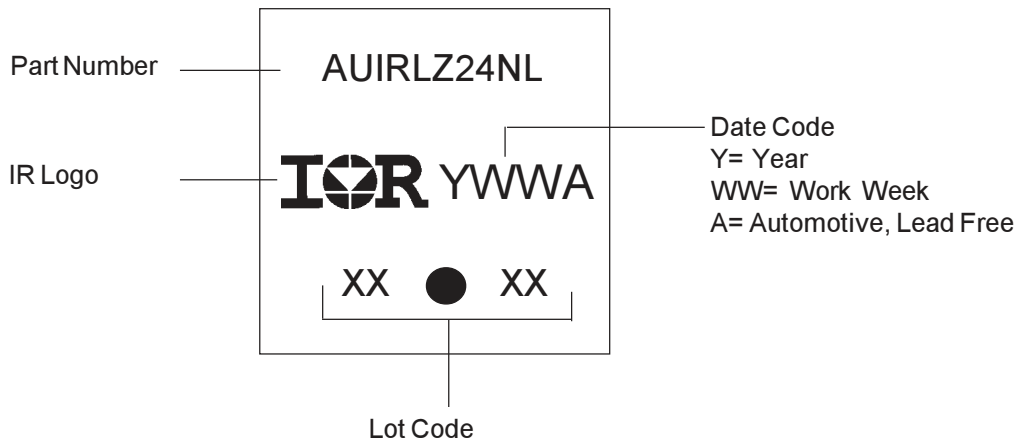
- 1.- GATE
- 2.- DRAIN
- 3.- SOURCE
- 4.- DRAIN

**DIODES**

- 1.- ANODE (TWO DE) / OPEN (ONE DE)
- 2, 4.- CATHODE
- 3.- ANODE



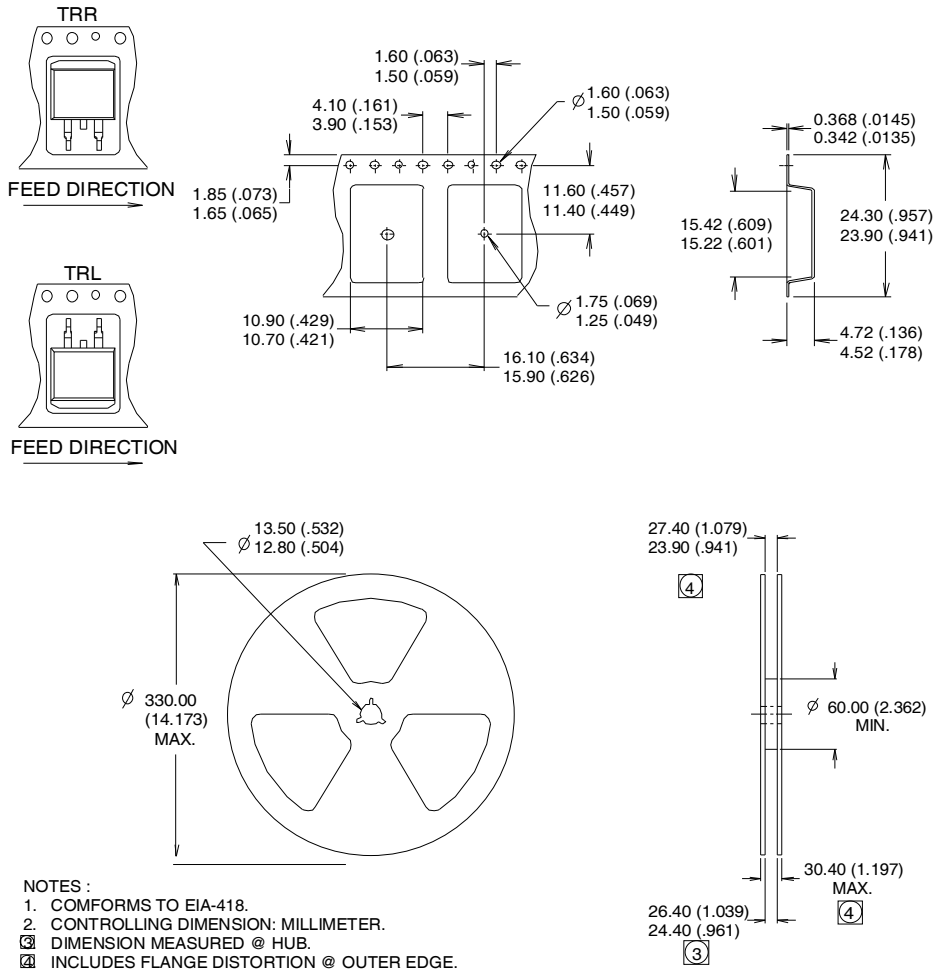
## TO-262 Part Marking Information



Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

## D<sup>2</sup>Pak Tape & Reel Information

Dimensions are shown in millimeters (inches)



Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

**Qualification Information<sup>†</sup>**

<b>Qualification Level</b>		Automotive (per AEC-Q101)	
		Comments: This part number(s) passed Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
<b>Moisture Sensitivity Level</b>		3L-D2 PAK	MSL1
		3L-TO-262	
<b>ESD</b>	Machine Model	Class M2(+/- 150V ) <sup>††</sup> (per AEC-Q101-002)	
	Human Body Model	Class H1A(+/- 500V ) <sup>††</sup> (per AEC-Q101-001)	
	Charged Device Model	Class C5(+/- 2000V ) <sup>††</sup> (per AEC-Q101-005)	
<b>RoHS Compliant</b>		Yes	

† Qualification standards can be found at International Rectifier's web site: <http://www.irf.com/>

†† Highest passing voltage

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For technical support, please contact IR’s Technical Assistance Center  
<http://www.irf.com/technical-info/>

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