CMLT2907A

SURFACE MOUNT SILICON DUAL PNP TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT2907A consists of two individual, isolated 2907A PNP silicon transistors, manufactured by the epitaxial planar process and epoxy molded in an SOT-563 surface mount package. This device has been designed for small signal general purpose and switching applications.

MARKING CODE: L07



MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	60	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	I _C	600	mA
Power Dissipation	P_{D}	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =50V		10	nA
I _{CBO}	V _{CB} =50V, T _A =125°C		10	μΑ
I _{CEV}	V_{CE} =30V, V_{BE} =0.5V		50	nA
BV _{CBO}	I _C =10μA	60		V
BV_{CEO}	I _C =10mA	60		V
BV _{EBO}	I _E =10μA	5.0		V
V _{CE(SAT)}	I_C =150mA, I_B =15mA		0.4	V
VCE(SAT)	I_C =500mA, I_B =50mA		1.6	V
V _{BE(SAT)}	I_C =150mA, I_B =15mA		1.3	V
V _{BE} (SAT)	I_C =500mA, I_B =50mA		2.6	V
h _{FE}	V_{CE} =10V, I_{C} =0.1mA	75		
h _{FE}	V_{CE} =10V, I_{C} =1.0mA	100		
h _{FE}	V_{CE} =10V, I_{C} =10mA	100		
h _{FE}	V_{CE} =10V, I_{C} =150mA	100	300	
h _{FE}	V_{CE} =10V, I_{C} =500mA	50		

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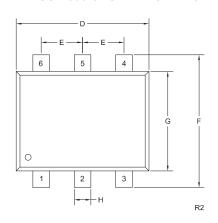


ELECTRICAL CHARACTERISTICS PER TRANSISTOR - Continued: (T_A=25°C unless otherwise noted)

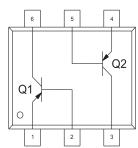
SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
f_{T}	V_{CE} =20V, I_{C} =50mA, f=100MHz	200		MHz
C _{ob}	V_{CB} =10V, I_E =0, f=1.0MHz		8.0	pF
C _{ib}	V_{BE} =2.0V, I_{C} =0, f=1.0MHz		30	pF
^t on	V_{CC} =30V, V_{BE} =0.5V, I_{C} =150mA, I_{B1} =15mA		45	ns
^t d	V_{CC} =30V, V_{BE} =0.5V, I_{C} =150mA, I_{B1} =15mA		10	ns
t _r	V_{CC} =30V, V_{BE} =0.5V, I_{C} =150mA, I_{B1} =15mA		40	ns
^t off	V_{CC} =6.0V, I_{C} =150mA, I_{B1} = I_{B2} =15mA		100	ns
t _s	V_{CC} =6.0V, I_{C} =150mA, I_{B1} = I_{B2} =15mA		80	ns
t _f	V_{CC} =6.0V, I_{C} =150mA, I_{B1} = I_{B2} =15mA		30	ns

SOT-563 CASE - MECHANICAL OUTLINE





DIMENSIONS				
	INCHES		MILLIMETERS	
SYMBOL	MIN	MAX	MIN	MAX
Α	0.0027	0.007	0.07	0.18
В	0.008		0.20	
С	0.017	0.024	0.45	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.059	0.067	1.50	1.70
G	0.043	0.051	1.10	1.30
Н	0.006	0.012	0.15	0.30
SOT-563 (REV: R2)				



LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

MARKING CODE: L07

R4 (29-June 2015)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- · Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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