

SPECIFICATION FOR LCD MODULE

MODULE NO: AFY800480B0-7.0N12NTM-C REVISION NO: E

| Customer's Approval: | | | | | | |
|---------------------------|-----------|------|--|--|--|--|
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| | SIGNATURE | DATE | | | | |
| PREPARED BY (RD ENGINEER) | | | | | | |
| CHECKED BY | | | | | | |
| APPROVED BY | | | | | | |

REVISION RECORD

| REV NO. | REV DATE | CONTENTS | REMARKS |
|---------|------------|---|-------------|
| 0 | 2015-08-05 | First release | Preliminary |
| А | 2015-08-28 | Update drawing and change CTP IC | P5、6 |
| В | 2015-09-10 | Change the driver IC and CTP IC | P4、5 |
| С | 2016-02-20 | Update the TBD data | Page4,7 |
| D | 2016-05-16 | Update the Surface luminance Typ. according to the principle of TFT surface brightness multiplied by 90%. | Page7 |
| Е | 2016-07-27 | Revise the interface description | Page9 |
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1. GENERAL INFORMATION

| No. | Item | Contents | Unit |
|-----|--------------------------------|--------------------------------|------|
| 1 | LCD size | 7.0 inch (Diagonal) | / |
| 2 | LCD type | TN/Normally white/Transmissive | / |
| 3 | Viewing direction(eye) | 12 O'clock | / |
| 4 | Gray scale inversion direction | 6 O'clock | / |
| 5 | Resolution(H*V) | 800*480 | / |
| 6 | Module size (L*W*H) | 164.9*100.0*5.4 | mm |
| 7 | Active area (L*W) | 153.84*85.63 | mm |
| 8 | Pixel pitch (L*W) | 0.1923*0.1784 | mm |
| 9 | Interface type | RGB Interface | / |
| 10 | Module power consumption | 0.462(Without backlight) | W |
| 11 | Back light type | LED | / |
| 12 | Driver IC | HX8264+HX8664 or compatible | / |
| 13 | Weight | 179 | g |

2. ABSOLUTE MAXIMUM RATINGS

| Item | Symbol | Min | Max | Unit |
|----------------------------------|--------|------|--------------|------|
| Power supply input voltage (LCM) | VDD | -0.5 | 5.0 | V |
| Backlight current (normal temp.) | ILED | - | 75 | mA |
| Operation temperature | Тор | -20 | 70 | °C |
| Storage temperature | Tst | -30 | 80 | °C |
| Humidity | RH | - | 90%(Max60°C) | RH |

3. ELECTRICAL CHARACTERISTICS

DC CHARACTERISTICS (at Ta=25°C)

| Item | Symbol | Min | Тур | Max | Unit | Note |
|--|--------|--------|------|--------|------|------|
| Power supply input voltage (LCM) | VDD | 2.7 | 3.3 | 3.6 | V | |
| I/O logic voltage | VDDIO | N/A | N/A | N/A | V | |
| Input voltage 'H' level | VIH | 0.7VDD | - | VDD | V | |
| Input voltage 'L' level | VIL | 0 | - | 0.3VDD | V | |
| Power supply current | IVDD | - | 140 | - | mA | |
| TFT gate on voltage | VGH | - | 18 | - | V | |
| TFT gate off voltage | VGL | - | -8 | - | V | |
| Analog power supply voltage | AVDD | - | 10.3 | - | V | |
| Differential input common mode voltage | Vcom | - | 3.4 | - | V | |

4. BACKLIGHT CHARACTERISTICS

(at Ta=25°C.RH=60%)

| (at 1a=25 0;1111=00 /6) | 11 14=25 0,1111=00 70 | | | | | | | |
|-------------------------|-----------------------|---------|----------------|------|------|---------|--|--|
| Item | Symbol | Min. | Тур. | Max. | Unit | Note | | |
| LED forward voltage | VF | - | 25.6 | - | V | IF=60mA | | |
| LED forward current | IF | - | 60 | - | mA | | | |
| LED power consumption | PLED | - | 1.536 | - | W | Note1 | | |
| Number of LED | - | | 24 | | PCS | | | |
| Connection mode | - | 8 in se | ries *3 in par | / | | | | |
| LED life-time | - | 20000 | - | - | Hrs | Note2 | | |

Note1.Calculator Value for reference ILED×VLED×LED Quantity = PLED

Note2. The LED Life-time define as the estimated time to 50% degradation of initial brightness at Ta=25°C and IF =60mA. The LED lifetime could be decreased if operating IF is larger than 60mA.

5. TOUCH PANEL CHARACTERISTICS (at Ta=25°C)

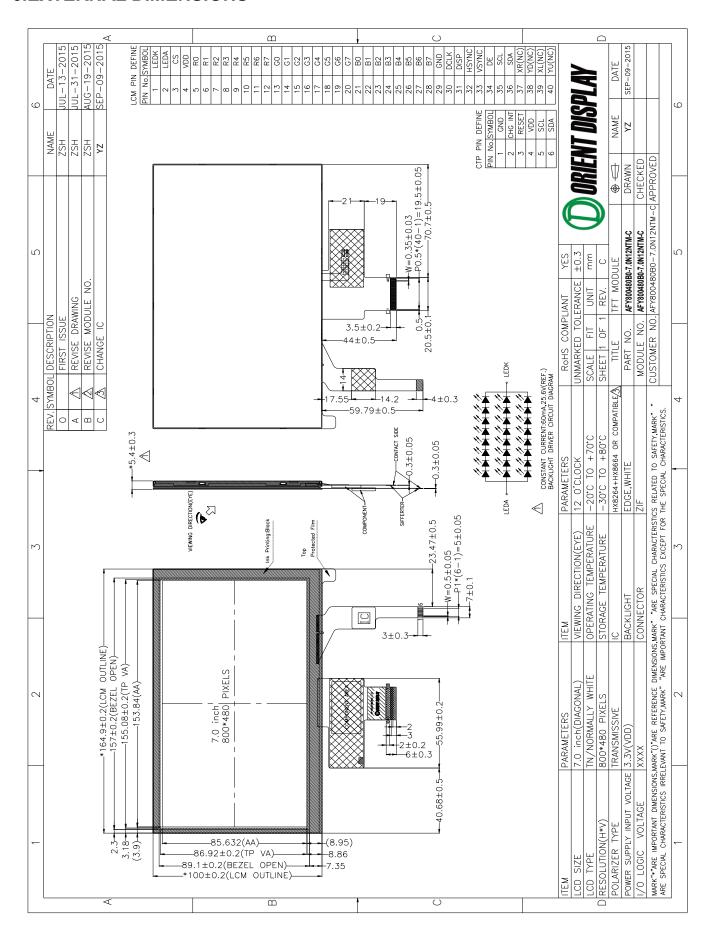
| FPC Design | Item | Description | Remark |
|------------|-------------------------|-----------------|--------|
| | IC solution on TP Model | GT911 | |
| | Touch Count Max | 5 point | |
| [√] COF | Display Resolution | 800*480 | |
| | Interface Type | I2C | |
| | I2C Slave Address | 0x5D | |
| | Origin of Coordinate | Top left corner | |
| | IC solution on Broad | | |
| []COB | Driving Channels | | |
| | Sensing Channels | | |

| Parameter | Min | Тур | Max | Unit |
|--------------------------|-----|-----|-----|------|
| Interface Signal Voltage | 1.8 | 3.3 | 3.6 | V |
| Power Voltage | 2.6 | 3.3 | 3.6 | V |
| Power ripple | | | 50 | MV |

Note1: The detail refer to the Specification For IC

Note2: '*'means that the item is optional according to the product requirement

6.EXTERNAL DIMENSIONS



7.ELECTRO-OPTICAL CHARACTERISTICS

| Item | Symbol | Condition | Min | Тур | Max | Unit | Remark | Note |
|----------------------|---------|-----------|--------|--------|--------|-------|---------|--------|
| Response time | Tr+ Tf | | - | 25 | 50 | ms | FIG.1 | Note 4 |
| Contrast ratio | Cr | - | 400 | 500 | - | | FIG.2 | Note 1 |
| Surface Luminance | Lv | θ=0° | 330 | 472 | - | cd/m² | FIG.2 | Note 2 |
| Luminance uniformity | δWHITE | θ=0° | 60 | 75 | - | % | FIG.2 | Note 3 |
| NTSC | - | θ=0° | - | 50 | - | % | FIG.2 | Note 5 |
| | θ | Ø = 90° | 60 | 70 | - | deg | FIG.3 | |
| Viewing angle | | Ø = 270° | 50 | 60 | - | deg | FIG.3 | Note 6 |
| viewing angle | | Ø = 0° | 60 | 70 | - | deg | FIG.3 | Note 6 |
| | | Ø = 180° | 60 | 70 | - | deg | FIG.3 | |
| | Red x | | 0.5378 | 0.5778 | 0.6178 | - | | |
| | Red y | | 0.2872 | 0.3272 | 0.3672 | • | | |
| | Green x | θ=0° | 0.3128 | 0.3528 | 0.3928 | - | | |
| CIE (x, y) | Green y | ∅=0° | 0.5548 | 0.5948 | 0.6348 | • | FIG.2 | Note 5 |
| chromaticity | Blue x | | 0.1140 | 0.1540 | 0.1940 | - | CIE1931 | Note 5 |
| Blue y | Ta=25°C | 0.0977 | 0.1377 | 0.1777 | - | | | |
| | White x | | 0.2685 | 0.3085 | 0.3485 | - | | |
| | White y | | 0.3189 | 0.3589 | 0.3989 | - | | |

Note1. Definition of contrast ratio

Contrast Ratio (CR) is defined mathematically by the following formula. For more information see FIG 2.

Contrast Ratio = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5,P6,P7,P8,P9)

Note2. Definition of surface luminance

Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)

Note3. Definiton of luminance uniformity

The luminance uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

Yu= Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)
Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)

Note4. Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For additional information see FIG1.

Note5. Definition of color chromaticity (CIE1931)

CIE (x, y) chromaticity ,The x,y value is determined by screen active area center position P5, For more information see FIG 2.

Note6. Definition of Viewing ange.

Viewing angle is the angle at which the contrast ratio is greater than 10. angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope or DMS series Instruments or compatible. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on TOPCON's BM-7 or BM-5 photo detector or compatible.

Note: For TFT module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

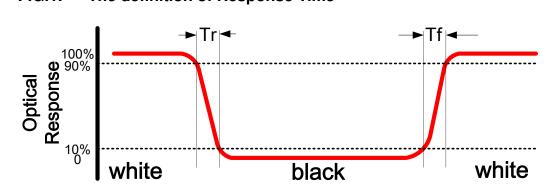


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

Size:S≤5"(see Figure a)

A:5 mm B:5 mm

H,V: Active Area

Light spot size ∅=5mm(BM-5),∅=7.7mm (BM-7)500mm distance or compatible distance from the LCD surface to detector lens.

test spot position: see Figure a.

measurement instrument: TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)

Size:5" < S \leq 12.3"(see figure b)

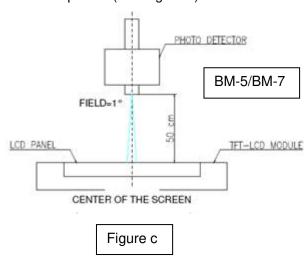
H,V: Active Area

Light spot size \varnothing =5mm(BM-5), \varnothing =7.7mm (BM-7)500mm distance or compatible distance from the LCD surface to detector lens

test spot position: see Figure b

measurement instrument : TOPCON's luminance meter

BM-5 or BM-7 or compatible (see Figure c)



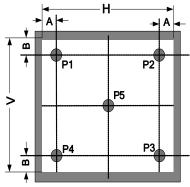


Figure a

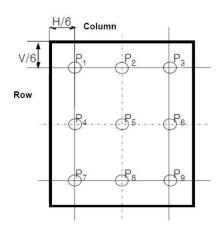
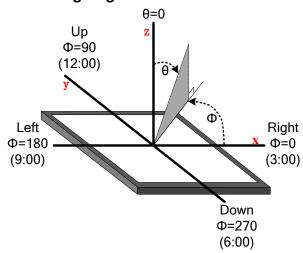


Figure b

FIG.3. The definition of viewing angle



8.INTERFACE DESCRIPTION

LCM Interface description

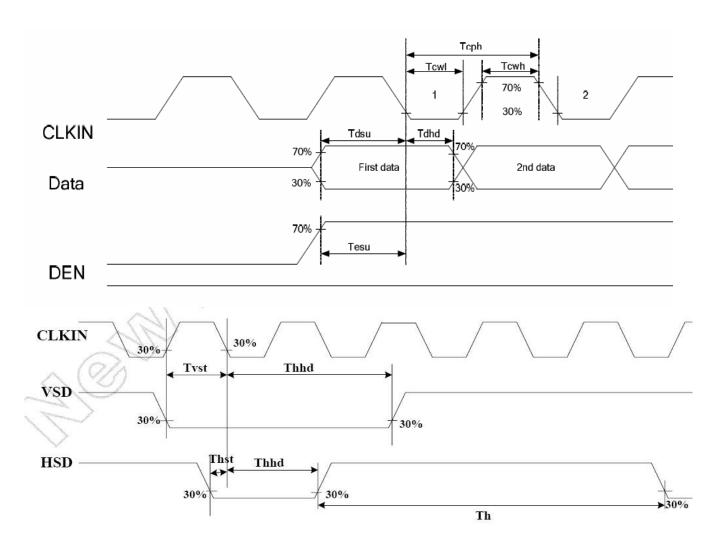
| Interface No. | Name | I/O or connect to | Description | |
|---------------|--------|-------------------|--|--|
| 1 | LEDK | Р | Power for LED backlight(Cathode) | |
| 2 | LEDA | P | Power for LED backlight(Anode) | |
| 3 | CS | 1 | SPI function chip select signal | |
| 4 | VDD | Р | Digital Power | |
| 5-12 | R0-R7 | I | Red data | |
| 13-20 | G0-G7 | I | Green data | |
| 21-28 | B0-B7 | I | Blue data | |
| 29 | GND | Р | Power Ground | |
| 30 | DCLK | I | Clock input | |
| 31 | DISP | I | Display on/off | |
| 32 | HSYNC | I | Horizontal sync input. Negative polarity | |
| 33 | VSYNC | 1 | Vertical sync input. Negative polarity | |
| 34 | DE | I | Data Enable signal ;normally pull low | |
| 35 | SCL | 1 | SPI SCL function | |
| 36 | SDA | I | SPI SDA function | |
| 37 | XR(NC) | 1 | / | |
| 38 | YD(NC) | / | / | |
| 39 | XL(NC) | / | / | |
| 40 | YU(NC) | / | / | |

CTP interface description

| Interface No. | Name | I/O or connect to | Description |
|---------------|---------|-------------------|------------------------|
| 1 | GND | Р | Ground |
| 2 | CHG INT | 0 | State change interrupt |
| 3 | RESET | 1 | Reset low |
| 4 | VDD | Р | Power supply of CTP |
| 5 | SCL | 1 | Serial interface clock |
| 6 | SDA | I/O | Serial interface date |

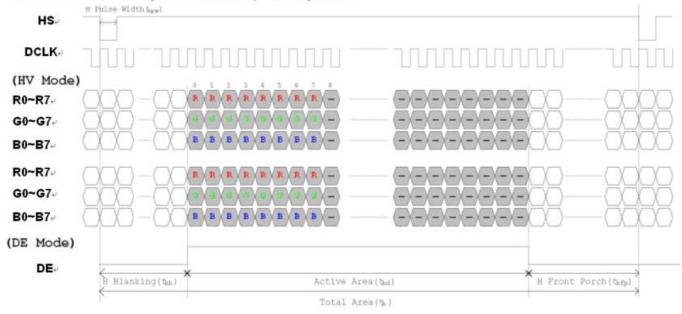
9. AC CHARACTERISTICS

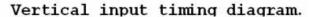
| Item | Symbol | Min | Тур | Max | Unit | Remark |
|-------------------------|--------|-----|-----|-----|------|-------------|
| HS setup time | Thst | 8 | - | - | ns | |
| HS hold time | Thhd | 8 | - | - | ns | |
| VS setup time | Tvst | 8 | - | - | ns | |
| VS hold time | Tvhd | 8 | - | - | ns | |
| Data setup time | Tdsu | 8 | - | - | ns | |
| Data hold time | Tdhd | 8 | - | - | ns | |
| DE setup time | Tesu | 8 | - | - | ns | |
| DE hold time | Tehd | 8 | - | - | ns | |
| Dvdd power on slew rate | Tpor | - | - | 20 | ms | 0 to90%DVDD |
| RESET pulse width | Trst | 1 | - | - | us | |
| DCLK cycle time | Tcoh | 20 | - | - | ns | |
| DCLK pulse duty | Tcwh | 40 | 50 | 60 | % | |

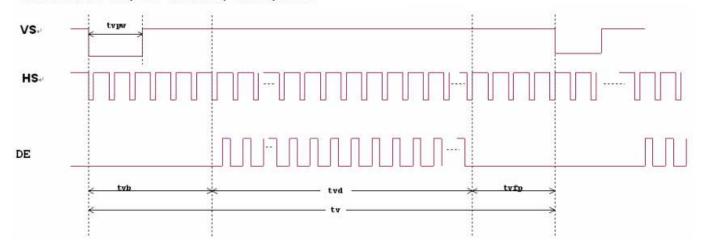


| Item | Symbol | Min | Тур | Max | Unit | Remark |
|-------------------------|--------|-----|-----|------|------|--------|
| Horizontal Display Area | thd | | 800 | | DCLK | |
| Dclk frequency | fclk | - | 30 | 50 | MHZ | |
| One horizontal line | th | 889 | 928 | 1143 | DCLK | |
| HS pulse width | thpw | 1 | 48 | 255 | DCLK | |
| HS blanking | thb | | 88 | | DCLK | |
| HS front porch | thfp | 1 | 40 | 255 | DCLK | |
| Vertical Display Area | tvd | | 480 | | TH | |
| VS period time | tv | 513 | 525 | 767 | TH | |
| VS pulse width | tvpw | 3 | 3 | 255 | TH | |
| VS blanking | tvb | | 32 | | TH | |
| VS front porch | tvfp | 1 | 13 | 255 | TH | |

Horizontal input timing diagram.





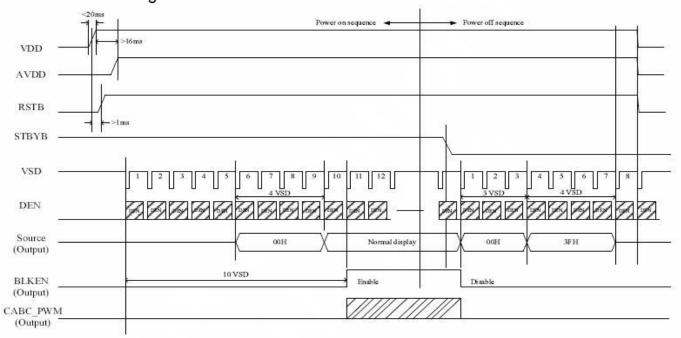


10.POWER SEQUENCE

To prevent the device damage from latch up, the power on/off sequence shown below must be followed.

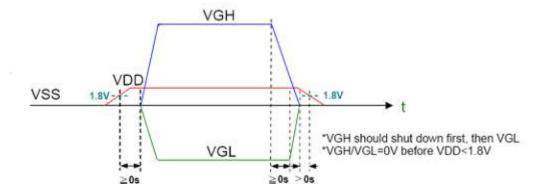
Power ON: VDD, VSS → VDDA, VSSA Power OFF: VDDA, VSSA → VDD, VSS

HX8264-D08 has a power on/off sequence control function. In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Please refer to "AC Characteristics" for more detail on timing.



To prevent device latch up or power on display noise, the power on/off sequence shown below must be followed. After VDD reaches 1.8V, VGH/VGL can start to power on. Before VDD drops to 1.8V, VGH/VGL must start to power down. After VGH/VGL start to power on, CPV and STV should be not floating, and /XAO should be at VDD level or floating. The other control signals have no timing limitation.

When power on: VDD→VGH/VGL When power off: VGH→VGL→VDD



11.RELIABILITY TEST CONDITIONS

| No. | Test Item | Test Condition | Inspection after test |
|-----|----------------------------|--|--|
| 1 | High Temperature Storage | 80±2°C/240 hours | |
| 2 | Low Temperature Storage | -30±2°C/240 hours | Inspection after |
| 3 | High Temperature Operating | 70±2°C/120 hours | 2~4hours storage at room temperature, the |
| 4 | Low Temperature Operating | -20±2°C/120 hours | sample shall be free |
| 5 | Temperature Cycle | -20±2°C~25~70±2°C*10cycles (30min.) (5min.) (30min.) | from defects: 1.Current changing value before test and |
| 6 | Damp Proof Test | 50°C*90% RH/120 hours | after test is 50% larger; |
| 7 | Vibration Test | Frequency: 10Hz~55Hz~10Hz Amplitude:1.5mm, X,Y,Z direction for | 2. function defect: Non-display ,abnormal-d isplay, missing lines, |
| | | total 3hours (Packing condition) | Short lines, ITO |
| 8 | Dropping test | Drop to the ground from 1m height, one time, every side of carton. (Packing condition) | corrosion; 3.visual defect: Air bubble in the LCD, Seal |
| 9 | ESD test | Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time | leak, Glass crack。 |

Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 3~5pcs.
- 3.For Damp Proof Test, Pure water(Resistance > $10M\Omega$) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

12. INSPECTION CRITERION

12.1 description

This specification is made to be used as the standard acceptance/rejection criteria for TFT LCM Product.

1.Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1 :1999 and ANSI/ASQC Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65 Minor defect: AQL 1.5

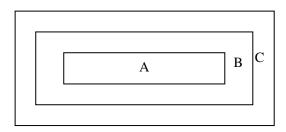
2. Inspection condition

●Viewing distance for cosmetic inspection is about 30±5cm with bare eyes, and under an environment 600~1000lux for visual inspection and 0~200lux for function test., all directions for inspecting the sample should be within 45°against perpendicular line. (Normal temperature 18~28°C and normal humidity 60±15%RH).

Driving voltage

The Vop value from which the most optical contrast can be obtained near the specified Vop in the specification (Within ± 0.5 V of the typical value at 25°C.).

3. Definition of inspection zone in LCD



Zone A: character/Digit area

Zone B: viewing area except Zone A (ZoneA+ZoneB=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD.

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

12.2 Inspection criterion

12.2.1 function defect

| Items to be inspected | Inspection criterion | Classification of defects |
|------------------------|--|---------------------------|
| | No display Display abnormally | |
| All functional defects | 3) Missing vertical, horizontal segment | |
| | 4) Short circuit5) Back-light no lighting, flickering and abnormal lighting.6) obvious striation7) Current beyond specification value | MA |
| Missing | Missing component | |
| Outline dimension | Overall outline dimension exceed the drawing is not allowed. | |

12.2.2 LCD pixel defect (bad dot) (defect type:MI)

| Checking item | Judgment criterion | | | | |
|-------------------------------------|-------------------------------|------------------|-----------------|--|--|
| Item∖LCD size | S ≤5.0 Inch | 5.0 < S≤7.0 Inch | 7 < S≤12.3 Inch | | |
| Color bad dot-bright dot(R、G、B) | 1 | 2 | 3 | | |
| two adjacent bright point | 0 | 1 | 2 | | |
| three or more adjacent point | 0 | 0 | 0 | | |
| total points for bad dot-bright dot | 1 | 2 | 5 | | |
| Bad dot-dark dot | 2 | 4 | 5 | | |
| two adjacent dark point | 1 | 2 | 3 | | |
| three or more adjacent point | 0 | 1 | 1 | | |
| total points for bad dot -dark dot | 3 | 6 | 7 | | |
| patch bright dot | Invisible with ND5%,it is OK. | | | | |

| 12.2.3 Dot and line defect (defect type:MI) | | | | | | |
|--|--------------|-----------------|--------------------|------------------|-----------------|------------|
| Checking | | Figure | | | | |
| item | Diameter(mm |)\LCD Size | S ≤5.0 Inch | 5 < S≤7 Inch | 7 < S≤12.3 Inch | Figure |
| | D≤ | ≦0.1 | allowed | allowed | allowed | |
| | 0.1 < D≤0.2 | | 4 | allowed | allowed | |
| Dot defect | 0.2 < | D≤0.3 | 0 | 5 | 6 | 1 to |
| Dol delect | 0.3 < | D≤0.5 | 0 | 0 | б | a - |
| | D > 0.5 | | 0 | 0 | 0 | D=(a+b)/2 |
| | † | the distance be | tween the tw | o defect dot:DS≥ | ≥5mm | |
| | Length(mm) | Width(mm) | | Judgment crit | | |
| | disregard | W≤0.05 | allowed | allowed | allowed | ← L |
| line defect | L≤5 | 0.05 < W≤0.1 | 4 | 5 | 7 | \$ w |
| | L>5 | W > 0.1 | 0 | 0 | 0 | |
| Concave | LCD Size(mm) | | Judgment criterion | | | |
| point and | D≤ | ≦0.3 | allowed | allowed | allowed | |
| air bubble | 0.3 < | D≤1.0 | 3 | 4 | 5 | Ţ b |
| for | 1.0 < | D≤1.5 | 1 | 2 | 3 | a |
| polarizer | D> | > 1.5 | 0 | 0 | 0 | D=(a+b)/2 |
| | Length(mm) | Width(mm) | | Judgment cri | terion | |
| | disregard | W≤0.05 | allowed | allowed | allowed | |
| Fold mark | 1 < L≤5 | 0.05 < W≤0.2 | 3 | 4 | 5 | L |
| linear scar | L>5 | W > 0.2 | 0 | 0 | 0 | |
| for polarizer Notes:1.If the fold mark and linear scar for polarizer is visible with operating condition, the defect is judged with line judge; 2.If the fold mark and linear scar for polarizer is visible with | | | | | | |

non-operating condition, the defect is judged with the above judgment standard.

12.2.4 Corner and others crack for LCD (defect type:MI)

| Checking item | Judgment criterion | Figure |
|---------------------------|--|--------|
| electric conduction crack | X≤3.0mm,Y≤1/4w,Z≤t,N≤2 | TINE Z |
| corner crack | X≤3.0mm,Y≤3.0mm , Z≤t,N≤3 Corner crack extended to ITO PIN,none allowed | |
| surface crack | X≤1.5mm,Y≤1.0mm, Z≤t, N≤4 | Z T |

12.2.5 Module cosmetic criterion (defect type:MI)

| Item | Judgment criterion | | | |
|---------------------------------------|---|--|--|--|
| Difference in Spec. | None allowed | | | |
| Pattern peeling | No substrate pattern peeling and floating | | | |
| Soldering defects | No soldering missing No soldering bridge No cold soldering | | | |
| | Notes:detail judgment referring to IPC-A-610 grade II | | | |
| Resist flaw on Printed Circuit Boards | visible copper foil (0.5mm or more) on substrate pattern, none allowed | | | |
| Accretion of metallic Foreign matter | No accretion of metallic foreign matters (Not exceed 0.2mm) | | | |
| Stain | No stain to spoil cosmetic badly | | | |
| Plate discoloring | No plate fading, rusting and discoloring | | | |
| Newton ring | Referring to limited sample | | | |
| Mura | Invisible with 5%ND,allowed | | | |
| Light leaks | Referring to limited sample | | | |

13. HANDLING PRECAUTIONS

13.1 Mounting method

The LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[recommended below] and wipe lightly

- .lsopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- •.Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

13.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

13.4 Packing

Module employ LCD elements and must be treated as such.

- Avoid intense shock and falls from a height.
- •. To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

13.5 Caution for operation

- •.It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- •.An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- •.Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- •. If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- •. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

13.6 Storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storing in an ambient temperature 10°C to 30°C, and in a relative humidity of 45% to 75%. Don't expose to sunlight or fluorescent light.
- Storing in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

13.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

14. PRECAUTION FOR USE

- **14.1** A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- **14.2** On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.
 - When a question is arisen in this specification
 - When a new problem is arisen which is not specified in this specifications
 - When an inspection specifications change or operating condition change in customer is reported to ODNA, and some problem is arisen in this specification due to the change
 - When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

15. PACKING SPECIFICATION

Please consult our technical department for detail information.