



NCP1072DIPGEVB:6 W SMPS Evaluation Board

Evaluation Board Description

The NCP1072 / NCP1075 products integrate a fixed frequency current mode controller with a 700 V MOSFET. Available in a PDIP-7 or SOT-223 package, the NCP1072/5 offer a high level of integration, including soft-start, frequency-jittering, short-circuit protection, skip-cycle, a maximum peak current set point, ramp compensation, and a Dynamic Self-Supply (eliminating the need for an auxiliary winding).



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Unlike other monolithic solutions, the NCP1072/5 is quiet by nature: during nominal load operation, the part switches at one of the available frequencies (65, 100 or 130 kHz). When the output power demand diminishes, the IC automatically enters frequency foldback mode and provides excellent efficiency at light loads. When the power demand reduces further, it enters into a skip mode to reduce the standby consumption down to a no load condition.

Protection features include: a timer to detect an overload or a short-circuit event, Overvoltage Protection with auto-recovery and AC input line voltage detection.

For improved standby performance, the connection of an auxiliary winding stops the DSS operation and helps to reduce input power consumption below 50 mW at high line.

Evaluation Board Information

| Evaluation Board | Status | Pb-free | Short Description | Parts Used | Action |
|--------------------------------|--------|---------|---------------------------|------------------------------|--------|
| NCP1072DIPGEVB | Active | | 6 W SMPS Evaluation Board | NCP1072P100G | |

Technical Documents

| Type | Document Title | Document ID/ Size | Rev |
|----------------------------|---|---|-----|
| Eval Board: BOM | NCP1072DIPGEVB Bill of Materials ROHS Compliant | NCP1072DIPGEVB_BOM_ROHS.PDF - 115.0 KB | 0 |
| Eval Board: Gerber | NCP1072DIPGEVB Gerber Layout Files (Zip Format) | NCP1072DIPGEVB_GERBER.ZIP - 35.0 KB | 0 |
| Eval Board: Schematic | NCP1072DIPGEVB Schematic | NCP1072DIPGEVB_SCHEMATIC.PDF - 162.0 KB | 0 |
| Eval Board: Test Procedure | NCP1072DIPGEVB Test Procedure | NCP1072DIPGEVB_TEST_PROCEDURE.PDF - 86.0 KB | 0 |

