

# 3.3V 1:9 Clock Buffer

#### **Features**

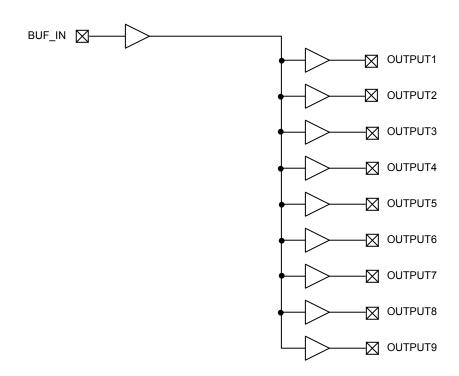
- One-Input to Nine-Output Buffer/Driver
- Buffers all frequencies from DC to 133.33MHz
- Low power consumption for mobile applications Less than 32mA at 66.6MHz with unloaded outputs
- Input-Output delay: 6nS(max)
- Output-output skew less than 250pS
- 16-pin SOIC Package
- Supply Voltage: 3.3V ± 0.3V

#### **Functional Description**

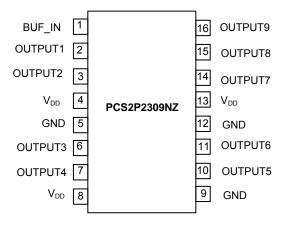
PCS2P2309NZ is a low-cost high-speed buffer designed to accept one clock input and distribute up to nine clocks in mobile PC systems and desktop PC systems. The device operates at 3.3V and outputs can run up to 133.33MHz.

PCS2P2309NZ is designed for low EMI and power optimization and consumes less than 32mA at 66.6MHz, making it ideal for the low-power requirements of mobile systems. It is available in an 16-pin SOIC Package.

#### **Block Diagram**



### **Pin Configuration**



**Pin Description** 

Pin#	Pin Name	Description
4, 8, 13	$V_{DD}$	3.3V Digital Voltage Supply
5, 9, 12	GND	Ground
1	BUF_IN	Input Clock
2, 3, 6, 7, 10, 11, 14, 15, 16	OUTPUT [1:9]	Outputs

**Absolute Maximum Ratings** 

Absolute Maximum Ratings					
Parameter	Min	Max	Unit		
Supply Voltage to Ground Potential	-0.5	+4.6	V		
DC Input Voltage (Except REF)	-0.5	V <sub>DD</sub> + 0.5	V		
DC Input Voltage (REF)	-0.5	7	V		
Storage Temperature	-65	+150	${\mathfrak C}$		
Max. Soldering Temperature (10 sec)		260	${\mathfrak C}$		
Junction Temperature		150	C		
Static Discharge Voltage (As per JEDEC STD22- A114-B)		2000	V		
Note: These are stress ratings only and functional usage is not implied. Exposure to absolute maximum ratin	gs for prolonged p	eriods can			

### **Operating Conditions**

Parameter	Description	Min	Max	Unit
$V_{DD}$	Supply Voltage	3.0	3.6	٧
T <sub>A</sub>	Operating Temperature	-40	85	C
$C_L$	Load Capacitance, Fout < 100MHz		30	pF
OL.	Load Capacitance,100MHz < Fout < 133.33MHz		15	pF
C <sub>IN</sub>	Input Capacitance		7	pF
BUF_IN, OUTPUT [1:9]	Operating Frequency	DC	133.33	MHz
t <sub>PU</sub>	Power-up time for all $V_{\text{DD}}$ 's to reach minimum specified voltage (power ramps must be monotonic)	0.05	50	mS

#### **Electrical Characteristics**

Symbol	Parameter		Test Conditions	Min	Max	Unit
V <sub>IL</sub>	Input LOW	Voltage <sup>1</sup>			0.8	V
V <sub>IH</sub>	Input HIGH Voltage <sup>1</sup>			2.2		V
I <sub>IL</sub>	Input LOW Current		V <sub>IN</sub> = 0V		50.0	μΑ
I <sub>IH</sub>	Input HIGH Current		$V_{IN} = V_{DD}$		100.0	μΑ
V <sub>OL</sub>	Output LOW Voltage <sup>2</sup>		I <sub>OL</sub> = 12mA		0.4	٧
V <sub>OH</sub>	Output HIGH Voltage <sup>2</sup>		I <sub>OH</sub> = -12mA	2.4		V
	Supply	0℃ to +70℃	Unloaded outputs at 66 66MUz		30	mΛ
I <sub>DD</sub>	Current -40℃ to +85℃	Unloaded outputs at 66.66MHz		32	mA	

### Switching Characteristics<sup>1</sup>

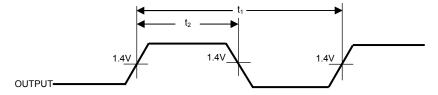
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
t <sub>3</sub>	Rise Time <sup>2</sup>	Measured between 0.8V and 2.0V		1.5	2	nS
t <sub>4</sub>	Fall Time <sup>2</sup>	Measured between 2.0V and 0.8V		1.5	2	nS
t <sub>D</sub>	Duty Cycle <sup>2</sup> = $t_2 \div t_1$	Measured at 1.4V (For an Input Clock Duty Cycle 50%)	45	50	55	%
t <sub>5</sub>	Output to Output Skew <sup>2</sup>	All outputs equally loaded			±250	рS
t <sub>6</sub>	Propagation Delay, BUF_IN Rising Edge to OUTPUT Rising Edge <sup>2</sup>	Measured at V <sub>DD</sub> /2		4	6	nS

Notes: 1. BUF\_IN input has a threshold voltage of V<sub>DD</sub>/2.
2. Parameter is guaranteed by design and characterization. It is not 100% tested in production.

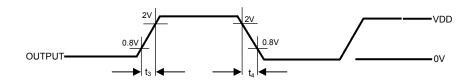
All parameters specified with loaded outputs.
 Parameter is guaranteed by design and characterization. It is not 100% tested in production.

### **Switching Waveforms**

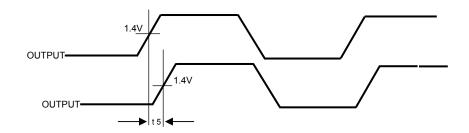
## **Duty Cycle Timing**



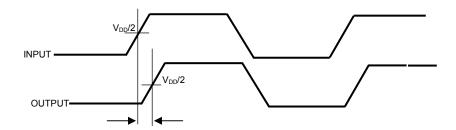
### All Outputs Rise/Fall Time



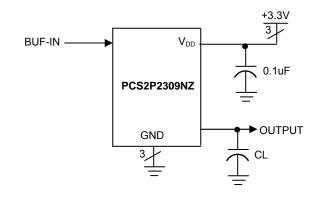
### **Output-Output Skew**



### Input-Output Propagation Delay

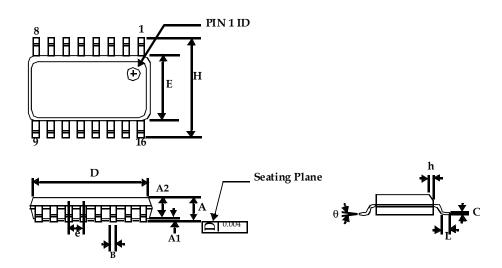


#### **Test Circuit**



# **Package Information**

# 16-lead (150 Mil) Molded SOIC



	Dimensions				
Symbol	Inches		Millimeters		
	Min	Max	Min	Max	
Α	0.053	0.069	1.35	1.75	
A1	0.004	0.010	0.10	0.25	
A2	0.049	0.059	1.25	1.50	
В	0.013	0.022	0.33	0.53	
С	0.008	0.012	0.19	0.27	
D	0.386	0.394	9.80	10.01	
Е	0.150	0.157	3.80	4.00	
е	0.050 BSC 1.27		1.27	BSC	
Н	0.228	0.244	5.80	6.20	
h	0.010	0.016	0.25	0.41	
L	0.016	0.035	0.40	0.89	
θ	0°	8°	0°	8°	

**Ordering Code** 

Part Number	Marking	Package Type	Temperature
P2I2309NZG-16-ST	2I2309NZ	16-pin 150-mil SOIC, Green	-40℃ to +85℃
PCS2I2309NZG16SR	2I2309NZ	16-pin 150-mil SOIC ,Tape and Reel, Green	-40℃ to +85℃

A "G" placed at the end of last row of marking or just below the last row toward the center of package indicates Pb-free for 16 pin SOIC packages.

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