

Digital Attenuator 15.5 dB, 5-Bit, TTL Driver, DC-3.5 GHz

Rev. V5

Features

- Attenuation: 0.5 dB Steps to 15.5 dB
- Single Positive Supply
- Contains Internal DC to DC Converter
- Low DC Power Consumption
- Small Footprint, JEDEC Package
- Integral TTL Driver
- 50 ohm Impedance
- Lead-Free CSP-1 Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of AT90-1283

Description

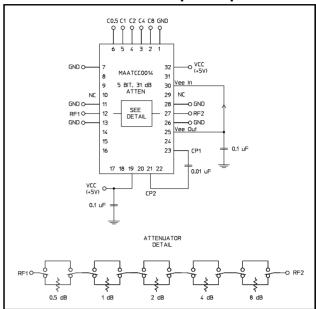
M/A-COM's MAATCC0014 is a GaAs FET 5-bit digital attenuator with integral TTL driver. Step size is 0.5 dB providing a 15.5 dB total attenuation range. This device is in an PQFN plastic surface mount package. The MAATCC0014 is ideally suited for use where accuracy, fast speed, very low power consumption and low costs are required. For dual supply designs without switching noise, use MAAD-007081-000100.

Ordering Information

Part Number	Package
MAATCC0014	Bulk Packaging
MAATCC0014TR	1000 piece reel
MAATCC0014-TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

Schematic with Off-Chip Components



Pin Configuration³

Pin No.	Function	Pin No.	Function
1	GND	17	NC
2	C8 18		NC
3	C4	19	Vcc
4	C2	20	NC
5	C1	21	Ср
6	C0.5	22	NC
7	GND	23	Ср
8	NC	24	NC
9	NC	25	V _{EE} ²
10	NC ¹	26	GND
11	GND	27	RF2
12	RF1	28	GND
13	GND	29	NC ¹
14	NC	30	V _{EE} ²
15	NC	31	NC
16	NC	32	Vcc

- 1. Pins 10 and 29 must be isolated.
- VEE is produced internally and requires a .1 μF cap to GND. Generated noise is typical of switching DC-DC Converters.
- The exposed pad centered on the package bottom must be connected to RF and DC ground. (For PQFN Packages)

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Visit www.macom.com for additional data sheets and product information.

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^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.



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Electrical Specifications: $T_A = +25$ °C

Parameter	Test Conditions	Frequency	Units	Min	Тур	Max
Insertion Loss	_	DC - 3.5 GHz	dB	_	2.8	3.2
Attenuation Accuracy	Individual Bits 0.5-1-4-8 dB Individual Bit 2 dB Any Combination of Bits 1 to 15.5 dB	DC - 3.5 GHz DC - 3.5 GHz DC - 3.5 GHz	dB dB dB	_ _ _		±(.3 +5% of atten setting) ±(.4 +10% of atten setting) ±(.5 +7% of atten setting)
VSWR	Full Range	DC - 3.5 GHz	Ratio	1	1.6:1	1.8:1
Switching Speed	50% Cntl to 90%/10% RF 10% to 90% or 90% to 10%		ns ns		75 20	150 50
1 dB Compression		50 MHz 0.5 - 3.5 GHz	dBm dBm		+21 +29	11
Input IP ₃	Two-tone inputs up to +5 dBm	50 MHz 0.5-3.5 GHz	dB dB		+35 +48	
Vcc	_	_	V	4.75	5.0	5.25
V _{IL} V _{IH}	LOW-level input voltage HIGH-level input voltage		V V	0.0 2.0	_	0.8 5.0
lin (Input Leakage Current)	Vin = V _{CC} or GND	_	uA	-1.0	_	1.0
Icc ⁴	Vcc min to max, Logic "0" or "1"	_	mA	_	6	10
Turn-on Current ⁵	For guaranteed start-up	_	mA	_	_	125
Δlcc (Additional Supply Current Per TTL Input Pin)	V _{CC} = Max, Vcntrl = V _{CC} - 2.1 V	_	mA	_	_	1.0
Switching Noise	Generated from DC-DC Converter with recommended capacitors	3.5 MHz	dBm	_	-93	_
Thermal Resistance θjc	_	_	°C/W	1	15	_

^{4.} During turn-on, the device requires an initial start up current (lcc) specified as "Turn-on Current". Once operational, lcc will drop to the specified levels.

Absolute Maximum Ratings ^{6,7}

Parameter	Absolute Maximum	
Max. Input Power 0.05 GHz 0.5 - 3.5 GHz	+27 dBm +34 dBm	
V _{CC}	-0.5V ≤ V _{CC} ≤ +6.0V	
Vin ⁸	-0.5V ≤ Vin ≤ V _{CC} + 0.5V	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-65°C to +125°C	

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- Standard CMOS TTL interface, latch-up will occur if logic signal is applied prior to power supply.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Moisture Sensitivity

The MSL rating for this part is defined as Level 2 per IPC/JEDEC J-STD-020. Parts shall be stored and/or baked as required for MSL Level 2 parts.

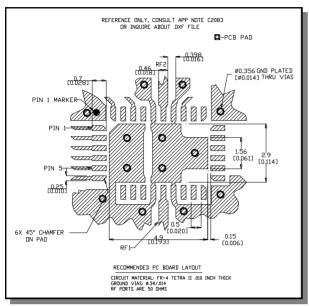
^{5.} The DC-DC converter is guaranteed to start in 100 μs as long as the power supplies have the maximum turn-on current available for start-up.



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Recommended PCB Configuration9



 Application Note C2083 is available on line at www.macom.com

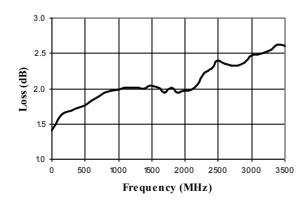
Truth Table (Digital Attenuator)

C8	C4	C2	C1	C0.5	Attenuation
0	0	0	0	0	Loss, Reference
0	0	0	0	1	0.5 dB
0	0	0	1	0	1.0 dB
0	0	1	0	0	2.0 dB
0	1	0	0	0	4.0 dB
1	0	0	0	0	8.0 dB
1	1	1	1	1	15.5 dB

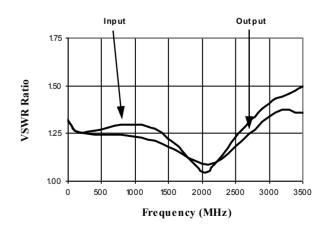
0 = TTL Low; 1 = TTL High

Typical Performance Curves

Insertion Loss



VSWR @ Insertion Loss



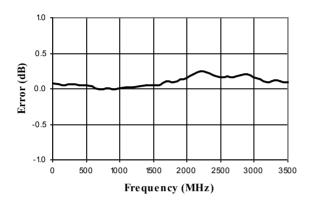


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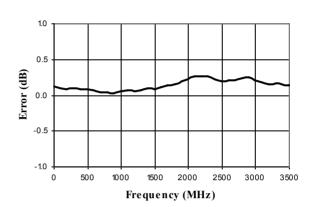
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Typical Performance Curves

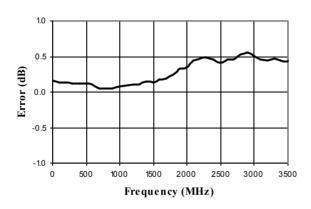
Attenuation Error, 0.5 dB Bit



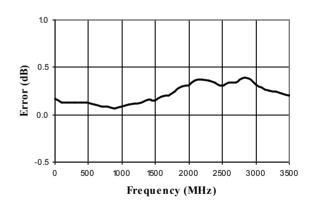
Attenuation Error, 1 dB Bit



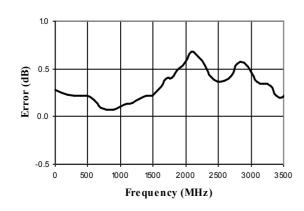
Attenuation Error, 2 dB Bit



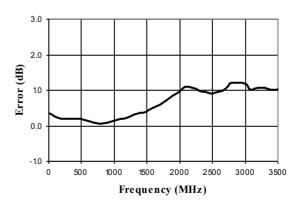
Attenuation Error, 4 dB Bit



Attenuation Error, 8 dB Bit



Attenuation Error, Max. Attenuation



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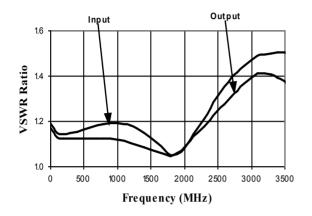


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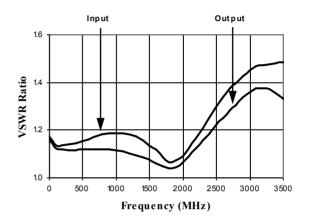
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Typical Performance Curves

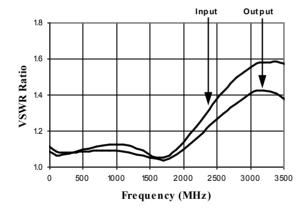
VSWR, 0.5 dB Bit



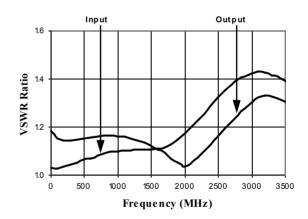
VSWR, 1 dB Bit



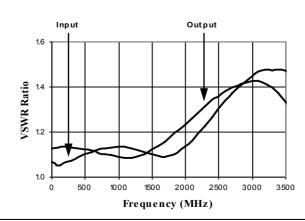
VSWR, 2 dB Bit



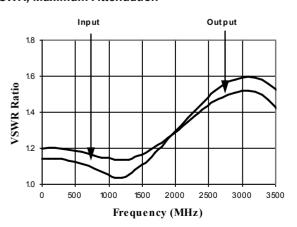
VSWR, 4 dB Bit



VSWR, 8 dB Bit



VSWR, Maximum Attenuation

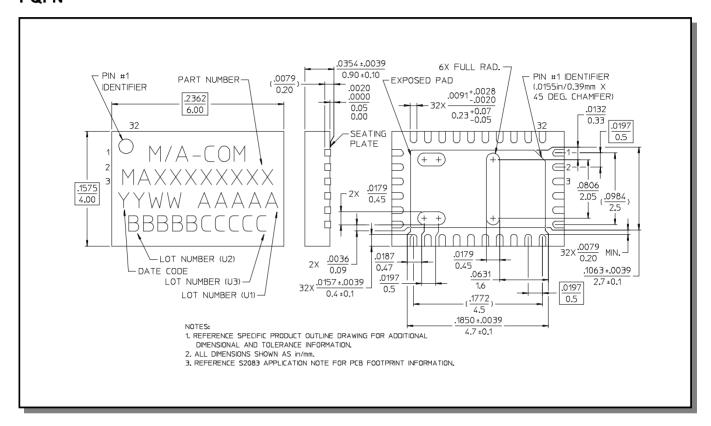




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CSP-1, Lead-Free 4 x 6 mm, 32-lead PQFN[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.



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