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# NPN Silicon Power Transistors High Voltage Planar

The MJW18020 planar High Voltage Power Transistor is specifically Designed for motor control applications, high power supplies and UPS's for which the high reproducibility of DC and Switching parameters minimizes the dead time in bridge configurations.

### **Features**

- High and Excellent Gain Linearity
- Fast and Very Tight Switching Times Parameters tsi and tfi
- Very Stable Leakage Current due to the Planar Structure
- High Reliability
- Pb-Free Package is Available\*

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-Emitter Sustaining Voltage	V <sub>CEO</sub>	450	Vdc
Collector-Emitter Breakdown Voltage	V <sub>CES</sub>	1000	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	1000	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	9.0	Vdc
Collector Current - Continuous - Peak (Note 1)	I <sub>C</sub>	30 45	Adc
Base Current - Continuous - Peak (Note 1)	I <sub>B</sub>	6.0 10	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	250 2.0	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	50	°C/W
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	TL	275	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

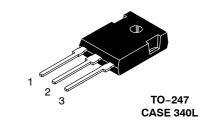
1. Pulse Test: Pulse Width = 5  $\mu$ s, Duty Cycle  $\leq$  10%.



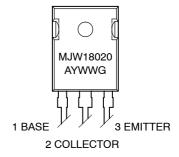
### ON Semiconductor®

http://onsemi.com

## 30 AMPERES 1000 VOLTS BV<sub>CES</sub> 450 VOLTS BV<sub>CEO</sub>, 250 WATTS



### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week

G = Pb-Free Package

### **ORDERING INFORMATION**

Device	Package	Shipping
MJW18020	TO-247	30 Units/Rail
MJW18020G	TO-247 (Pb-Free)	30 Units/Rail

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characte	ristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector-Emitter Sustaining Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)		V <sub>CEO(sus)</sub>	450	_	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>CEO</sub> , I <sub>B</sub> = 0)		I <sub>CEO</sub>	_	_	100	μAdc
Collector Cutoff Current (V <sub>CE</sub> = Rated V <sub>C</sub> (T <sub>C</sub> = 125°C)	ces, V <sub>EB</sub> = 0)	I <sub>CES</sub>	-	-	100 500	μAdc
Emitter Cutoff Current ( $V_{CE} = 9 \text{ Vdc}, I_{C} = 0$ )		I <sub>EBO</sub>	-	-	100	μAdc
ON CHARACTERISTICS			II.	1	I.	
DC Current Gain $(I_C = 3 \text{ Adc}, V_{CE} = 5 \text{ Adc})$ $(I_C = 10 \text{ Adc})$ $(I_C = 20 \text{ Adc})$ $(I_C = 20 \text{ Adc})$	$(T_C = 125^{\circ}C)$ $(T_C = 125^{\circ}C)$ $(T_C = 125^{\circ}C)$ $(T_C = 125^{\circ}C)$	h <sub>FE</sub>	14 - 8 5 5.5 4	30 16 14 9 7	34 - - - -	
(I <sub>C</sub> = 10 mAdc V <sub>CE</sub> =	= 5 Vdc)		14	25	_	
Base-Emitter Saturation Voltage $(I_C = 1 (I_C = 2))$	0 Adc, I <sub>B</sub> = 2 Adc) 0 Adc, I <sub>B</sub> = 4 Adc)	$V_{BE(sat)}$	_	0.97 1.15	1.25 1.5	Vdc
Collector–Emitter Saturation Voltage ( $I_C = 10 \text{ Adc}, I_B = 2 \text{ Adc}$ ) ( $I_C = 20 \text{ Adc}, I_B = 4 \text{ Adc}$ )  DYNAMIC CHARACTERISTICS	(T <sub>C</sub> = 125°C) (T <sub>C</sub> = 125°C)	V <sub>CE(sat)</sub>	- - - -	0.2 0.3 0.5 0.9	0.6 - 1.5 2.0	Vdc
Current Gain Bandwidth Product		f <sub>T</sub>	_	13	_	MHz
$(I_C = 1 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 1 \text{ MHz})$						
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 1 MHz)		C <sub>ob</sub>	-	300	500	pF
Input Capacitance (V <sub>EB</sub> = 8.0)		C <sub>ib</sub>	-	7000	9000	pF
SWITCHING CHARACTERISTICS: Res	istive Load (D.C. = 10%, Pulse Width	= 70 μs)	-1		<u>I</u>	
Turn-On Time	$(I_C = 10 \text{ Adc}, I_{B1} = I_{B2} = 2 \text{ Adc},$ Vcc = 125  V)	t <sub>On</sub>	-	540	750	ns
Storage Time		t <sub>s</sub>	_	4.75	6	μs
Fall Time		t <sub>f</sub>	_	380	500	ns
Turn-Off Time		t <sub>Off</sub>	_	5.2	6.5	μs
Turn-On Time	$(I_C=20 \text{ Adc}, I_{B1}=I_{B2}=4 \text{ Adc},$	t <sub>On</sub>	_	965	1200	ns
Storage Time	Vcc = 125 V)	t <sub>s</sub>	-	2.9	3.5	μs
Fall Time		t <sub>f</sub>	-	350	500	ns
Turn-Off Time		t <sub>Off</sub>	-	3.25	4	μs
SWITCHING CHARACTERISTICS: Indu	ective Load (V <sub>clamp</sub> = 300 V , Vcc = 15	V, L = 200 μH)				
Fall Time	$(I_C = 10 \text{ Adc}, I_{B1} = I_{B2} = 2 \text{ Adc})$	t <sub>fi</sub>	-	142	250	ns
Storage Time		t <sub>si</sub>	-	4.75	6	μs
Crossover Time	1	t <sub>c</sub>	-	320	500	ns
Fall Time	(I <sub>C</sub> = 20 Adc, I <sub>B1</sub> = I <sub>B2</sub> = 4 Adc)	t <sub>fi</sub>	-	350	500	ns
Storage Time		t <sub>si</sub>	-	3.0	3.5	μs
	Crossover Time					

### TYPICAL CHARACTERISTICS

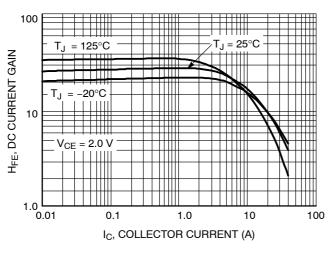


Figure 1. DC Current Gain, V<sub>CE</sub> = 2.0 V

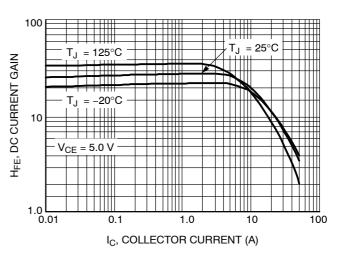


Figure 2. DC Current Gain, V<sub>CE</sub> = 5.0 V

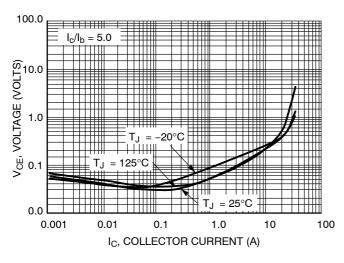


Figure 3. Typical Collector–Emitter Saturation Voltage,  $I_{\text{C}}/I_{\text{B}} = 5.0$ 

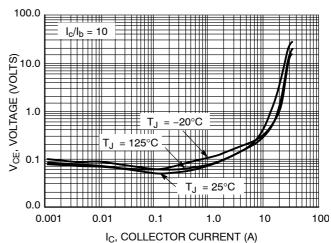


Figure 4. Typical Collector–Emitter Saturation Voltage,  $I_C/I_B = 10$ 

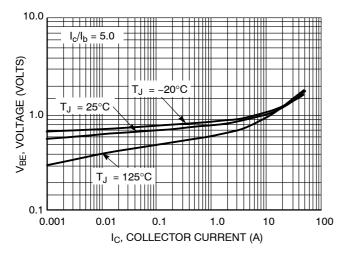


Figure 5. Typical Base–Emitter Saturation Voltage,  $I_C/I_B = 5.0$ 

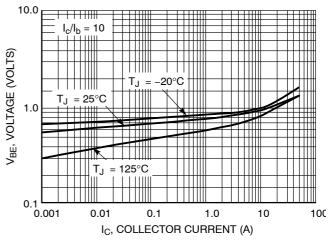


Figure 6. Typical Base–Emitter Saturation Voltage, I<sub>C</sub>/I<sub>B</sub> = 10

### **TYPICAL CHARACTERISTICS**

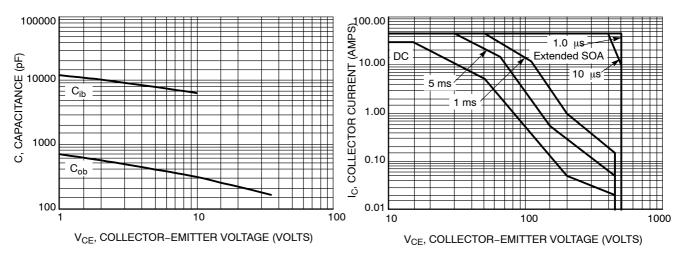


Figure 7. Typical Capacitance

Figure 8. Forward Bias Safe Operating Area

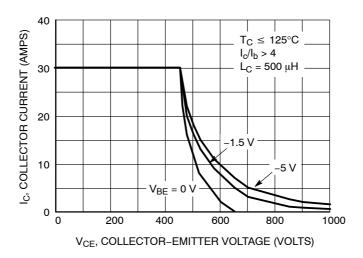
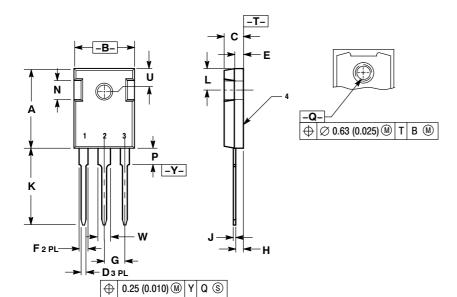


Figure 9. Reverse Bias Safe Operating Area

### PACKAGE DIMENSIONS

**TO-247** CASE 340L-02 ISSUE E



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Е	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215 BSC		
Н	1.50	2.49	0.059	0.098	
ſ	0.40	0.80	0.016	0.031	
K	19.81	20.83	0.780	0.820	
٦	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
Р		4.50		0.177	
Q	3.55	3.65	0.140	0.144	
U	6.15	6.15 BSC		BSC	
W	2.87	3.12	0.113	0.123	

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