



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
20V	28mΩ @ V _{GS} = 4.5V	7.63A
	41mΩ @ V _{GS} = 2.5V	4.35A

Description

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power Management Functions
- DC-DC Converters

Features

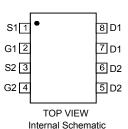
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching SpeedLow Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

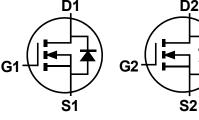
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.072 grams (approximate)



TOP VIEW





N-Channel MOSFET

N-Channel MOSFET

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2041LSD-13	SO-8	2,500/Tape & Reel

SO-8

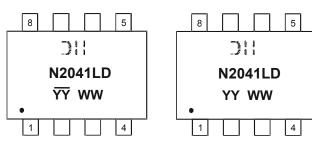
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



Chengdu A/T Site

Shanghai A/T Site

Control Control<



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Char	acteristic		Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Note 5)	Steady State	T _A = +25°C T _A = +85°C	ID	7.63 4.92	A
Pulsed Drain Current (Note 6)			I _{DM}	30	A

Thermal Characteristics

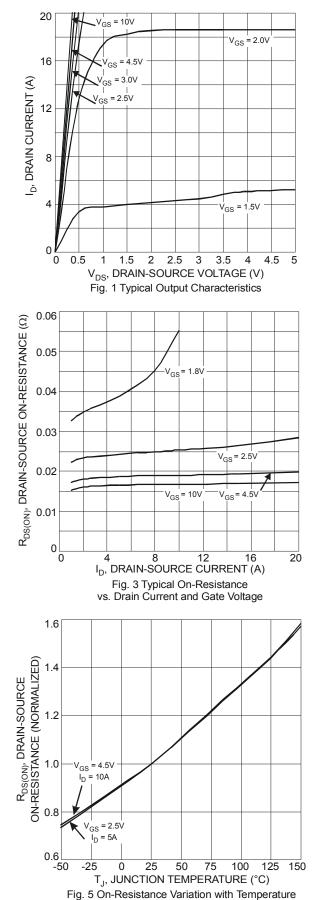
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.16	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	R _{0JA}	107.4	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

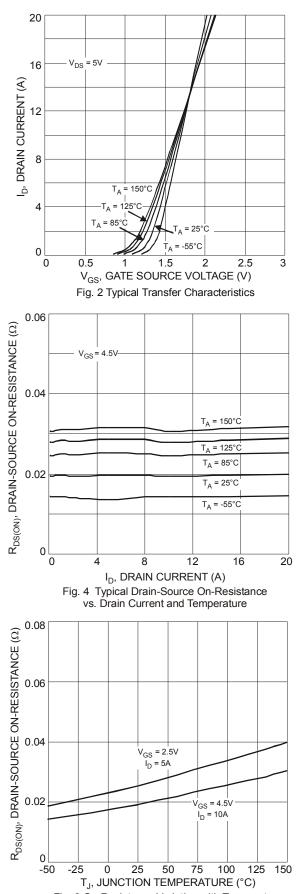
Electrical Characteristics (@T _A = +25°C, unless otherwise specified.)						
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	· ·					
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS		—	1	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.5	—	1.2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	Pro (out)		19	28	mΩ	V _{GS} = 4.5V, I _D = 6A
	R _{DS} (ON)		25	41		V _{GS} = 2.5V, I _D = 5.2A
Forward Transfer Admittance	Y _{fs}		6	—	S	V _{DS} = 10V, I _D = 6A
Diode Forward Voltage	V _{SD}		0.7	1.2	V	V _{GS} = 0V, I _S = 1.7A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	550	—		V _{DS} =10V, V _{GS} = 0V, f = 1MHz
Output Capacitance	Coss	_	88	—	pF	
Reverse Transfer Capacitance	Crss		81	—		
Gate Resistance	Rg		1.34	_	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Qg	_	15.6		nC	V _{GS} = 10V, V _{DS} = 10V, I _D = 6A
Total Gate Charge	Qg		7.2	_		V _{GS} = 4.5 V, V _{DS} = 10V, I _D = 6A
Gate-Source Charge	Q _{gs}	-	1	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.9	_		
Turn-On Delay Time	t _{D(on)}	_	4.69	—		V_{DD} = 10V, V_{GEN} = 4.5V, R _g = 1Ω, I _D = 6.7A
Turn-On Rise Time	tr	_	13.19	—		
Turn-Off Delay Time	t _{D(off)}		22.1		ns	
Turn-Off Fall Time	t _f	_	6.43	_		

 Device mounted on FR-4 PCB with minimum recommended pad layout.
Repetitive rating, pulse width limited by function temperature.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing. Notes:



DMN2041LSD





1.2

20

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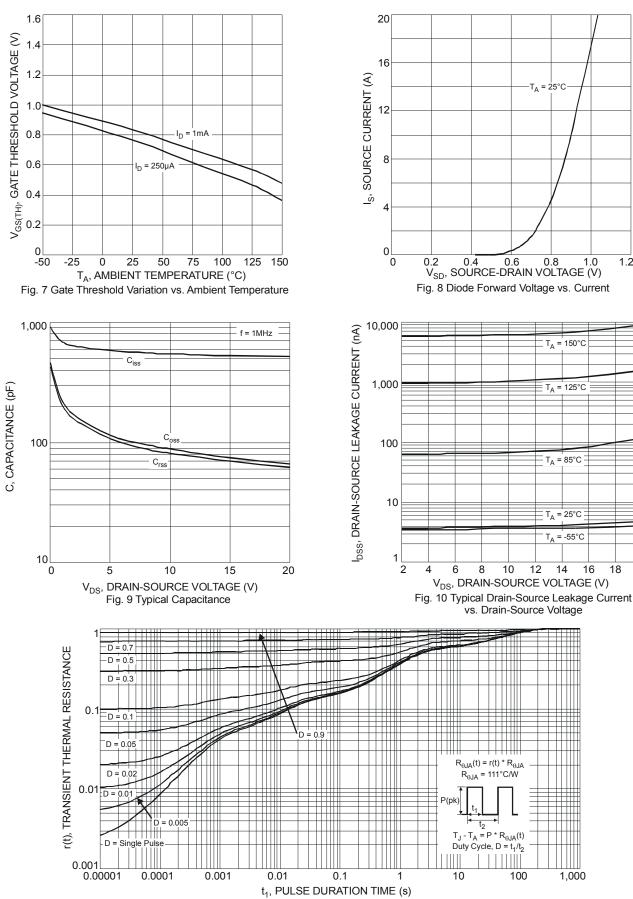


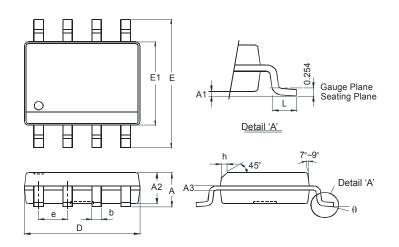
Fig. 11 Transient Thermal Response

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Package Outline Dimensions

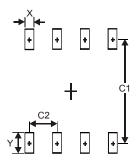
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8				
Dim	Min	Max		
Α	-	1.75		
A1	0.10	0.20		
A2	1.30	1.50		
A3	0.15	0.25		
b	0.3	0.5		
D	4.85	4.95		
E	5.90	6.10		
E1	3.85	3.95		
e	1.27 Typ			
h	-	0.35		
L	0.62	0.82		
θ	0°	8°		
All Di	All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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