

COMPLIANT

HALOGEN FREE

Ultrafast Rectifier, 20 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	20 A			
V_R	600 V			
V _F at I _F	1.26 V			
t _{rr} (typ.)	61 ns			
T _J max.	175 °C			
Package	TO-220 FullPAK 2L			
Circuit configuration	Single			

FEATURES

- · Low forward voltage drop
- · Ultrafast soft recovery time
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

Ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PC, TV and monitor, games units and DVD AC/DC power supplies.

MECHANICAL DATA

Case: TO-220 FullPAK 2L

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current in DC	I _{F(AV)}	T _C = 102 °C	20	Α	
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	190	A	
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	600	-	-	
Forward voltage	V	I _F = 20 A	-	1.4	1.63	V
Forward voltage V _F	٧F	I _F = 20 A, T _J = 125 °C	-	1.26	1.49	
Payaraa laakaga ayrrant	1	$V_R = V_R$ rated	-	0.3	15	
Reverse leakage current	I _R	$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	50	500	μΑ
Junction capacitance	C _T	V _R = 600 V	-	18	=	pF
Series inductance	L _S	L _S Measured lead to lead 5 mm from package body - 8 -		nH		



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		T _J = 25 °C		-	61	-	ns
neverse recovery time	t _{rr}	T _J = 125 °C	l _F = 20 A dl _F /dt = 1000 A/μs	-	87	-	
Peak recovery current		T _J = 25 °C		-	13	-	Α
Feak recovery current	I _{RRM}	T _J = 125 °C	$V_{\rm R} = 400 \text{ V}$	-	21	-	A
Dayleysa ya sayley ya baysa	0	T _J = 25 °C	*H = 100 *	-	480	-	nC
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	1080	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to case	R _{thJC}		-	2.5	3	
Thermal resistance, junction to ambient	R_{thJA}	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2	-	g
Weight			-	0.07	-	oz.
Mounting torque			6	_	12	kgf · cm
Mounting torque			(5)	_	(10)	(lbf · in)
Marking device		Case style: 2L TO-220 FullPAK		E4TU	2006FP	

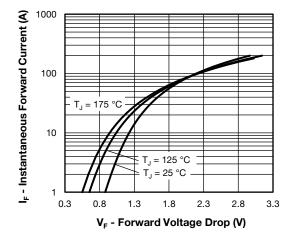


Fig. 1 - Typical Forward Voltage Drop Characteristics

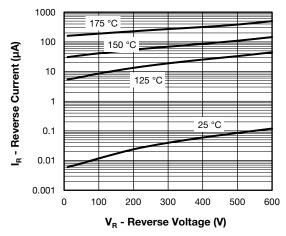


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

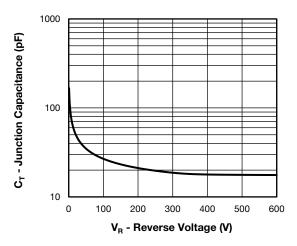


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

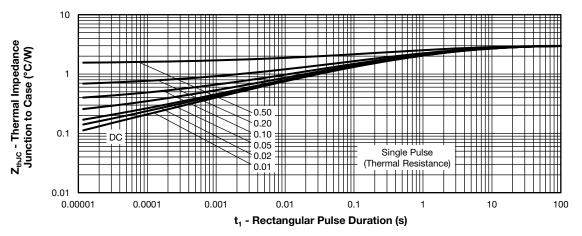


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

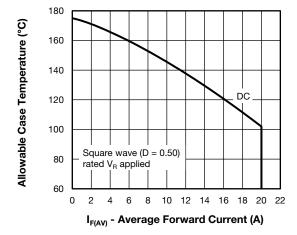


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

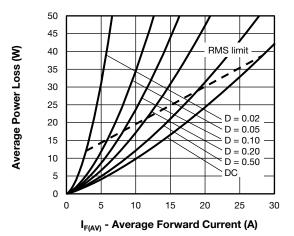


Fig. 6 - Forward Power Loss Characteristics

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Vishay Semiconductors

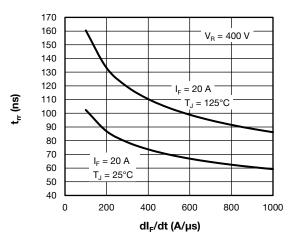


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

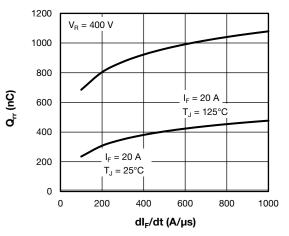


Fig. 8 - Typical Reverse Recovery Charge vs. dl_F/dt

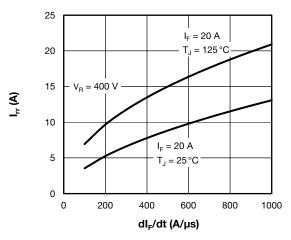
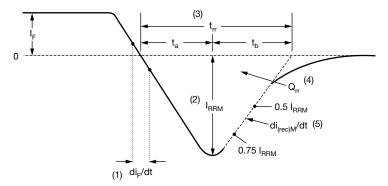


Fig. 9 - Typical Reverse Recovery Current vs. dl_F/dt



- di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

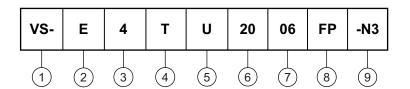
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Circuit configuration:

E = single diode

3 - 4 = Gen 4 FRED Pt

4 - T = TO-220

U = ultrafast recovery time

6 - Current code: 20 = 20 A

7 - Voltage code: 06 = 600 V

8 - FP = FullPAK

9 - Environmental digit:

-N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

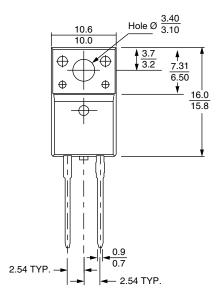
ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-E4TU2006FP-N3	50	1000	Antistatic plastic tube		

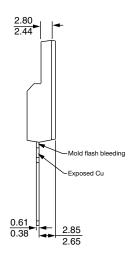
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?96157		
Part marking information	www.vishay.com/doc?95392		
SPICE model	www.vishay.com/doc?96822		

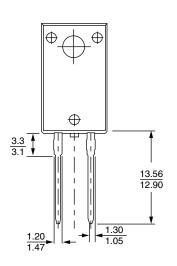


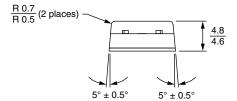
2L TO-220 FullPAK

DIMENSIONS in millimeters









Bottom view



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Vishay

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