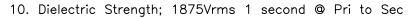
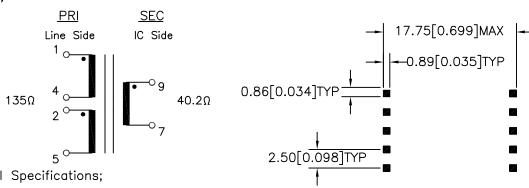
PRELIMINARY 02/23/00 MP HDSL transformer designed for use with Level One HDSL IC chips SK70706/SK70721 at 784, 528 and 400Kbps rates.

- A. Electrical Specifications (@ 25°C)
 - 1. Primary (Line side) Impedance; 135Ω
 - 2. Secondary (IC side) Impedance; 40.2Ω
 - 3. Longitudinal Balance; 50dBm MIN @ 40KHz TO 300KHz, 0dBm (Per FCC part 68.310, pin 7 grounded) 4. Insertion Loss; 0.75dB MAX @ 100KHz, 0dBm

 - 5. Total Harmonic Distortion; -80dB TYP @ 5KHz, 5Vp-p
 - 6. Primary Open Circuit Inductance; 2.92mH ±5% @ 10KHz, 1.0Vrms Measured (1-5) with 2 & 4 shorted
 - 7. DC Current; 160mA
 - 8. DC Resistance; $(1-4)=2.11\Omega \pm 10\%$ $(2-5)=2.11\Omega \pm 10\%$ $(7-9)=2.63\Omega \pm 10\%$
 - 9. Turns Ratio; $(1-4):(2-5)=1:1.00\pm2\%$ $(7-9):(1-5)=1:1.80\pm2\%$ 2 & 4 shorted

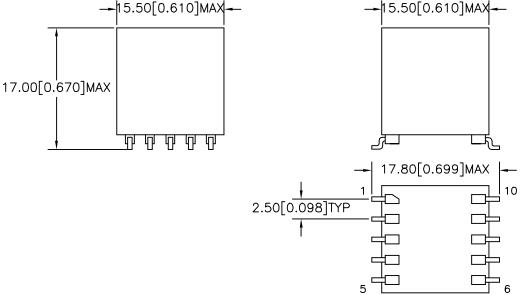


- B. Marking; TTC-4038, TAMURA, date code and country of origin
- C. Safety; Complies with UL1950 3rd Edition and IEC950
- D. Schematic;



Date Code -





PREPARED BY:

D. Rund				
ENGINEER:	DWG CONTROL NO. REV	HDSL		
M. Pitchai	P-A1-12364 ACAD\TTC\A1123641.DWG PRE	TRANSFORMER	TTC-4038	
QUALITY CONTROL:	ICONTENTS OF THIS DRAWING ARE	TAMURA CORPORATION OF AMERICA	MODEL SPECIFICATIO	Ν
D. Kelley	PRIOR NOTICE	43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (909) 699-1270 FAX 9096769482	DIM: mm(In) SCL: 2/1 SH: 1 ()F 1
APPROVED:		DINT OF POOLINENT AND CURRENT MATTER DICOLOGER HE		TANUIDA

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