



20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	27mΩ @ V _{GS} = -4.5V	-7.6A
-20V	32mΩ @ V _{GS} = -2.5V	-6.7A
-20 V	50mΩ @ V _{GS} = -1.8V	-5.2A
	90mΩ @ V _{GS} = -1.5V	-3.9A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- · Battery management applications
- Power management functions
- DC-DC converters

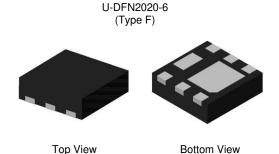
Features

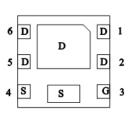
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

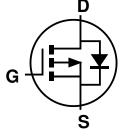
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: U-DFN2020-6
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 <a> §
- Weight: 0.007 grams (Approximate)







Pin Out Bottom View

Internal Schematic

Ordering Information (Note 4)

Part Number	Dookogo	Morking	Reel Size (inches)	Packing		
Part Number	Package Marking		neer Size (Inches)	Qty.	Carrier	
DMP2023UFDF-7	U-DFN2020-6 (Type F)	3F	7	3,000	Reel	
DMP2023UFDF-13	U-DFN2020-6 (Type F)	3F	13	10,000	Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



3F = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2014		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	В		J	K	L	М	N	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



3F = Product Type Marking Code YWX = Date Code Marking

Y = Year (ex: 2 = 2022)
W = Week (ex: a = Week 27; z Represents Week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Kev

Year	2014		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	4		2	3	4	5	6	7	8	9	0	1
Week	Week 1-26			27-52				53				
Code		A	-Z	•		a	-Z			Z	2	

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Υ	Z



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Drain Current (Note 5) V 45V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	ΙD	-7.6 -6.1	А
Continuous Drain Current (Note 5) V _{GS} = -4.5V	t < 5s	$T_A = +25$ °C $T_A = +70$ °C	l _D	-9.5 -7.6	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	5)		I _{DM}	-40	Α
Continuous Source-Drain Diode Current T _A = +25°C			ls	-2	Α
Avalanche Current (Note 6) L = 0.1mH	I _{AS}	- 23	А		
Avalanche Energy (Note 6) L = 0.1mH			Eas	27	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 7)	T _A = +25°C	Pn	0.73	W
Total Fower Dissipation (Note 7)	$T_A = +70^{\circ}C$	PD	0.47	VV
Thermal Resistance. Junction to Ambient (Note 7)	Steady State	R _{0,JA}	171	°C/W
Thermal nesistance, Junction to Ambient (Note 1)	t < 5s	ΠθJA	112	C/ V V
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	D-	2.03	w
Total Fower Dissipation (Note 3)	$T_A = +70$ °C	P _D	1.30	VV
Thormal Posistanas, Junation to Ambient (Note 5)	Steady State	Rела	62	
Thermal Resistance, Junction to Ambient (Note 5)	t < 5s	МӨЈА	40	°C/W
Thermal Resistance, Junction to Case (Note 5)	Steady State	Rejc	9.3	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BVDSS	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	-1	μΑ	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						•
Gate Threshold Voltage	V _{GS(TH)}	-0.4		-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
			_	27		$V_{GS} = -4.5V, I_{D} = -7.0A$
Static Drain-Source On-Resistance	D		_	32	mΩ	$V_{GS} = -2.5V$, $I_{D} = -5.0A$
Static Drain-Source On-Resistance	Rds(on)	_	_	50	11122	$V_{GS} = -1.8V$, $I_{D} = -3.0A$
			_	90		$V_{GS} = -1.5V, I_D = -1.0A$
Diode Forward Voltage	V _{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -1.0A$
DYNAMIC CHARACTERISTICS (Note 9)		•				•
Input Capacitance	Ciss	_	1837	_		151/1/ 01/
Output Capacitance	Coss	_	131	_	рF	$V_{DS} = -15V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	115	_		1 = 1.01011 12
Gate Resistance	Rg	_	14.8	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	27	_		\\ 45\\ \\ 45\\
Gate-Source Charge	Qgs	_	2.8	_	nC	V _{DS} = -15V, V _{GS} = -4.5V I _D = -4.0A
Gate-Drain Charge	Qgd	_	3.1	_		ID = -4.0A
Turn-On Delay Time	tD(ON)	_	5.8	_		
Turn-On Rise Time	tr	_	19.3	_		$V_{DS} = -15V, V_{GS} = -4.5V$
Turn-Off Delay Time	t _{D(OFF)}	_	168.5	_	ns	$R_G = 1\Omega, I_D = -4.0A$
Turn-Off Fall Time	tr	_	77.3	_		
Reverse Recovery Time	trr	_	46.5	_	ns	IF = -1.0A, dI/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	_	33.8	_	nC	I _F = -1.0A, dI/dt = 100A/μs

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

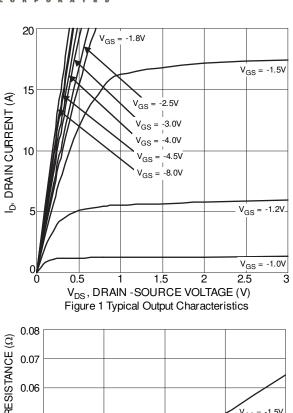
6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_{J} = +25°C.

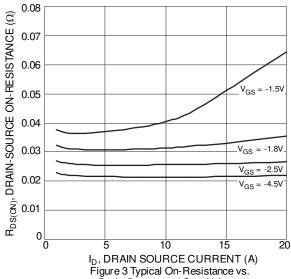
7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

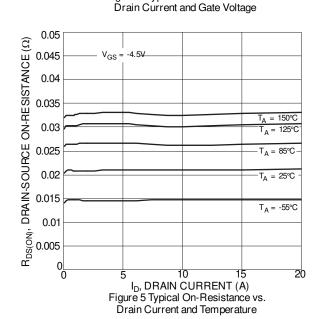
8. Short duration pulse test used to minimize self-heating effect.

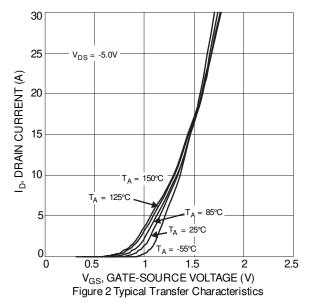
9. Guaranteed by design. Not subject to product testing.

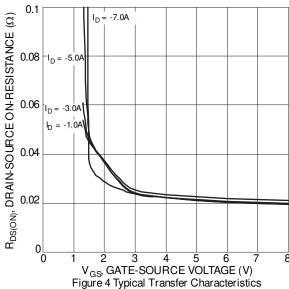












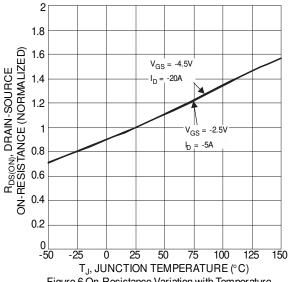
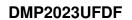
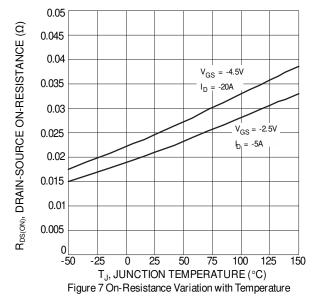
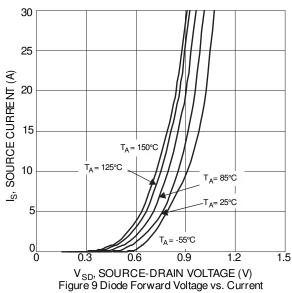


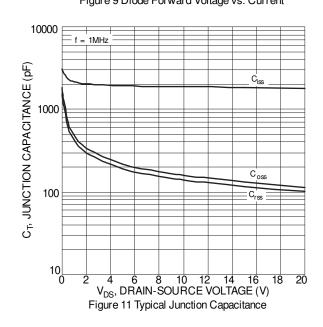
Figure 6 On-Resistance Variation with Temperature











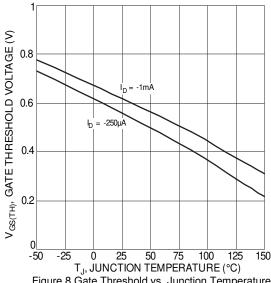


Figure 8 Gate Threshold vs. Junction Temperature

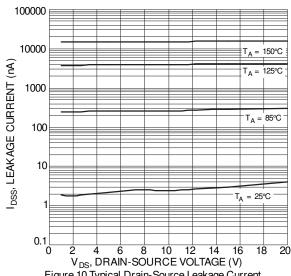
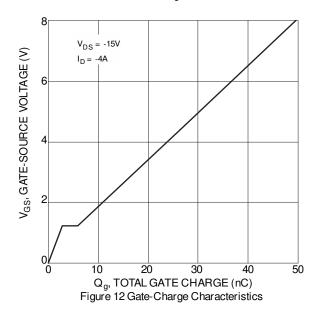
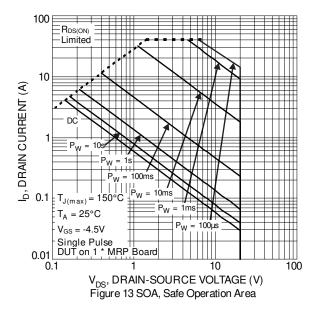
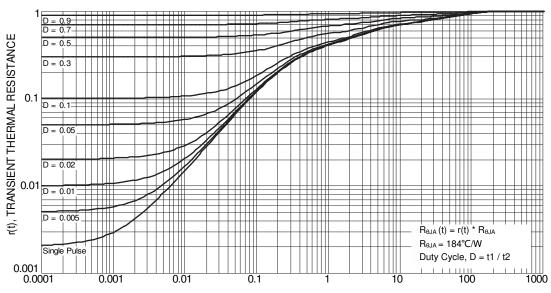


Figure 10 Typical Drain-Source Leakage Current vs. Voltage









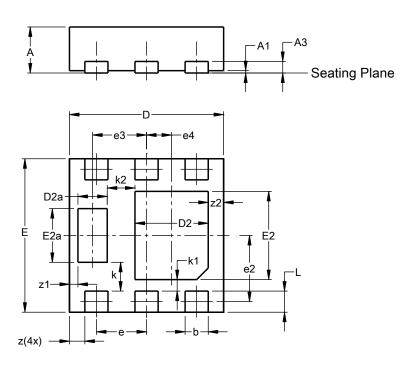
t1, PULSE DURATION TIME (sec) Figure 14 Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

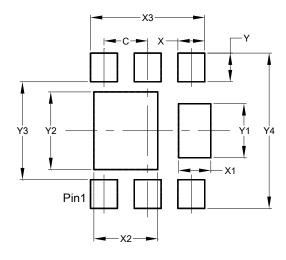


	U-DFN2020-6 (Type F)							
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
А3	-	1	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
Е	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е		0.65 BS						
e2	C	.863 BS	SC					
е3		0.70 BS	С					
e4	C	.325 BS	SC					
k		0.37 BS	С					
k1	(0.15 BS	С					
k2		0.36 BS						
L	0.225 0.325 0.275							
Z		0.20 BS						
z1	_).110 BS						
z2		0.20 BS	_					
All D	imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value
	(in mm)
С	0.650
X	0.400
X1	0.480
X2	0.950
Х3	1.700
Υ	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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