






SPECIFICATIONS

CUSTOMER : _____
MODEL NO. : **GFE128064FA-YPAE**
VERSION : **B**
DATE : **2022.12.01**
CERTIFICATION : **ROHS**

Customer Sign	Approved By	Prepared By	Prepared By
			

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CONTENTS

1. Scope	-----	4
2. Product Specifications	-----	4
2.1 General	-----	4
2.2 Mechanical Characteristics	-----	4
2.3 Absolute Maximum Ratings	-----	5
2.4 Electrical Characteristics	-----	5
2.5 Optical Characteristics Absolute maximum ratings	-----	5
2.6 Optical Characteristics	-----	6
2.7 LED Back-light Characteristics	-----	7
3. Reliability	-----	8
4. Operating Instructions	-----	9
4.1 Input signal Function	-----	9
4.2 Voltage Generator Circuit	-----	9
4.3 Timing Diagram	-----	10
4.4. Display Command	-----	12
4.5. Circuit Block Diagram	-----	13
5 Notes	-----	14
6 Operation Precautions	-----	14
7 LCM Dimensions	-----	15
8 Package information	-----	16
Appendix : Inspection Standard		



1. SCOPE

This specification covers the engineering requirements for the GFE128064FA-YPAE liquid crystal module.

2. PRODUCT SPECIFICATIONS

2.1 General

- 128 × 64 dot matrix LCD
- STN (Yellow-Green), Positive mode LCD panel
- Transflective Wide temperature type
- 6 o'clock
- Multiplexing driving : 1/64duty, 1/9bias
- Controller IC : SBN6400 ,SBN0064 or Compatible
- Backlight: Yellow-Green

2.2 Mechanical Characteristics

Item	Value	Unit
Number of dots	128X64	Dot
Dot size	0.39X0.39	mm
Dot pitch	0.43X0.43	mm
Module dimension	75(W)X52.7(H)X8.4(T)	mm
Viewing Area	60(W)X32.6(H)	mm
Active Area	55 X 27.48	mm
Module	No Connector	
Remark	-	



2.3 Absolute Maximum Ratings (Without LED back-light)

Characteristic	Symbol	Unit	Value
Operating Voltage (logic)	V_{DD}	V	-0.3 to +7.0
Input Voltage	V_{IN}	V	-0.3 to $V_{DD}+0.3$

Note 1: Referenced to $V_{SS}=0V$

2.4 Electrical Characteristics (Without LED back-light)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating Voltage (logic)	$V_{DD}-V_{SS}$	--	--	3.3	--	V
Input Voltage	V_{IH}	--	$0.8V_{DD}$	--	V_{DD}	V
	V_{IL}	--	V_{SS}	--	$0.2V_{DD}$	
Output Voltage	V_{OH}	$I_{OH}=-0.1mA$	$0.8V_{DD}$	--	V_{DD}	V
	V_{HL}	$I_{OL}=0.1mA$	V_{SS}	--	$0.2V_{DD}$	

2.5 Optical Characteristics Absolute maximum ratings

Item	Symbol	Rating	Unit
Operating temperature range	Top	-20~70	°C
Storage temperature range	Tst	-30~80	°C

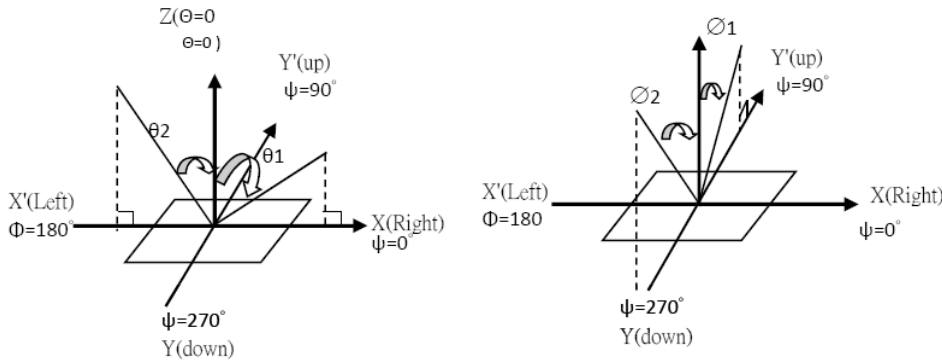


2.6. Optical Characteristics

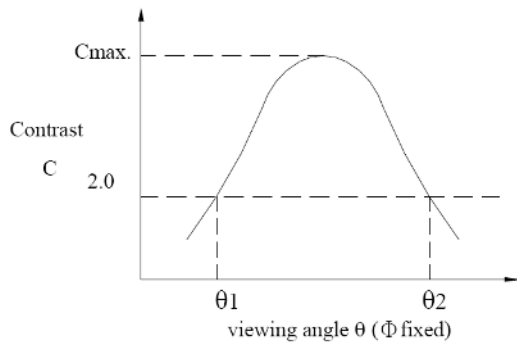
1/64 duty, 1/9 bias, $V_{op}=8.5\text{ V}$, $T_a=25^\circ\text{C}$

Item	Symbol	Conditions	Min.	Typ.	Max	Reference
Driving voltage	V_{op}		8.2	8.5	8.8	
Viewing angle	θ_1 、 θ_2	$C \geq 2.0, \Phi = 0^\circ\text{C}$	30°	-	-	Notes 1 & 2
Contrast	C	$\theta = 5^\circ, \Phi = 0^\circ$	2.0	-	-	Note 3
Response time(rise)	t_{on}	$\theta = 5^\circ, \Phi = 0^\circ$	-	80ms	160ms	Note 4
Response time(fall)	t_{off}	$\theta = 5^\circ, \Phi = 0^\circ$	-	100ms	200ms	Note 4

Note 1: Definition of angles θ and Φ

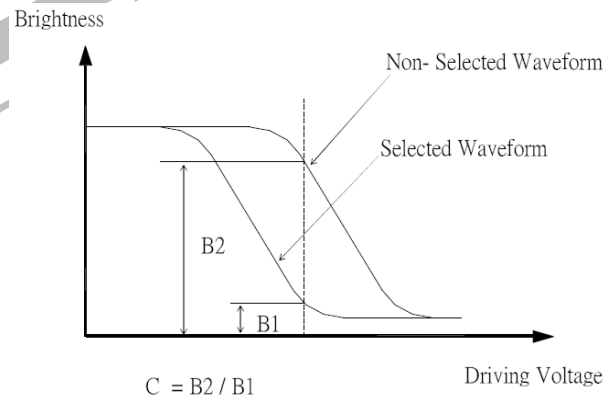


Note 2: Definition of viewing angles θ_1 and θ_2

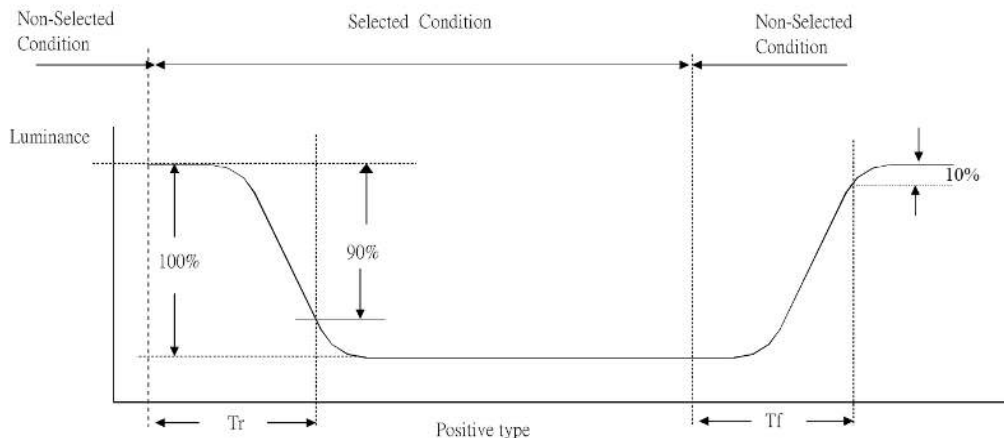


Note : Optimum viewing angle with the naked eye and viewing angle θ at C_{max} . Above are not always the same

Note 3: Definition of contrast C



Note 4: Definition of response time





2.7 LED Back-light Characteristics

2.7.1 Electrical / optical specifications

Ta = 25°C

Item	Symbol	Min	Typ	Max	Unit	Condition
Forward Voltage	V _f	2.7	3.0	3.3	V	I _f =60mA
Reverse Current	I _r	---	---	60	mA	V _r =5V
Peakwavelength	λ _p	565	570	575	nm	I _f =60mA
Spectral Line half width	Δλ	---	30	---	nm	I _f =60mA
Luminance	L _v	600	800	---	cd/m ²	I _f =60mA
Illuminance power deviation	ΔEH	70	---	---	%	I _f =60mA Yellow Green

Note: * Measured at the bare LED back-light unit.

2.7.2 LED Maximum Operating Range

Item	Symbol	White	Unit
Power Dissipation	P _{AD}	264	mW
Forward Current	I _F	120	mA
Reverse Voltage	V _R	5	V



3. RELIABILITY

NO.	ITEM	CONDITION		STANDARD	NOTE
1	High Temp. Storage	80°C	120 hrs	Appearance Without defect	
2	Low Temp. Storage	-30°C	120 hrs	Appearance Without defect	
3	High Temp. & High Humi. Storage	40°C 90% RH	120 hrs	Appearance Without defect	
4	High Temp. Operating Display	70°C	120 hrs	Appearance Without defect	
5	Low Temp. Operating Display	-20°C	120 hrs	Appearance Without defect	
6	Thermal Shock	-20°C, 30min. → 70°C, 30min. ↑ (1cycle)		Appearance Without defect	10 cycles

** Dissipation current, contrast and display functions

** Polarizing filter deterioration, other appearance defects

** The function test shall be conducted after 4hours storage at the normal temperature and humidity after remove from the test chamber.

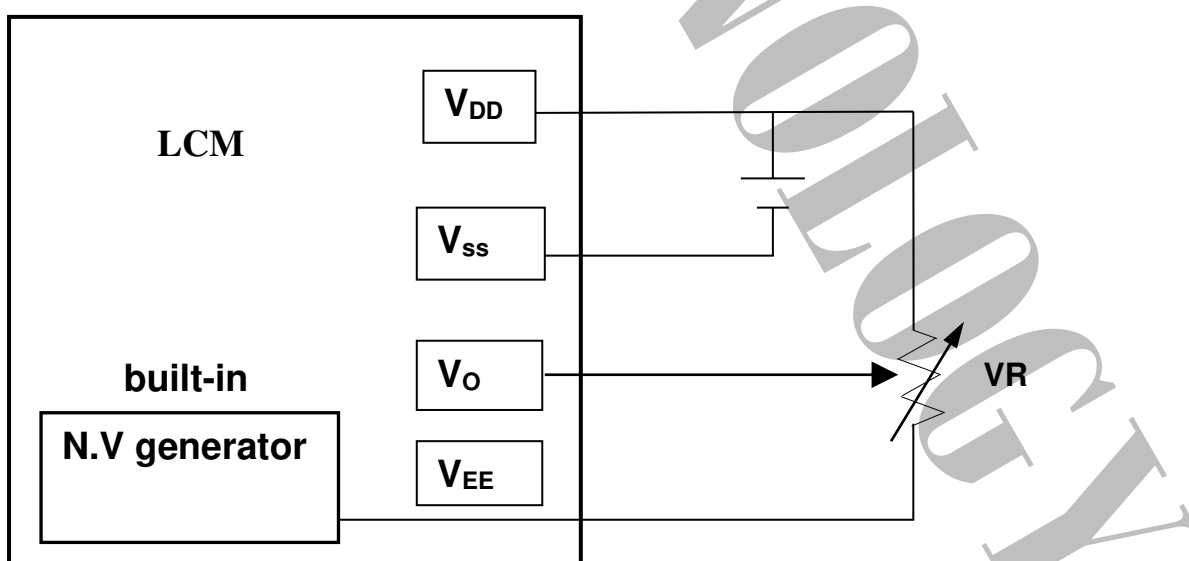


4. OPERATING INSTRUCTIONS

4.1 Input signal Function

Pin No	Symbol	I/O	Function
1	VDD	I	Power supply for logic (+3.3V)
2	VSS	I	Signal ground (GND)
3	Vo	I	Operating voltage for LCD (variable)
4~11	DB0~DB7	I/O	Bi-direction, tri-state 8-bit parallel data bus for interface with a host microcontroller.
12	CS1	I	Chip1 enable (segment 1 to segment 64),Active high
13	CS2	I	Chip2 enable (segment 65 to segment 128),Active high
14	RST	I	Reset signal
15	R/W	I	Data read & write
16	D/I	I	"L" is instruction , "H" is data
17	E	I	Enable signal
18	VEE	O	Negative voltage
19	LEDA(5V)	-	LED backlight drive voltage(+5V)
20	LEDK	-	LED backlight drive voltage ground

4.2 Voltage Generator Circuit

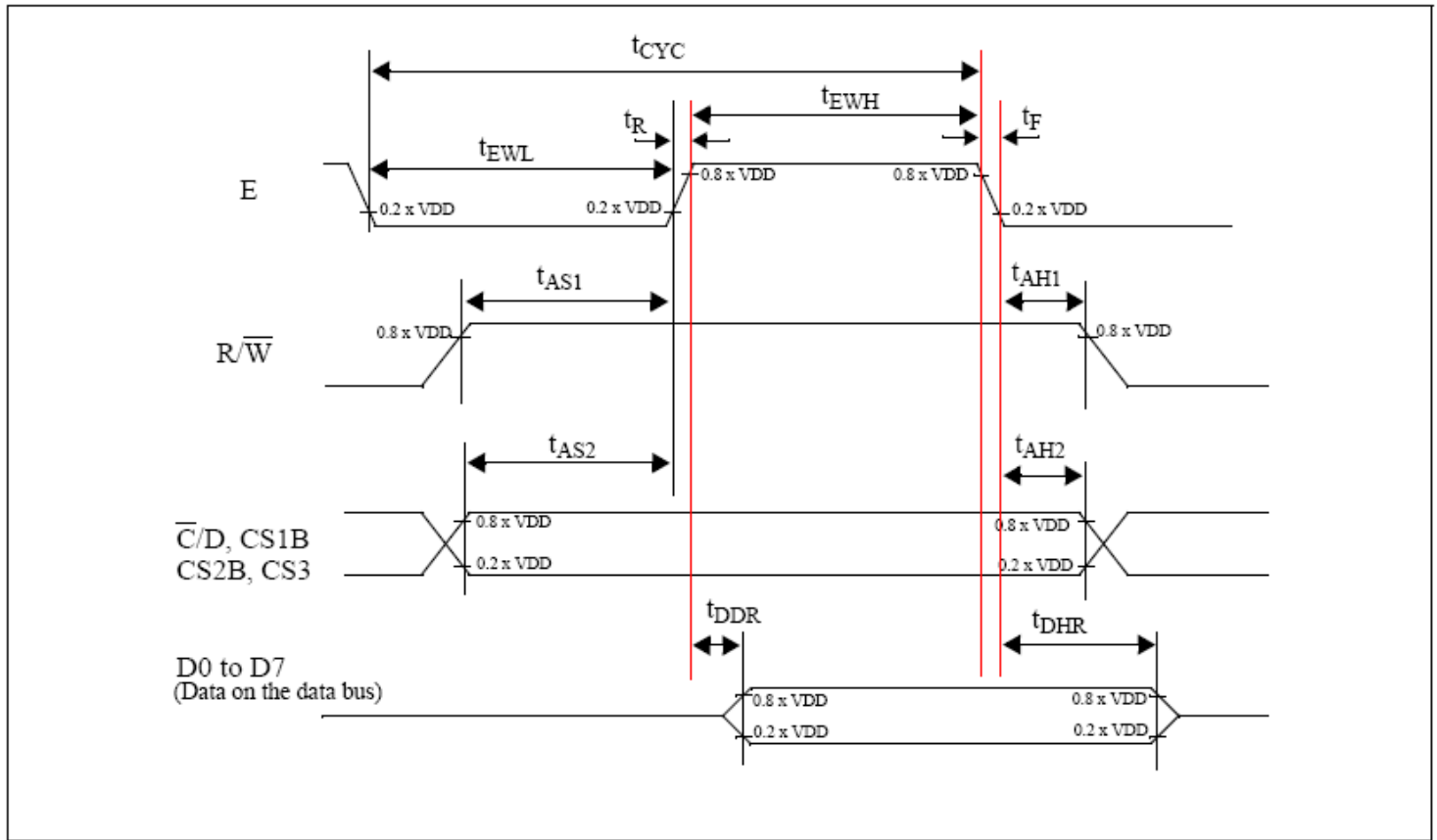


$V_{DD} - V_o$: LCD Driving Voltage
VR : 10K~20K



4.3 Timing Diagram

MPU write timing

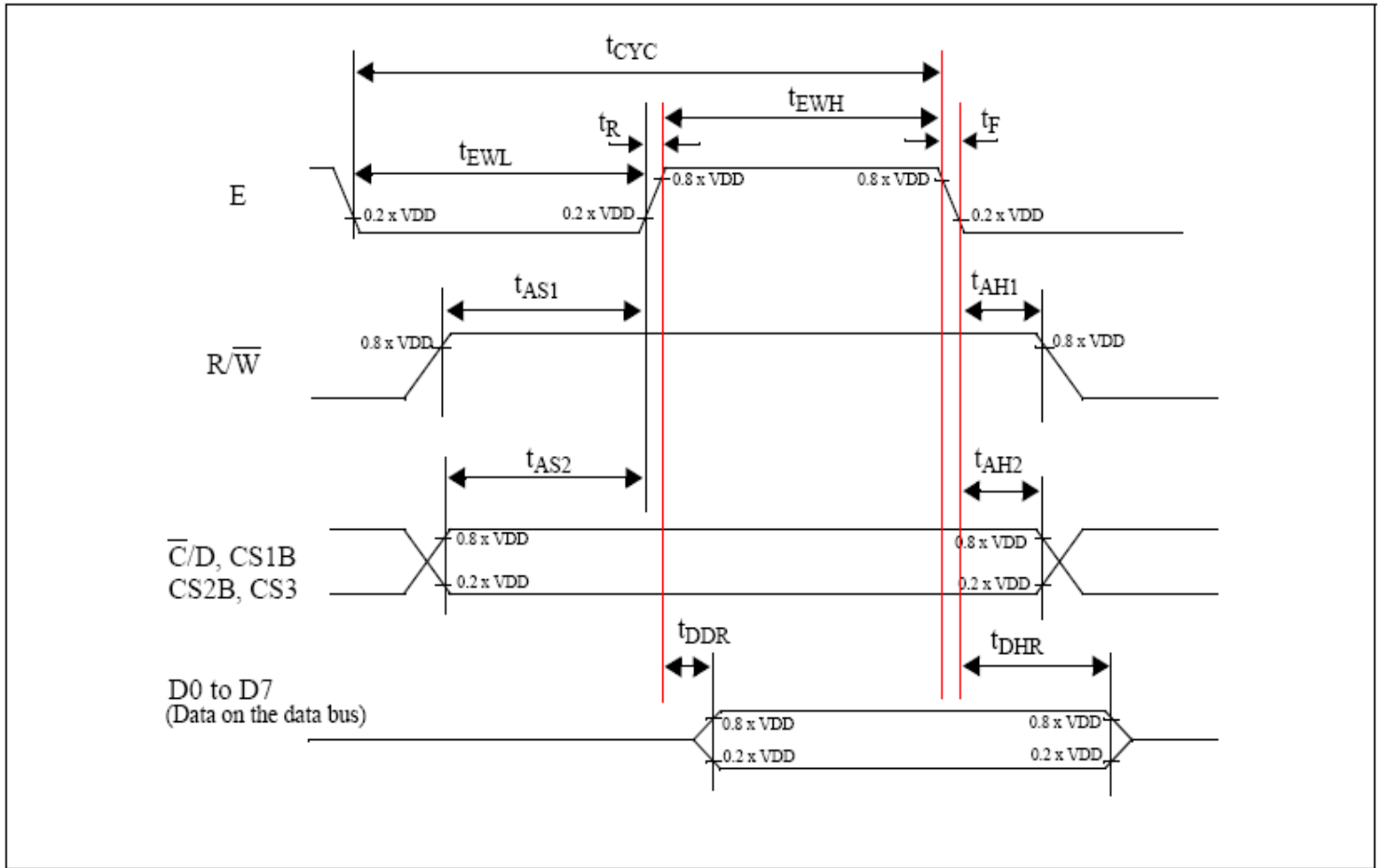


$V_{DD} = 5\text{ V} \pm 10\%$; $V_{SS} = 0\text{ V}$; $T_{amb} = -20\text{ }^\circ\text{C}$ to $+75\text{ }^\circ\text{C}$.

symbol	parameter	min.	max.	test conditions	unit
t_{CYC}	Enable (E) cycle time	1000			ns
t_{EWL}	Enable (E) LOW width	450			
t_{EWH}	Enable (E) HIGH width	450			
t_R	Enable (R) rise time		20		
t_F	Enable (F) fall time		20		
t_{AS1}	Write set-up time	140			
t_{AH1}	Write hold time	10			
t_{AS2}	C/D, CS1B, CS2B, CS3 set-up time	140			
t_{AH2}	C/D, CS1B, CS2B, CS3 hold time	10			
t_{DSW}	Data setup time (on the data bus)	200		The loading on the data bus is shown in Fig. 18.	
t_{DHW}	Data hold time (on the data bus)	10			



MPU read timing



$V_{DD} = 5V \pm 10\%$; $V_{SS} = 0V$; $T_{amb} = -20^\circ C$ to $+75^\circ C$.

symbol	parameter	min.	max.	test conditions	unit
t_{CYC}	Enable (E) cycle time	1000			ns
t_{EWL}	Enable (E) LOW width	450			
t_{EWH}	Enable (E) HIGH width	450			
t_R	Enable (R) rise time		20		
t_F	Enable (F) fall time		20		
t_{AS1}	READ set-up time	140			
t_{AH1}	READ hold time	20			
t_{AS2}	C/D, CS1B, CS2B, CS3 set-up time	140			
t_{AH2}	C/D, CS1B, CS2B, CS3 hold time	10			
t_{DDR}	Data delay time (on the data bus)	320		The loading on the data bus is shown in Fig. 18.	
t_{DHR}	Data hold time (on the data bus)	20			

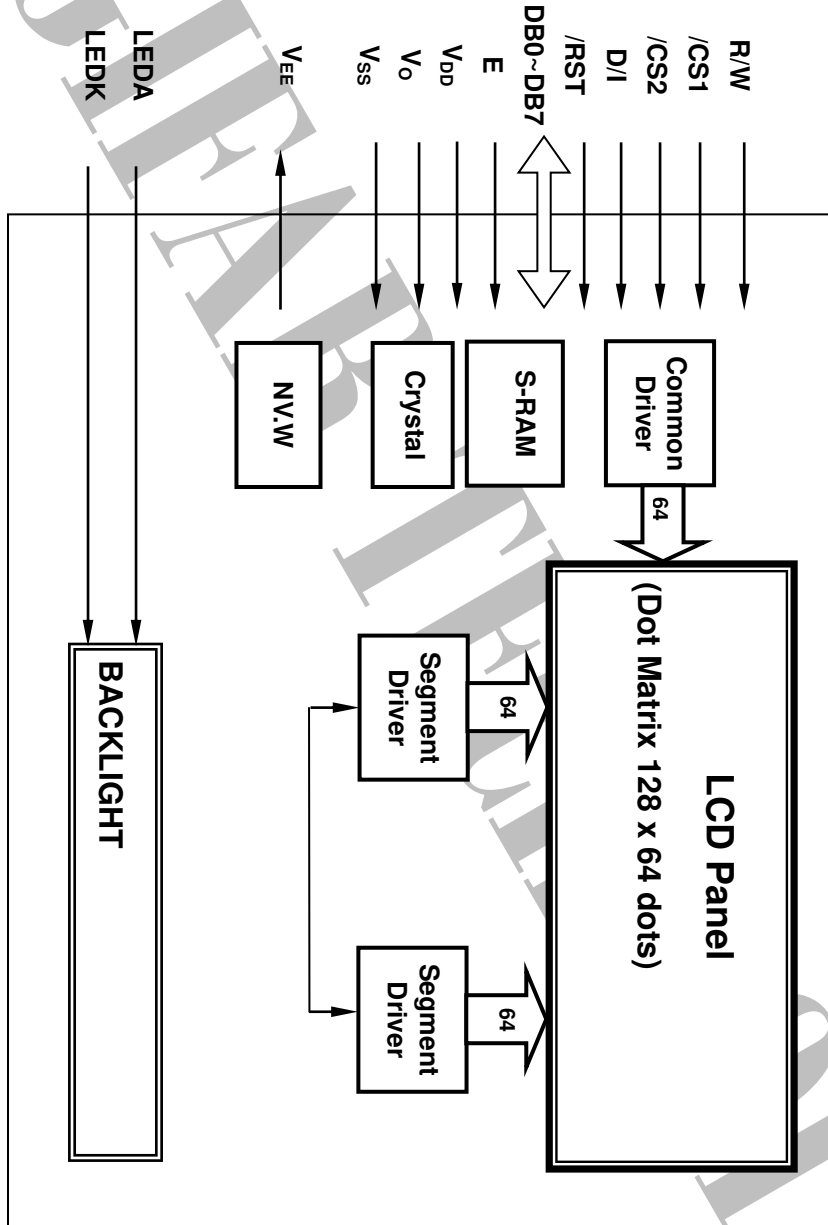


4.4.Display Command

Instructions	Code										Functions	
	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		
Display on/off	0	0	0	0	1	1	1	1	1	1/0	Controls display on/off. RAM data and internal status are not affected.	
Display start line	0	0	1	1	Display start line (0-63)					Specifies the RAM line displayed at the top of the screen.		
Set Page (x address)	0	0	1	0	1	1	1	Page (0-7)			Sets the page (X address) of RAM at the page (X address) register.	
Set Y address	0	0	0	1	Y address (0-63)						Sets the Y address in the Y address in the counter.	
Status read	1	0	Bus y	0	0	Reset N/0	0	0	0	0	Reads the status. Reads ON/OFF Busy 1: Reset 0: Normal 1: Display off 0: Display on 1: Internal operation 0: Ready	
Write display data	0	1	Write data								Writes data DB0 (LSB) to DB7 (MSB) on the data bus into display RAM.	Has access to the address of the display RAM specified in advance. After the access, Y address is increased by 1.
Read display data	1	1	Read data								Reads data DB0 (LSB) to DB7 (MSB) from the display RAM to the data bus.	



4.5 Circuit Block Diagram





5. NOTES

▪ Safety

- If the LCD panel breaks, be careful not to get the liquid crystal in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

Handling

- Avoid static electricity as this can damage the CMOS LSI.
- The LCD panel is plate glass; do not hit or crush it.
- Do not remove the panel or frame from the module.
- The polarizing plate of the display is very fragile; handle it very carefully

Mounting and Design

- Mount the module by using the specified mounting part and holes.
- To protect the module from external pressure, leave a small gap by placing transparent plates (e.g. acrylic or glass) on the display surface, frame, and polarizing plate
- Design the system so that no input signal is given unless the power-supply voltage is applied.
- Keep the module dry. Avoid condensation, otherwise the transparent electrodes may break.

Storage

- Store the module in a dark place where the temperature is $25\text{ }^{\circ}\text{C}\pm 10\text{ }^{\circ}\text{C}$ and the humidity below 65% RH.
- Do not store the module near organic solvents or corrosive gases.
- Do not crush, shake, or jolt the module (including accessories).

Cleaning

- Do not wipe the polarizing plate with a dry cloth, as it may scratch the surface.
- Wipe the module gently with soft cloth soaked with a petroleum benzine.
- Do not use ketonic solvents (ketone and acetone) or aromatic solvents (toluene and xylene), as they may damage the polarizing plate.

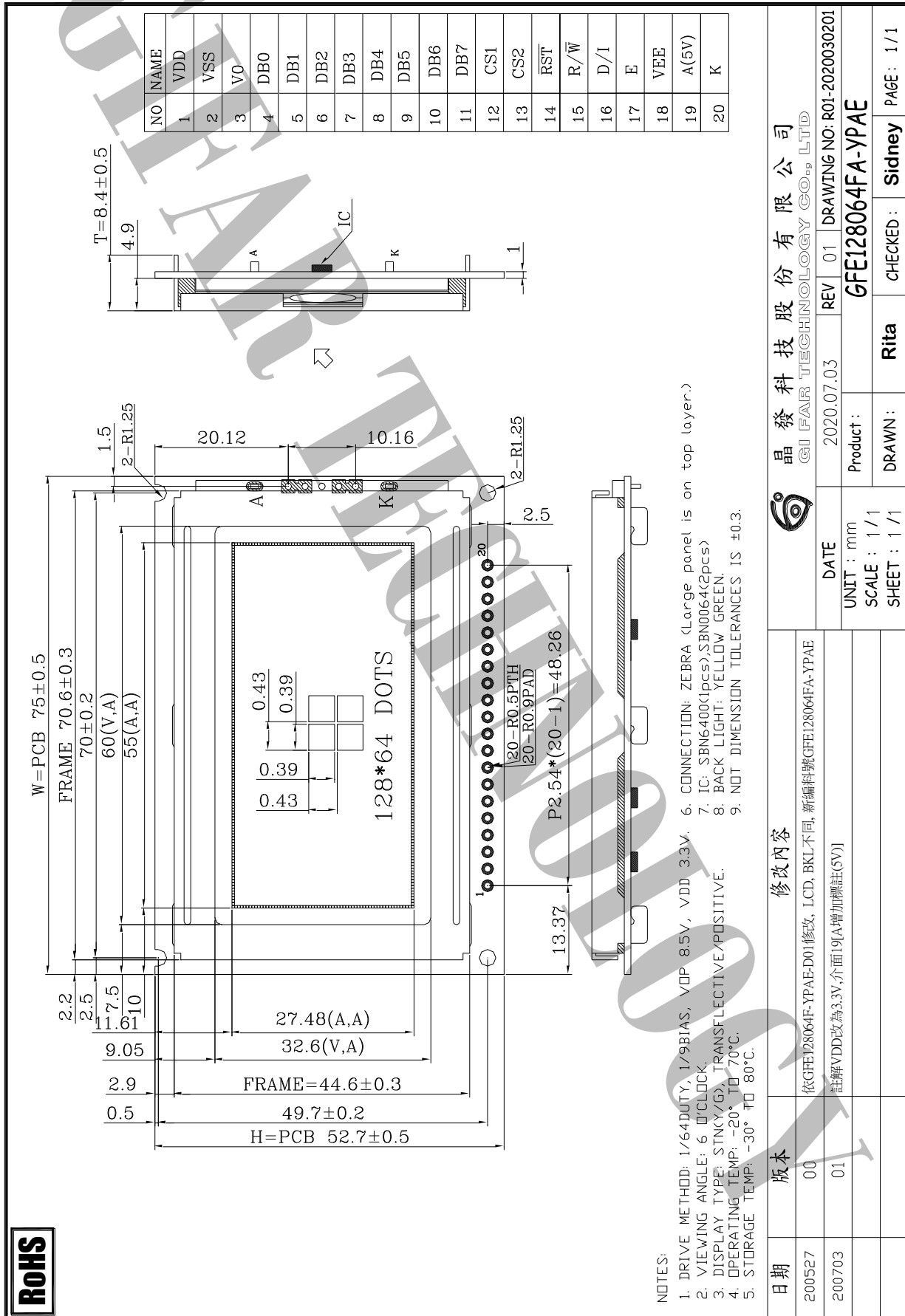
6. OPERATION PRECAUTIONS

Any changes that need to be made in this specification or any problems arising from it will be dealt with quickly by discussion between both companies.

Quality warranty period: Within one year after shipment date (excluding abnormal usage way and abnormal environments.)



7. LCM Dimension





8. PACKAGE INFORMATION

1	1 Tray	:	14 pcs (modules)
2	1 stack	:	7 tray +1 Cover tray
3	1 Carton	:	(1 Cover tray + 7 tray)x 3 stack
4	Total pcs	:	1 Carton (14pcs * 7tray * 3 stack) = 294 pcs
5	Carton size = NO. 17	:	495*315*435mm
7	Net weight	:	TBD KG
8	Gross weight	:	TBD KG

** Packaging information**

- 1 Tray = 14 pcs



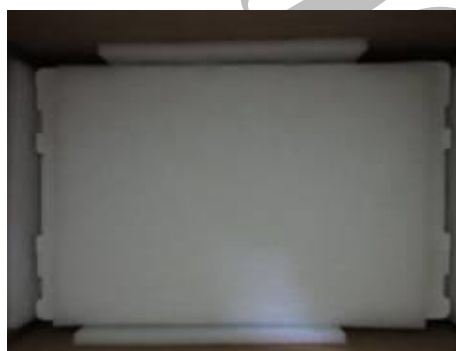
- 1 stack= 7 tray+1 Cover tray



**Each layer of tray should be staggered stacked



- 1 Carton = 3 stack, Total pcs = 294 pcs



出貨檢驗標準書
Shipping inspection standard

核准 Approved by	審核 Checked by	作成 Made by
ANDY	JACKY	RUBY

1.目的 Purpose :

規範出貨產品之檢驗項目及判斷標準，確保產品出貨能滿足客戶要求。

Standardize the inspection items and judgment standards to ensure the products that shipped out can meet customer's requirements.

2.範圍 Area :

適用於出廠之所有產品。

Applicable to all products shipped from the factory.

3.名詞解釋 Explanation of terms :

3-1 主要缺陷：亦會造成功能缺失或嚴重外觀缺陷。

Major Defects: It also causes loss of function or serious appearance defects.

3-2 次要缺陷：稍有缺陷但不影響客戶使用。

Minor defect: Slightly defective but does not affect customer use.

4.檢驗體制 Inspection system :

4-1 抽樣計劃：依 ANSI/ASQ Z1.4 一般檢驗水準 II 之 正常檢驗一次抽驗方案。

Sampling plan: According to ANSI/ASQ Z1.4 general inspection level II the normal inspection one-time sampling plan.

4-2 允收水準 Acceptable Level : (AQL)

主要缺陷 Major defect : 0.4 %

次要缺陷 Minor defect : 0.65 %

5.檢驗條件 Inspection conditions :

5-1 使用相關之檢測儀器及測試、量測工具。

Use relevant testing instrument, testing and measuring tools .

5-2 環境要求：其條件需控制在常溫下 $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ 及溼度 70%RH 以下。

Environmental requirements: The conditions should be controlled at room temperature $23^{\circ}\text{C}\pm 3^{\circ}\text{C}$ and humidity below 70%RH.

5-3 外觀檢驗：須在 $380\pm 20\%$ LUX 的白色日光燈下，其目視距離需於產品離 30 ± 5 cm 檢驗。

Appearance inspection: Under the white fluorescent lamp of $380\pm 20\%$ LUX , the visual distance shall be checked above the product 30 ± 5 cm.

5-4 電性測試 Electrical Testing :

5-4-1 有背光之產品需關燈並在 $5\sim 300\text{Lux}\pm 3\%$ 下檢驗。

The products with backlight should be tested at $5\sim 300\pm 3\%$ Lux.

5-4-2 無背光之產品需開燈並在 $60\sim 300\text{Lux}\pm 3\%$ 白色日光燈下檢驗。

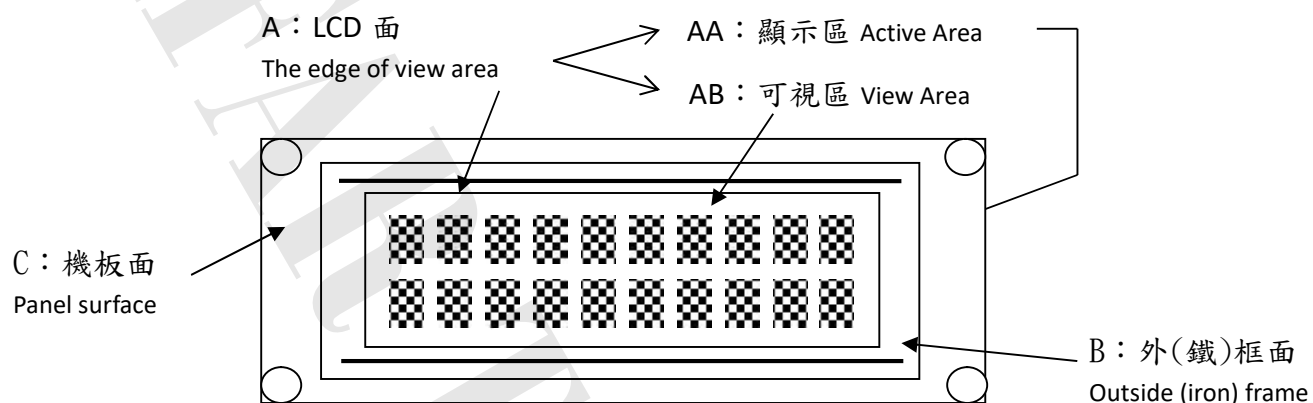
Products without backlight need to be turned on and tested under $60\sim 300 \pm 3\%$ LUX white fluorescent lamps .

5-5 檢查視角依產品視角方向。

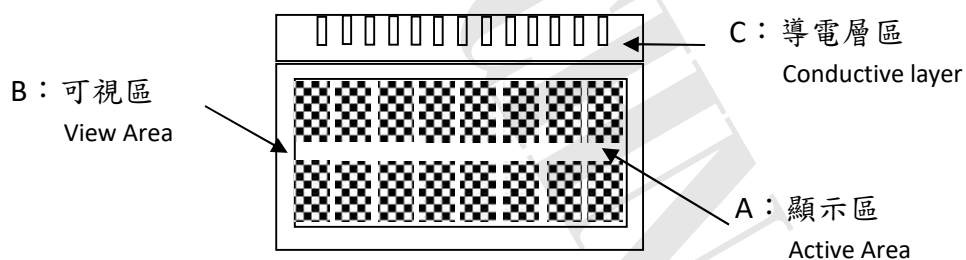
Check the viewing angle according to the product viewing angle.

5-6 其不良現象檢視區域 Bad phenomenon View area

5-6-1 適用種類 Applicable category : COB、TFT

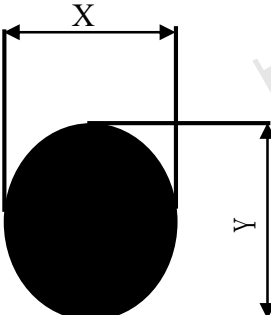
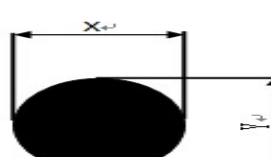
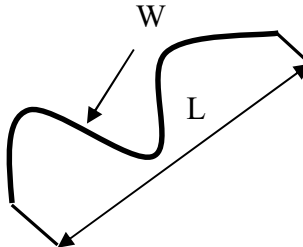


5-6-2 適用種類 Applicable category : COG、TAB、TN



6. 檢驗標準 Inspection standards

COB

種類 Category		COB																			
編號 No.	檢驗項目 Item	檢驗內容及判定標準 Inspection Content & Standard	區域 Zone	類別 Category	缺陷等級 Level																
1	點類(一) Dot (1)	黑點、刺傷...等圓狀 Black dot、Stab...and other round shape $\varphi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points have to be ≥ 5 mm <table border="1"> <thead> <tr> <th>φ (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.1$</td> <td>無視 Ignore</td> </tr> <tr> <td>$0.1 < \phi \leq 0.25$</td> <td>3</td> </tr> <tr> <td>$0.25 < \phi \leq 0.3$</td> <td>1</td> </tr> <tr> <td>$\phi > 0.3$</td> <td>0</td> </tr> </tbody> </table>	φ (mm)	允收數 Acceptance Qty	$\phi \leq 0.1$	無視 Ignore	$0.1 < \phi \leq 0.25$	3	$0.25 < \phi \leq 0.3$	1	$\phi > 0.3$	0	A	外觀 Appearance	次要 Minor AQL0.65%					
		φ (mm)	允收數 Acceptance Qty																		
$\phi \leq 0.1$	無視 Ignore																				
$0.1 < \phi \leq 0.25$	3																				
$0.25 < \phi \leq 0.3$	1																				
$\phi > 0.3$	0																				
其點位於側邊 無視 The dot is located on the side(B area)-OK <table border="1"> <thead> <tr> <th>φ (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.3$</td> <td>無視 Ignore</td> </tr> <tr> <td>$0.3 < \phi \leq 0.5$</td> <td>3</td> </tr> <tr> <td>$0.5 < \phi \leq 1$</td> <td>1</td> </tr> <tr> <td>$\phi > 1$</td> <td>0</td> </tr> </tbody> </table>	φ (mm)	允收數 Acceptance Qty	$\phi \leq 0.3$	無視 Ignore	$0.3 < \phi \leq 0.5$	3	$0.5 < \phi \leq 1$	1	$\phi > 1$	0	B	外觀 Appearance	次要 Minor AQL0.65%								
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$0.5 < \phi \leq 1$	1																				
$\phi > 1$	0																				
2	點類(二) Dot (2)	氣泡、凹凸點 Bubble、Uneven dots $\varphi = \frac{(X + Y)}{2}$ 	兩點距離須超過 5 mm Two points to be ≥ 5 mm <table border="1"> <thead> <tr> <th>φ (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>$\phi \leq 0.2$</td> <td>無視 Ignore</td> </tr> <tr> <td>$0.2 < \phi \leq 0.5$</td> <td>2</td> </tr> <tr> <td>$\phi > 0.5$</td> <td>0</td> </tr> </tbody> </table>	φ (mm)	允收數 Acceptance Qty	$\phi \leq 0.2$	無視 Ignore	$0.2 < \phi \leq 0.5$	2	$\phi > 0.5$	0	A	外觀 Appearance	次要 Minor AQL0.65%							
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$0.2 < \phi \leq 0.5$	2																				
$\phi > 0.5$	0																				
3	線類 Line	刮傷、毛屑...等線狀 Scratch、Fiber.. and other linear shape. 	<table border="1"> <thead> <tr> <th>L (mm)</th> <th>W (mm)</th> <th>允收數 Acceptance Qty</th> </tr> </thead> <tbody> <tr> <td>--</td> <td>$W \leq 0.02$</td> <td>無視 Ignore</td> </tr> <tr> <td>$L \leq 5$</td> <td>$W \leq 0.03$</td> <td>3</td> </tr> <tr> <td>$L \leq 3$</td> <td>$W \leq 0.05$</td> <td>2</td> </tr> <tr> <td>$L > 5$</td> <td>$W > 0.05$</td> <td>0</td> </tr> </tbody> </table>	L (mm)	W (mm)	允收數 Acceptance Qty	--	$W \leq 0.02$	無視 Ignore	$L \leq 5$	$W \leq 0.03$	3	$L \leq 3$	$W \leq 0.05$	2	$L > 5$	$W > 0.05$	0	A	外觀 Appearance	次要 Minor AQL0.65%
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$L \leq 3$	$W \leq 0.05$	2																			
$L > 5$	$W > 0.05$	0																			
4	底色 Background color	同批供貨不能有明顯色差 No obvious color difference allowed in same shipment. (必要時與客端制定限度樣) (According to the gold samples if necessary)	A	外觀 Appearance	次要 Minor AQL0.65%																

5	零件位置 Parts position	與工程 BOM 上標示不符 拒收 Different with the BOM marked Reject	C	外觀 Appearance	主要 Major AQL 0.4%
6	板面潔淨 FPC/PCB's surface is clean	※ 焊接面上附著錫渣、珠 拒收 Solder side has tin slag, beads or particle Reject ※ 焊接面上附著於過多錫油 拒收 Solder side has too much tin oil Reject	C	外觀 Appearance	次要 Minor AQL0.65%
7	點、線類 (三) Dot、Line (3)	※ 於全黑、白畫面下看見之區塊狀或線狀不良 拒收 There is a block or linear in the view area under the screen is whole black or white. Reject ※ 但依 2% ND Filter 遮蓋無視 允收 But after inspecting by 2% ND Filter without seeing block or linear, it is confirmed Acceptance	AA	電訊 Electronics	次要 Minor AQL0.65%
8	點、線類 (四) Dot、Line (4)	畫面中顯示出現黑、白、亮、異色點或線狀 There is a black, white, bright or other dot or lines showing in the view area. ※ 依編號 1、3 之判定標準 According to the inspection standard: No. 1 and 3.	AA	電訊 Electronics	次要 Minor AQL0.65%
9	缺字 Lack of characters	顯示時畫面缺少部份字元 拒收 Lacking part of characters in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
10	無動作 No reaction	顯示畫面一直處於起始畫面而無法進行切換 拒收 The display (view area) always shows in the initial screen and can't be switched to others. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
11	無畫面 No display	通電後，完全無任何畫面顯示 拒收 After connecting to the power, there is no display. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
12	斷線 Broken line	顯示畫面中少直、橫線 拒收 There is a lack of vertical or horizontal lines in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
13	CROSS TALK	顯示畫面時有局部之條紋或拖影 There are some stripes or shadow/smear showing in the view area. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	AA	電訊 Electronics	次要 Minor AQL0.65%

14	I CON	顯示畫面缺少部份顯示圖案 拒收 Lack of partial ICON in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
15	深淺不一 Color difference	顯示畫面的對比，比其他顯示深或淺並依電氣規格(VOP)值判定 The contrast of display is obviously lighter or darker than others and according to the VOP value in the electronics specification. 拒收或與客端簽訂限度樣 Reject or inspect according to the golden sample	AA	電訊 Electronics	次要 Minor AQL0.65%
16	畫面異常 Abnormal screen	通電後畫面出現未定義之電訊不良現象 拒收 After connecting to the power, there is an undefined electronics appearance showing in the view area. Reject	AA	電訊 Electronics	主要 Major AQL 0.4%
17	背光色不均 Uneven color of backlight	※ 點亮後 LED 有明暗不均現象依其均勻度判定 拒收 After lighting LEDs have brightness and darkness uneven the determined according to its uniformity. Reject ※ 點亮後 LED 色澤不一致 拒收 LED color is inconsistent after lighting Reject	A	電訊 Electronics	次要 Minor AQL0.65%
18	亮度不足 Lack of brightness	波長、色座標、輝度與圖面標示定義不符 拒收 Wave length, chromatic coordinates, brightness don't correspond to the definition of the drawing. Reject	A	電訊 Electronics	次要 Minor AQL0.65%
19	尺寸量測 Size Measurement	未依圖面上標示 拒收 No correspond to the indication on the drawing. Reject	ALL	外觀 Appearance	主要 Major AQL 0.4%
20	其他 Other	如發現有上述未定義之不良則與客端簽訂限度樣 If there is another undefined defective situation. It will be listed as others. The inspection standard is according to the golden sample.	ALL	電訊 Electronics 外觀 Appearance	次要 Minor AQL0.65%