

T510 Space Grade High Reliability Alternative (HRA) Multiple Anode Low ESR MnO₂

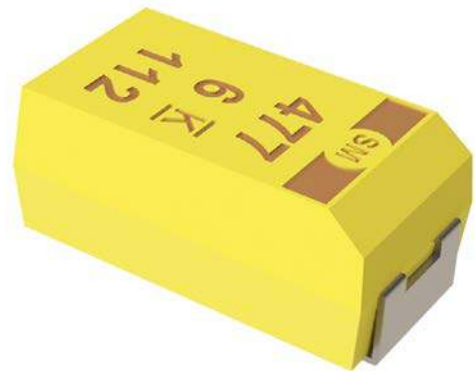
Overview

KEMET's Space Grade capacitors are suitable for use by the defense/aerospace customers in high reliability space applications. These capacitors meet the requirements of MIL-PRF-55365 as well as MIL-STD-1580. These capacitors incorporate an intensive testing and screening protocol which is customizable depending upon customer's specific needs.

The full part number allows for designation of surge current level (10 cycles -55°C and +85°C before and/or after Weibull grading, 10 cycles +25°C), performance testing level (see chart for details on available options), ESR (low and standard), and termination finish (see description in each series). Fused versions are available for built-in circuit protection, as well as multi-anode designs for very low ESR values.

Benefits

- ESR as low as 18 mΩ
- High ripple current capability
- 100% steady-state accelerated aging
- 100% surge current test
- Meets or exceeds EIA standard 535BAAC
- Taped and reeled per EIA 481
- Weibull Grading C (0.01%/1,000 hours)



Applications

Typical applications include decoupling and filtering in defense and aerospace end applications, such as DC/DC converters, portable electronics, telecommunications, and control units requiring high ripple current capability.

Environmental Compliance

- Halogen-free
- Epoxy compliant with UL94 V-0
- Molded Epoxy complies for outgassing testing under ASTM E 595.

K-SIM

For a detailed analysis of specific part numbers, please visit ksim.kemet.com to access KEMET's K-SIM software. KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels.

Ordering Information

T	510	X	337	K	010	C	H	64	1	A	
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Voltage (V)	Failure Rate/Design	Lead Material	Surge	ESR	Testing	Packaging (C-Spec)
T = Tantalum	Ultra-low ESR – Space grade	E X	First two digits represent significant figures. Third digit specifies number of zeros.	K = ±10% M = ±20%	004 = 4 006 = 6.3 010 = 10	C = 0.01% /1,000 hours	C = Hot solder dipped H = Standard solder-coated (SnPb 5% Pb minimum) T = 100% Matte Tin (Sn) plated	61 = No Surge 62 = 10 Cycles after Weibull, 25°C 63 = 10 Cycles, after Weibull, -55°C and 85°C 64 = 10 Cycles before Weibull, -55°C and 85°C 65 = 10 Cycles before and after Weibull, -55°C and 85°C	1 = ESR-standard	A = Option A B = Option B C = Option C	Blank = 7" Reel 7280 = 13" Reel 7610 = Bulk bag 7640 = Bulk plastic box WAFL = Waffle pack

Performance Characteristics

Item	Performance Characteristics
Operating Temperature	-55°C to 125°C
Rated Capacitance Range	330 – 1,000 µF at 120 Hz/25°C
Capacitance Tolerance	K Tolerance (10%), M Tolerance (20%)
Rated Voltage Range	4 – 10 V
DF (120 Hz)	Refer to Part Number Electrical Specification Table
ESR (100 kHz)	Refer to Part Number Electrical Specification Table
Leakage Current	≤ 0.01 CV (µA) at rated voltage after 5 minutes

Qualification

Test	Condition	Characteristics				
Endurance	85°C at rated voltage, 2,000 hours 125°C at 2/3 rated voltage, 2,000 hours	Δ C/C	Within ±10% of initial value			
		DF	Within initial limits			
		DCL	Within 1.25 x initial limit			
		ESR	Within initial limits			
Storage Life	125°C at 0 volts, 2,000 hours	Δ C/C	Within ±10% of initial value			
		DF	Within initial limits			
		DCL	Within 1.25 x initial limit			
		ESR	Within initial limits			
Thermal Shock	MIL-STD-202, Method 107, Condition B, mounted, -55°C to 125°C, 1,000 cycles	Δ C/C	Within ±5% of initial value			
		DF	Within initial limits			
		DCL	Within 1.25 x initial limit			
		ESR	Within initial limits			
Temperature Stability	Extreme temperature exposure at a succession of continuous steps at +25°C, -55°C, +25°C, +85°C, +125°C, +25°C	Δ C/C	+25°C	-55°C	+85°C	+125°C
		DF	IL*	±10%	±10%	±20%
		DCL	IL	IL	1.5 x IL	1.5 x IL
Surge Voltage	25°C and 85°C, 1.32 x rated voltage 1,000 cycles. (125°C, 1.2 x rated voltage)	Δ C/C	Within ±5% of initial value			
		DF	Within initial limits			
		DCL	Within initial limits			
		ESR	Within initial limits			
Mechanical Shock/ Vibration	MIL-STD-202, Method 213, Condition I, 100 G peak. MIL-STD-202, Method 204, Condition D, 10 Hz to 2,000 Hz, 20 G peak	Δ C/C	Within ±10% of initial value			
		DF	Within initial limits			
		DCL	Within initial limits			

*IL = Initial limit

Test Methods

Test Sequence	Test Method	Option A	Option B	Option C
100% Serialization	KEMET Standard			X
100% IR Reflow	MIL-PRF-55365	X	X	X
100% Thermal Shock	MIL-PRF-55365	X	X	X
100% Electrical Verification	KEMET Standard			X
Read and Record Attributes/Variables Data	KEMET Standard			X
100% Surge Current, Option C with 5% PDA Calculation	MIL-PRF-55365 with 5% PDA Calculation	X	X	X
100% Electrical Verification	KEMET Standard	X	X	X
Read and Record Attributes/Variables Data	KEMET Standard			X
100% Voltage Aging, 10 hours at 1.32 Vr	MIL-PRF-55365	X	X	X
100% Electrical Verification	KEMET Standard	X	X	X
100% Weibull Grading C	MIL-PRF-55365	X	X	X
100% Electrical Verification	KEMET Standard	X	X	X
Read and Record Attributes/Variables Data	KEMET Standard			X
100% Surge Current, Option A or B with 5% PDA Calculation	MIL-PRF-55365 with 5% PDA Calculation	X	X	X
100% Electrical Verification	KEMET Standard	X	X	X
Read and Record Attributes/Variables Data	KEMET Standard			X
3 Sigma Screening – All Electricals	KEMET Standard	X	X	X
Read and Record Attributes/Variables Data	KEMET Standard			X
Destructive Physical Analysis (DPA) (5 pieces–each lot)	MIL-PRF-55365	X	X	X
Group B Testing (22 pieces – each lot)	* See Note Below		X	X
Temperature Stability – sample	MIL-PRF-55365	X	X	X
Solderability – Sample	MIL-PRF-55365	X	X	X
Group C Testing (57 pieces – each Lot)**	MIL-PRF-55365		X	X
100% X-ray	MIL-PRF-55365	X		
100% X-ray – 2 Plane***	MIL-PRF-55365 and KEMET Standard		X	X
100% Physical Dimension Verification	MIL-PRF-55365	X	X	X
Data Pack				
Group A and C Summaries			X	X
2 Plane X-ray JPEG photos			X	X
DPA Report			X	X
Attributes/Variables Data for Cap/Df/DCL/ESR				X

X = Included in test option

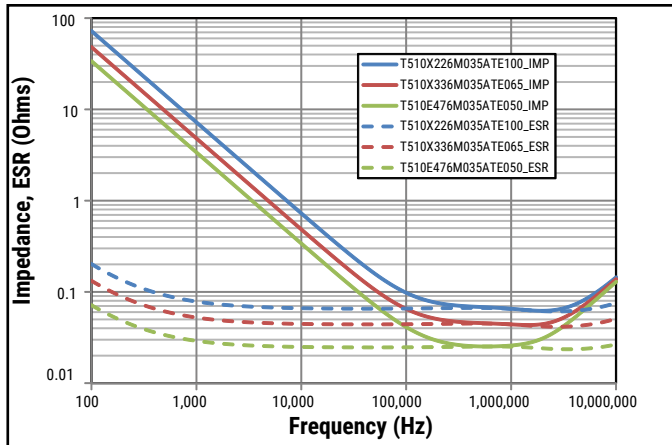
* Group B Testing = 10,000 Cycles Surge Current, 85°C, 40% Vr

** Group C Post Moisture ESR limit = 1.25 initial limit

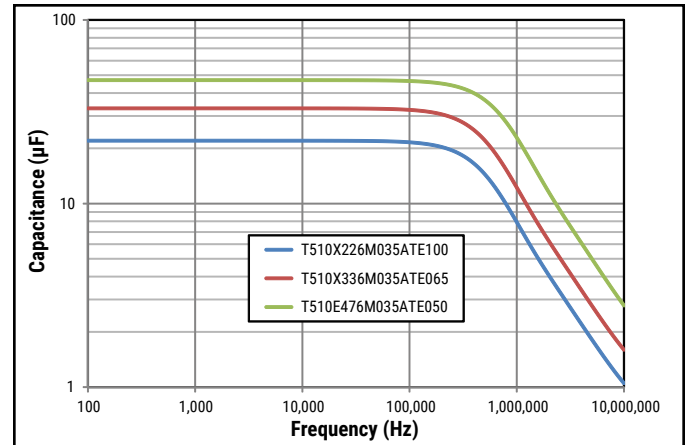
*** 2 Plane X-ray = Top and side views, molded case wall thickness minimum 0.005" on all sides, negative/positive termination attachment criteria per MIL-STD-1580

Electrical Characteristics

Impedance, ESR vs. Frequency



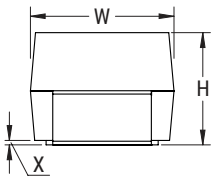
Capacitance vs. Frequency



Dimensions – Millimeters (Inches)

Metric will govern

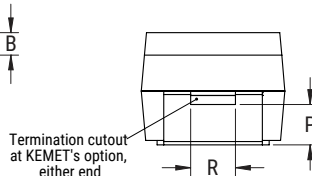
CATHODE (-) END VIEW



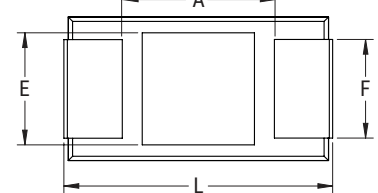
SIDE VIEW



ANODE (+) END VIEW



BOTTOM VIEW



Case Size		Component													
KEMET	EIA	L	W	H	F±0.1 ±(0.004)	S±0.3 ±(0.012)	B±0.15 (Ref)±0.006	X (Ref)	P (Ref)	R (Ref)	T (Ref)	A (Min)	G (Ref)	E (Ref)	Net Weight (mg)
X	7343-43	7.3±0.3 (0.287±0.012)	4.3±0.3 (0.169±0.012)	4.0±0.3 (0.157±0.012)	2.4 (0.094)	1.3 (0.051)	0.5 (0.020)	0.10±0.10 (0.004±0.004)	1.7 (0.067)	1.0 (0.039)	0.13 (0.005)	3.8 (0.150)	3.5 (0.138)	3.5 (0.138)	430.15
E	7360-38	7.3±0.3 (0.287±0.012)	6.0±0.3 (0.236±0.012)	3.6±0.2 (0.142±0.008)	4.1 (0.161)	1.3 (0.051)	0.5 (0.020)	0.10±0.10 (0.004±0.004)	n/a	n/a	0.13 (0.005)	3.8 (0.150)	3.5 (0.138)	3.5 (0.138)	500.73

Notes: (Ref) – Dimensions provided for reference only. No dimensions are provided for B, P or R because low profile cases do not have a bevel or a notch. These weights are provided as a reference. If exact weights are needed, please contact your KEMET Sales Representative.

Table 1 – Ratings & Part Number Reference

Rated Voltage	Rated Capacitance	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	ESR	Maximum Allowable Ripple Current (mArms)			Maximum Operating Temperature	MSL
							100 kHz, 25°C	100 kHz, 85°C	100 kHz, 125°C		
VDC at 85°C	μF	KEMET/EIA	(See below for part options)	μA at +20°C Maximum/ 5 Minimum	% at +20°C 120 Hz Maximum	mΩ at +20°C 100 kHz Maximum				°C	Reflow Temp ≤ 260°C
4	1000	E/7360-38	T510E108(1)004C(2)(3)1(4)	40.0	6	18	3900	3500	1500	125	1
6.3	470	X/7343-43	T510X477(1)006C(2)(3)1(4)	30.0	6	30	3000	2700	1200	125	1
6.3	680	E/7360-38	T510E687(1)006C(2)(3)1(4)	40.8	6	23	3500	3200	1400	125	1
10	330	X/7343-43	T510X337(1)010C(2)(3)1(4)	33.0	6	35	2800	2500	1100	125	1

(1) To complete KEMET part number, insert M for ± 20%, K for ± 10%. Designates Capacitance tolerance.

(2) To complete KEMET part number, insert C= Hot solder dipped, or H = Solder Plated. Designates Termination Finish.

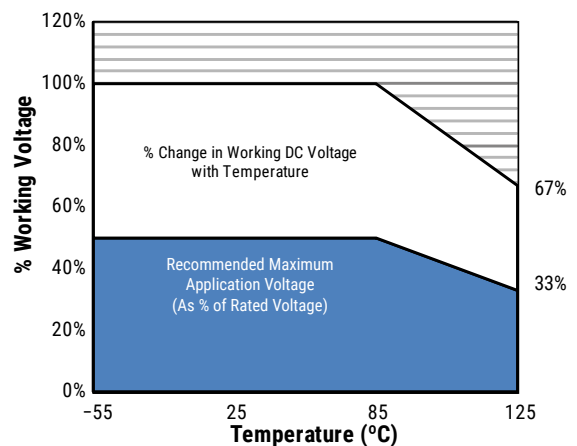
(3) To complete KEMET part number, insert 62 = 10 cycles +25°C, 63 = 10 cycles -55°C +85°C after Weibull, 64 = 10 cycles -55°C +85°C before Weibull or 65 = Both. Designates Surge current option.

(4) To complete KEMET part number, insert A = Option 1, B = Option 2 or C = Option 3. Designates Test Option. See Space Grade Test Methods chart for more information.

Refer to Ordering Information for additional detail.

Recommended Voltage Derating Guidelines

	-55°C to 85°C	85°C to 125°C
% Change in Working DC Voltage with Temperature	V _R	67% of V _R
Recommended Maximum Application Voltage	50% of V _R	33% of V _R



Ripple Current/Ripple Voltage

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device. Permissible AC ripple voltage which may be applied is limited by two criteria:

1. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.
2. The negative peak AC voltage in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage. See the Reverse Voltage section for allowable limits.

The maximum power dissipation by case size can be determined using the table at right. The maximum power dissipation rating stated in the table must be reduced with increasing environmental operating temperatures. Refer to the table below for temperature compensation requirements.

KEMET Series and Case Code	EIA Case Code	Maximum Power Dissipation (Pmax) mWatts at 25°C with +20°C Rise
T510X	7343-43	270
T510E	7360-38	285

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

$$I(max) = \sqrt{Pmax/R}$$

$$E(max) = Z \sqrt{Pmax/R}$$

- I* = rms ripple current (amperes)
E = rms ripple voltage (volts)
Pmax = maximum power dissipation (watts)
R = ESR at specified frequency (ohms)
Z = Impedance at specified frequency (ohms)

Temperature Compensation Multipliers for Maximum Ripple Current			
T ≤ 25°C	T ≤ 85°C	T ≤ 125°C	T ≤ 150°C
1.00	0.90	0.40	0.30

T = Environmental Temperature

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.

Reverse Voltage

Solid tantalum capacitors are polar devices and may be permanently damaged or destroyed if connected with the wrong polarity. The positive terminal is identified on the capacitor body by a stripe plus in some cases a beveled edge. A small degree of transient reverse voltage is permissible for short periods per the table. The capacitors should not be operated continuously in reverse mode, even within these limits.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
85°C	5% of Rated Voltage
125°C	1% of Rated Voltage

Table 2 – Land Dimensions/Courtyard

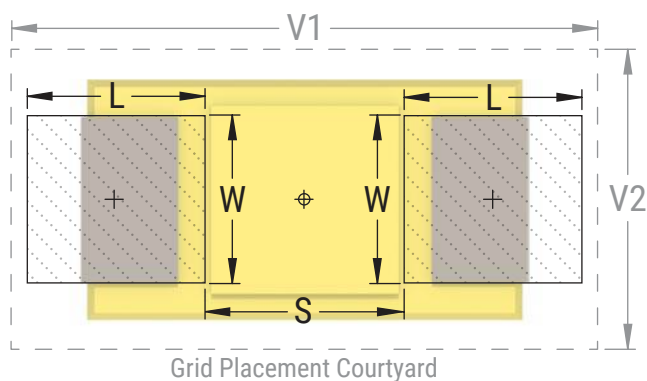
KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)					Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)				
		Case	EIA	W	L	S	V1	V2	W	L	S	V1	V2	W	L	S
E ¹	7360-38	4.25	2.77	3.67	10.22	7.30	4.13	2.37	3.87	9.12	6.80	4.03	1.99	4.03	8.26	6.54
X ¹	7343-43	2.55	2.77	3.67	10.22	5.60	2.43	2.37	3.87	9.12	5.10	2.33	1.99	4.03	8.26	4.84

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

¹ Height of these chips may create problems in wave soldering.



Soldering Process

KEMET’s families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET’s recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

Please note that although the X/7343-43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. “Wiping” the edges of a chip and heating the top surface is not recommended.

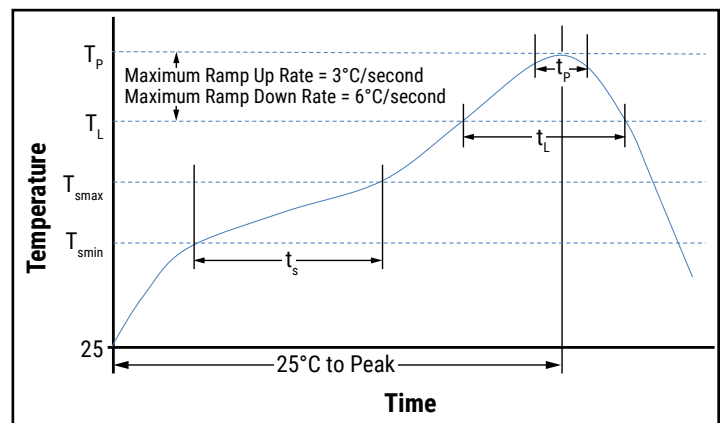
During typical reflow operations, a slight darkening of the gold-colored epoxy may be observed. This slight darkening is normal and not harmful to the product. Marking permanency is not affected by this change.

Profile Feature	SnPb Assembly	Pb-Free Assembly
Preheat/Soak		
Temperature Minimum (T_{smin})	100°C	150°C
Temperature Maximum (T_{smax})	150°C	200°C
Time (t_s) from T_{smin} to T_{smax}	60 – 120 seconds	60 – 120 seconds
Ramp-up Rate (T_L to T_P)	3°C/seconds maximum	3°C/seconds maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T_P)	220°C* 235°C**	250°C* 260°C**
Time within 5°C of Maximum Peak Temperature (t_p)	20 seconds maximum	30 seconds maximum
Ramp-down Rate (T_P to T_L)	6°C/seconds maximum	6°C/seconds maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow.

* For Case Size height > 2.5 mm

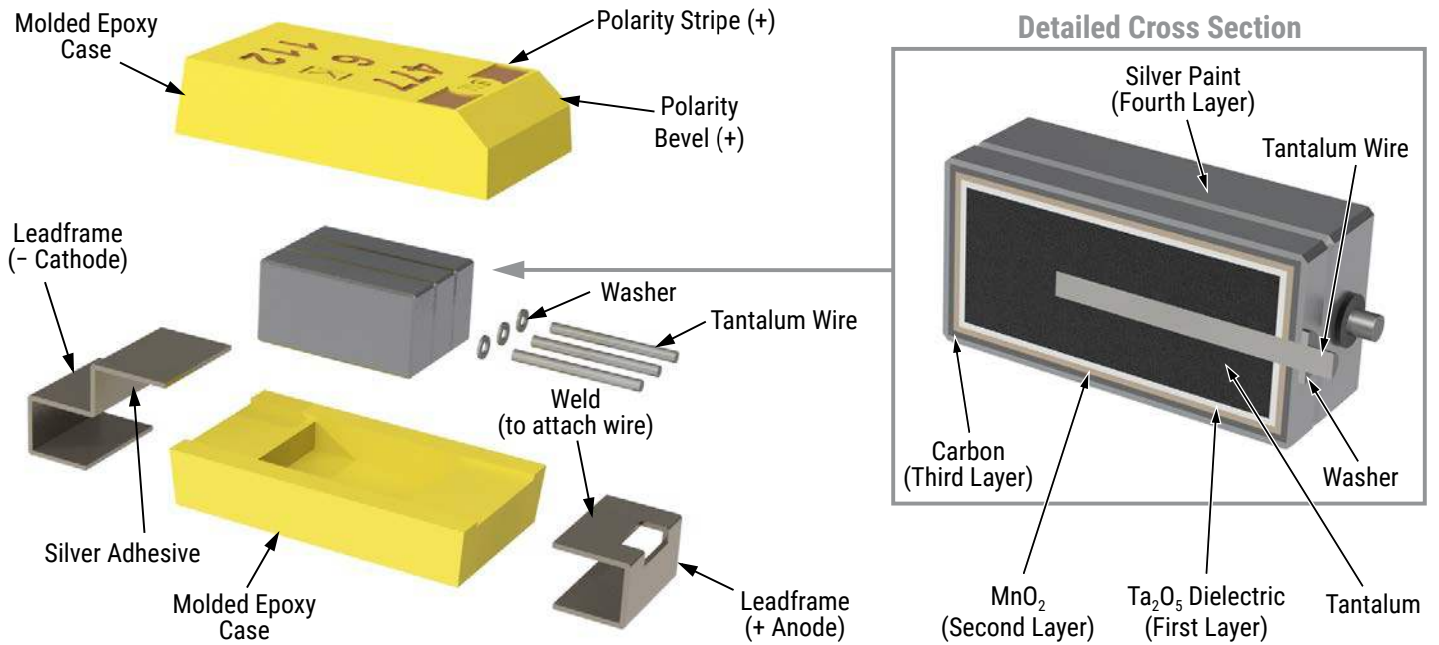
** For Case Size height ≤ 2.5 mm



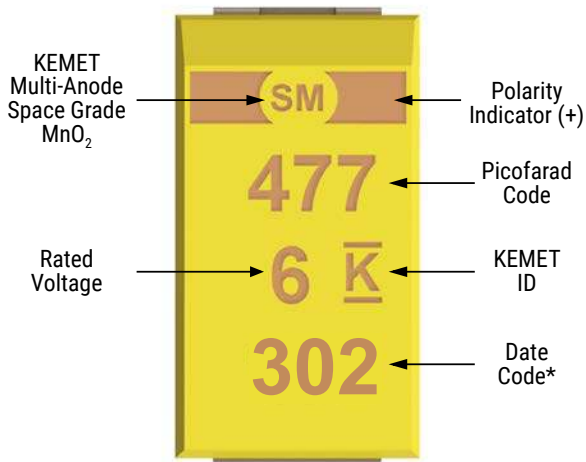
Storage

Tantalum chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature—reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 60% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulphur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within three years of receipt.

Construction



Capacitor Marking



* 302 = 2nd week of 2023

Parts screened with Option C are marked with a serial number below date code characters.

Date Code *	
1 st digit = Last number of Year	9 = 2019 0 = 2020 1 = 2021 2 = 2022 3 = 2023
2 nd and 3 rd digit = Week of the Year	01 = 1 st week of the Year to 52 = 52 nd week of the Year

Tape & Reel Packaging Information

KEMET’s molded tantalum and aluminum chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with *EIA Standard 481: Embossed Carrier Taping of Surface Mount Components for Automatic Handling*. This packaging system is compatible with all tape-fed automatic pick-and-place systems.

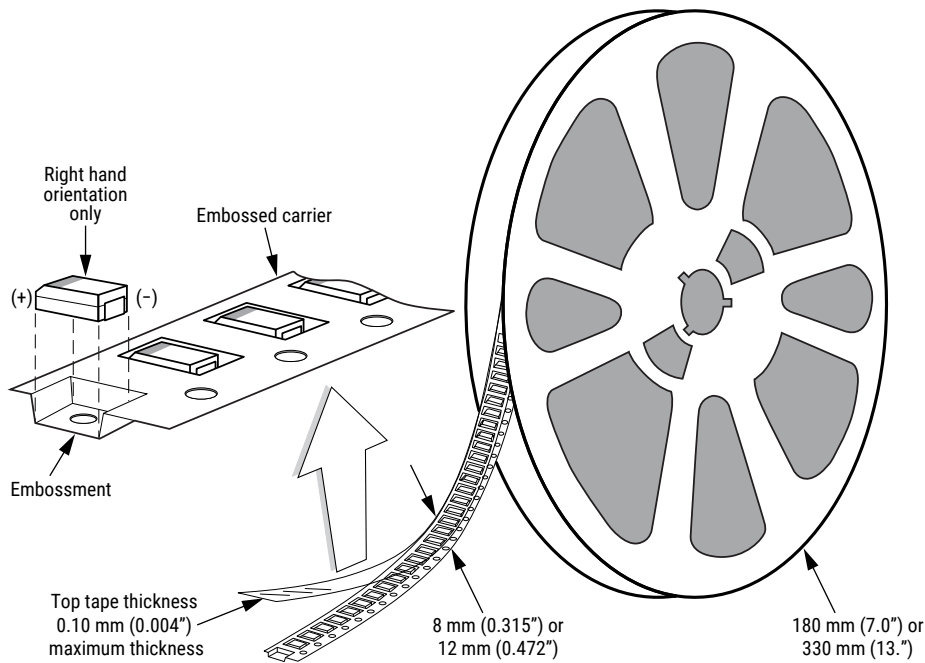


Table 3 – Packaging Quantity

Case Code		Tape Width (mm)	7" Reel*
KEMET	EIA		
X	7343-43	12	500
E	7360-38	12	500

Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

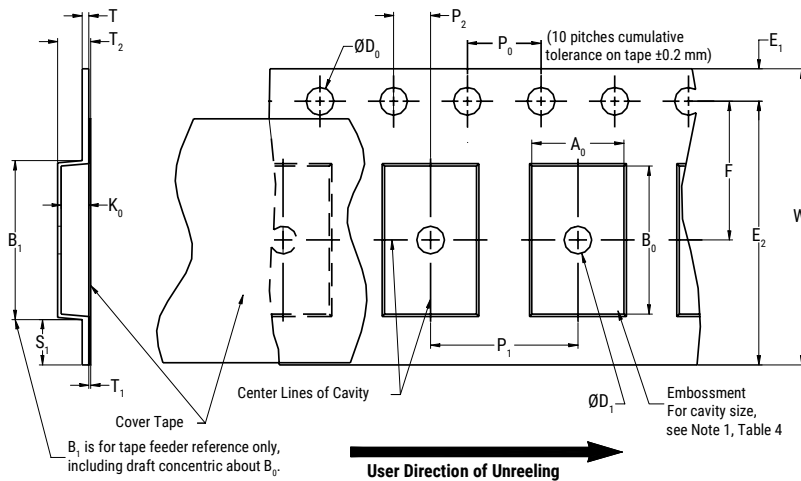


Table 4 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

Constant Dimensions – Millimeters (Inches)									
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.0 (0.039)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	25.0 (0.984)	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
12 mm		1.5 (0.059)							

Variable Dimensions – Millimeters (Inches)									
Tape Size	Pitch	B ₁ Maximum Note 4	E ₂ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ , B ₀ & K ₀	
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	2.0 ±0.05 or 4.0 ±0.10 (0.079 ±0.002 or 0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)	Note 5	
12 mm	Single (4 mm) and Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	2.0 ±0.05 (0.079 ±0.002) or 4.0 ±0.10 (0.157 ±0.004) or 8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
2. The tape, with or without components, shall pass around R without damage (see Figure 4).
3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481-D, paragraph 4.3, section b).
4. B₁ dimension is a reference dimension for tape feeder clearance only.
5. The cavity defined by A₀, B₀ and K₀ shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes (see Figure 2).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape (see Figure 3).
 - (e) see Addendum in EIA Standard 481-D for standards relating to more precise taping requirements.

Packaging Information Performance Notes

- 1. Cover tape break force:** 1.0 kg minimum.
- 2. Cover tape peel strength:** The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 newton (10 to 100 gf)
12 mm	0.1 to 1.3 newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

- 3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA Standards 556 and 624.

Figure 2 – Maximum Component Rotation

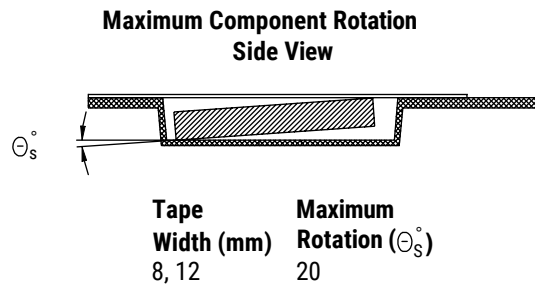
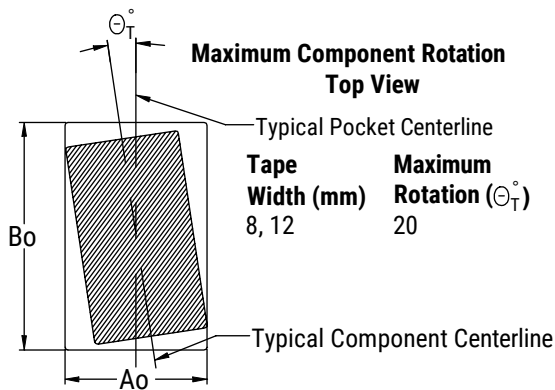


Figure 3 – Maximum Lateral Movement

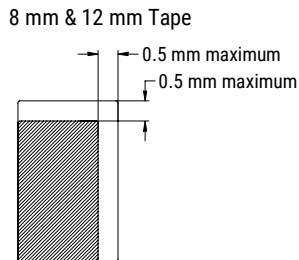


Figure 4 – Bending Radius

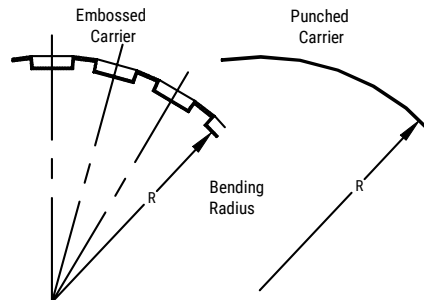
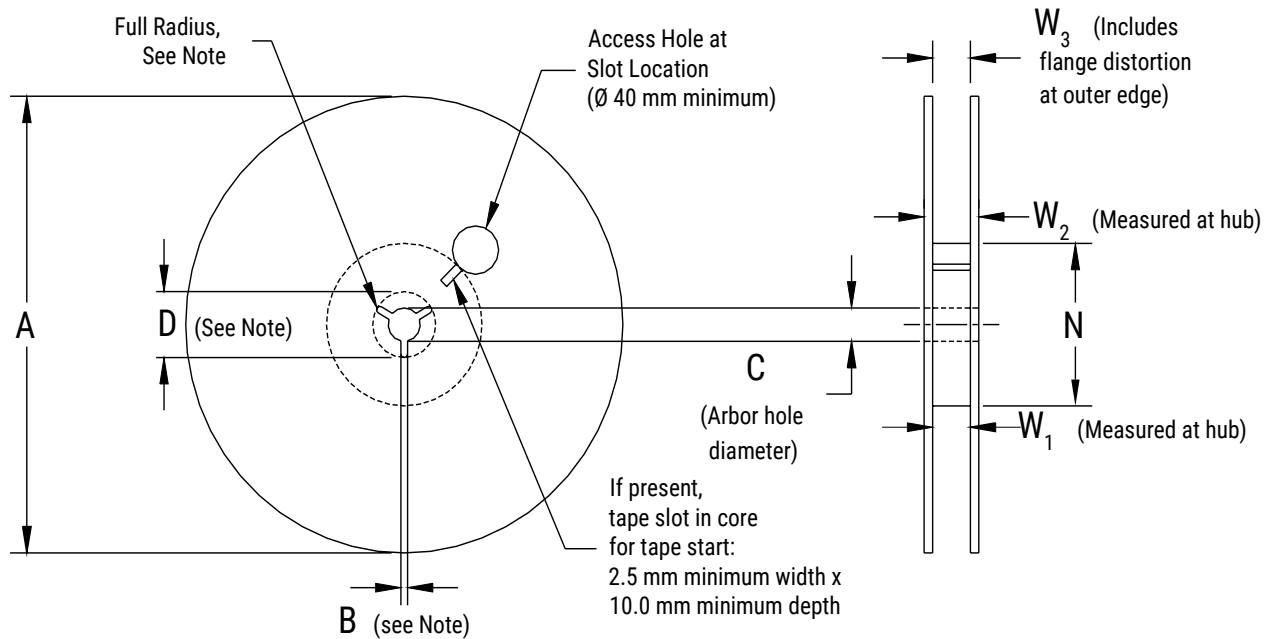


Figure 5 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 5 – Reel Dimensions

Metric will govern

Constant Dimensions – Millimeters (Inches)				
Tape Size	A	B Minimum	C	D Minimum
8 mm	178 ±0.20 (7.008 ±0.008)	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)
12 mm	330 ±0.20 (13.000 ±0.008)			
Variable Dimensions – Millimeters (Inches)				
Tape Size	N Minimum	W_1	W_2 Maximum	W_3
8 mm	50 (1.969)	8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)	Shall accommodate tape width without interference
12 mm		12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	

Figure 6 – Tape Leader & Trailer Dimensions

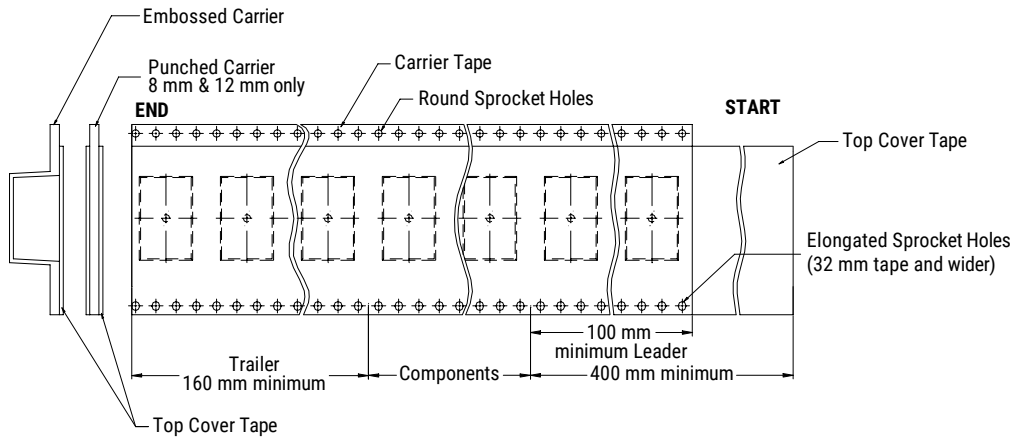
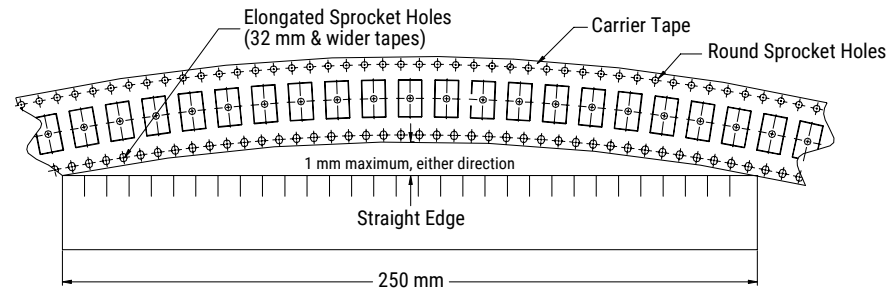


Figure 7 – Maximum Camber



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