RF/Microwave Capacitors RF/Microwave Multilayer Capacitors (MLC) 800E Series NPO Ceramic High RF Power Multilayer Capacitors





GENERAL DESCRIPTION

KYOCERA AVX's 800 E Series offers superb performance in demanding high RF power applications requiring consistent and reliable operation. The combination of highly conductive metal electrode systems, optimized case geometries, and proprietary dielectrics, yields the lowest ESR. KYOCERA AVX's new NPO low loss rugged dielectrics are designed to provide superior heat transfer in high RF power applications. Ultralow ESR and superior thermal performance ensure that the 800 E Series products are your best choice for high RF power applications from VHF through microwave frequencies.

FUNCTIONAL APPLICATIONS

- Bypass
- Impedance Matching Coupling DC Blocking
- Tuning

CIRCUIT APPLICATIONS

- HF/RF Power Amplifiers
- Plasma Chambers

- Transmitters
- Medical (MRI coils)
- Antenna Tuning

ENVIRONMENTAL CHARACTERISTICS

Thermal Shock	Mil-STD-202, Method 107, Condition A					
Moisture Resistance	Mil-STD-202, Method 106					
Low Voltage Humidity	Mil-STD-202, Method 103, condition A, with 1.5 VDC applied while subjected to an environment of 85°C with 85% relative humidity for 240 hours					
Life Test	MIL-STD-202, Method 108, for 2000 hours, at 125°C. Voltage applied. 120% of WVDC for capacitors rated at 1250 volts DC or less. 100% of WVDC for capacitors rated above 1250 volts DC					
Termination Styles	Available in various surface mount and leaded styles. See Mechanical Configurations					
Terminal Strength	Terminations for chips and pellets withstand a pull of 10 lbs. min., 25 lbs. typical, for 5 seconds in direction perpendicular to the termination surface of the capacitor. Test per MIL-STD-202, method 211.					

FEATURES

- Case E Size (.380" x .380")
- Capacitance Range 3.3 pF to 5100 pF
- Ultra Low ESR
- High O
- · High RF Power
- Ultra-Stable Performance
- High RF Current/Voltage
- High Reliability

PACKAGING OPTIONS



Tape & Reel



Trav (96 pcs)



ELECTRICAL SPECIFICATIONS

Temperature Coefficient (TCC)	0 ±30 PPM/°C (-55°C to +125°C)				
Capacitance Range	3.3 pF to 5100 pF				
Operating Temperature	-55°C to +125°C				
Quality Factor	Greater than 5,000 (3.3 pF to 1000 pF) @ 1 MHz. Greater than 5,000 (1100 pF to 5,100 pF) @ 1 KHz.				
Insulation Resistance (IR)	Max Test Voltage is 500 VDC 10 ⁵ Megohms min. @ 25°C at 500 VDC 10 ⁴ Megohms min. @ 125°C at 500 VDC				
Working Voltage (WVDC)	See Capacitance Values table				
Dielectric Withstanding Voltage (DWV)	120% of WVDC for 5 seconds				
Aging Effects	None				
Piezoelectric Effects	None				
Capacitance Drift	± (0.02% or 0.02 pF), whichever is greater				
Retrace	Less than ±(0.02% or 0.02 pF), whichever is greater.				

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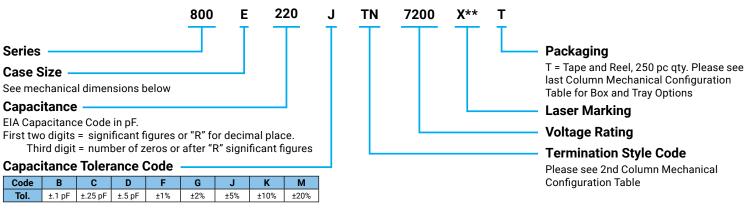
CAPACITANCE VALUES

Cap. Code	Cap. (pF)	Tol.	Rated WVDC	Cap. Code	Cap. (pF)	Tol.	Rated WVDC	Cap. Code	Cap. Code	Tol.	Rated WVDC		
3R3	3.3					360	36			391	390		
3R6	3.6			390	39			431	430				
3R9	3.9			430	43		7200	471	470		3600		
4R3	4.3			470	47			511	510				
4R7	4.7			510	51			561	560				
5R1	5.1	B, C, D		560	56			621	620				
5R6	5.6	, с, <i>р</i>		620	62			681	680				
6R2	6.2			680	68			751	750				
6R8	6.8			750 75		821	820						
7R5	7.5			820	82			911	910		2500		
8R2	8.2			910	91			102	1000				
9R1	9.1			101	100			112	1100				
100	10		7200	111 110	F, G, J, K		122	1200	F, G, J, K	2000			
110	11		7200	121	120			132	1300	г, G, J, K			
120	12			131	130			152	1500				
130	13			151	150			162	1600				
150	15			161	160			182	1800				
160	16			181	180			202	2000				
180	18	FGIK		201	200		3600	222	2200				
200	20	F, G, J, K		221	220		3000	242	2400				
220	22			241	240			272	2700				
240	24			271	270			302	3000		2000		
270	27			301	300			332	3300				
300	30			331	330			392	3900				
330	33		361	360			472	4700					
								512	5100				

VRMS = 0.707 X WVDC

· SPECIAL VALUES, TOLERANCES AND MATCHING AVAILABLE. PLEASE CONSULT FACTORY

HOW TO ORDER



**Optional

The above part number refers to a 800 E Series (case size E) 22 pF capacitor, J tolerance (\pm 5%), 7200 WVDC, with TN termination (Tin Plated over Non-Magnetic Barrier Termination), laser marking and Tape and Reel Packaging Add "D' instead of "X" for double-sided marking.

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MECHANICAL CONFIGURATION

Series & Case	Term.	n. Case Size	Outline W/T is a Termination	Body Dimensions inches (mm)				Lead and Termination imensions and Material	Dkg Type	Dkg Codo					
Size			W/T is a Termination Surface	Length (L)	Width (W)	Thickness (T)	Overlap (Y)	Materials	Pkg Type	Pkg Code					
800E	Т	E Solderable Nickle Barrier	$\begin{array}{c c} Y \rightarrow \leftarrow & \downarrow \\ & & \\ & & \\ & & \\ & & \\ & \rightarrow & LT & \leftarrow \uparrow \rightarrow & \leftarrow \end{array}$.380+.015010 (9.65+0.38-0.25)	.380+.015010	.190 (4.83)	.040 (1.02) max.	RoHS Compliant Tin Plated over Nickel Barrier Termination	T&R, 250 pcs Tray, 24 or 96 pcs	T J24 or J96					
800E	MS	E Microstrip	$\begin{array}{c c} \downarrow & \rightarrow \mid \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$					High Purity Silver Leads L _L = .750 (19.05) min	Tray, 16 or 32 pcs	J16 or J32					
800E	AR	E Axial Ribbon	$\begin{array}{c c} \downarrow & \rightarrow \mid \iota_{L} \mid \leftarrow & \downarrow^{T_{L}} \\ \hline w_{L} & & & \downarrow^{T_{L}} \\ \hline w_{L} & & & & \downarrow^{T_{L}} \\ \hline & & \downarrow^{T_{L}} \\ \hline & & \downarrow^{T_{L}} \\$						(9.65+0.38 -0.25)	(9.65+0.38 -0.25)	max.	N/A	$ \begin{array}{l} W_{L} = .350 \pm .010 \; (8.89 \pm 0.25) \\ T_{L} = .010 \pm .005 \; (0.25 \pm 0.13) \\ \text{Leads are Attached with} \\ \text{High Temperature Solder.} \end{array} $	Tray, 16 or 32 pcs	J16 or J32
800E	AW	E Axial Wire	→ L _L ← → L _T ← → L _T ←					Silver-plated Copper Leads Dia. = .032 ±.002 (.813 ±.051) L _L = 2.25 (57.2) min.	Box, 20 pcs	B20					

Custom lead styles and lengths are available; consult factory. All leads are high purity silver attached with high temperature solder and are RoHS compliant.

NON MECHANICAL CONFIGURATION

Series & Case	Term.		W/Lie a Lormination	Body Dimensions inches (mm)				Lead and Termination imensions and Material	Pkg Type	Dkg Codo
Size	Code			Length (L)	Width (W)	Thickness (T)	Overlap (Y)	Materials	гкутуре	Pkg Code
800E	TN	E Non-Mag Solderable Barrier	$\begin{array}{c} Y \rightarrow \parallel \longleftarrow \qquad \downarrow \\ & & \\ & & \\ & & \\ & \rightarrow LT \leftarrow \uparrow \rightarrow \downarrow \leftarrow \end{array}$.380 ±.010 (9.65 ±0.25)	170 (4.32) max.	.040 (1.02) max.	RoHS Compliant Tin Plated over Non-Magnetic Barrier Termination	T&R, 250 pcs Tray, 24 or 96 pcs	T J24 or J96
800E	MN	E Non-Mag Microstrip	$\begin{array}{c c} \downarrow & \rightarrow \mid \downarrow_{L} \mid \leftarrow & \downarrow_{T} \\ \hline w_{L} & & & \\ \hline w_{L} & & & \\ \hline \end{array} \right) \downarrow_{T} \mid \leftarrow & \\ \hline \end{array}$				N/A	$\begin{array}{c} \mbox{High Purity} \\ \mbox{Silver Leads} \\ \mbox{L}_{\tiny L} = .750 \ (19.05) \ min \\ \mbox{W}_{\tiny L} = .350 \pm .010 \ (8.89 \pm 0.25) \\ \mbox{T}_{\tiny L} = .010 \pm .005 \ (0.25 \pm 0.13) \\ \mbox{Leads are Attached with} \\ \mbox{High Temperature Solder.} \end{array}$	Tray, 16 or 32 pcs	J16 or J32
800E	AN	E Non-Mag Axial Ribbon	$\begin{array}{c c} \downarrow & & & T_L \\ \hline \begin{matrix} \downarrow & & \rightarrow \\ \hline w_L & & \end{matrix} \\ \hline \hline \begin{matrix} \downarrow & & \downarrow \\ \hline \end{matrix} \\ \hline \end{matrix} \\ \hline \begin{matrix} \downarrow & & \downarrow \\ \hline \end{matrix} \\ \hline \end{matrix} \\ \hline \end{matrix} \\ \hline \end{matrix} \\ \hline \begin{matrix} \downarrow & & \downarrow \\ \hline \end{matrix} \\ \hline \hline \\ \hline \end{matrix} \\ \hline \hline \\ \hline \hline \\ \hline \\$						Tray, 16 or 32 pcs	J16 or J32
800E	BN	E Non-Mag Axial Wire	→ LL ← w → LT ← ↑ ←					Silver-plated Copper Leads Dia. = .032 ±.002 (.813 ±.051) L _L = 2.25 (57.2) min.	Box, 20 pcs	B20

Custom lead styles and lengths are available; consult factory. All leads are high purity silver attached with high temperature solder and are RoHS compliant.

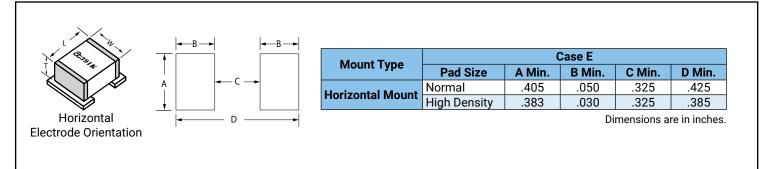
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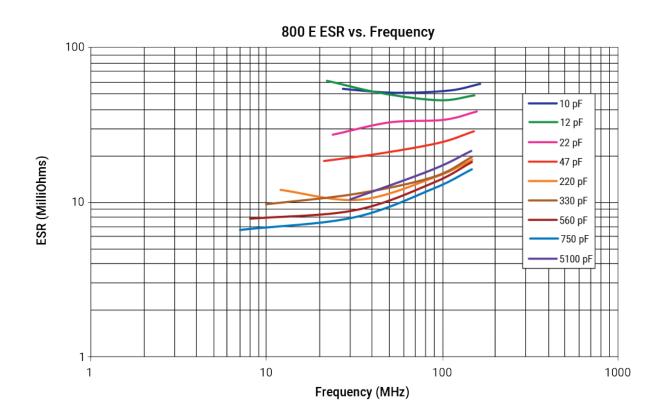
- RF MICROWAVE PRODUCTS -



SUGGESTED MOUNTING PAD DIMENSIONS



PERFORMANCE DATA

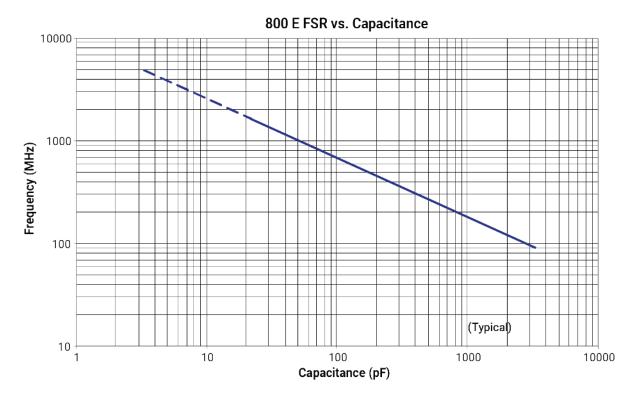


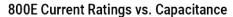
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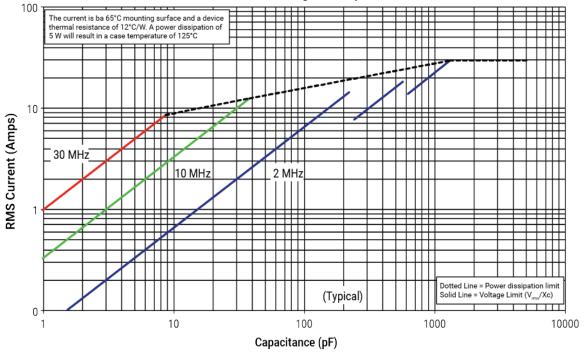
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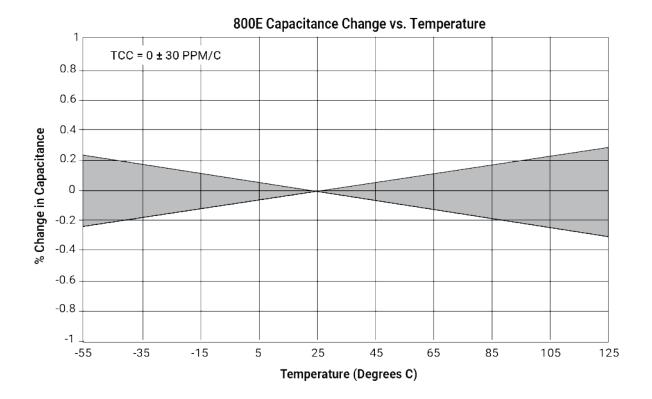


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