

A Product Line of Diodes Incorporated

the wide VCC range of 1.65V to 5.5V.

Description



Low Voltage SPST 0.8Ω Analog Switch

The PI5A3166 is a high-bandwidth, fast single-pole single-

throw (SPST) CMOS switch. It can be used as an analog

switch or as a low-delay bus switch. The device features ultra

low RON of  $0.8\Omega$  typical at 3.0V VCC and will operate over

The PI5A3166 features very low quiescent current even when

the control voltage is lower than the VCC supply. This feature

services the mobile handset applications very well by allowing

direct interface with baseband processor general purpose I/Os.

The control input, S, is independent of supply voltage.

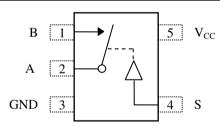
### Features

- → CMOS Technology for Bus and Analog Applications
- → Low On-Resistance:  $0.8\Omega$  at 3.0V
- → Wide V<sub>CC</sub> Range: 1.65V to 5.5V
- ➔ Rail-to-Rail Signal Range
- → Control Input Overvoltage Tolerance: 5.5V
- ➔ Fast Transition Speed: 2ns at 5.0V
- → High Bandwidth: 200MHz
- ➔ Extended Industrial Temperature Range: -40°C to 85°C
- → I/O pins Has Power-off Protection Function
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → Packaging (Pb-free & Green):
  - 5-pin SOT23
  - 5-pin SC70

# **Applications**

- → Cell Phones
- → PDAs
- ➔ Portable Instrumentation
- → Battery powered Communications
- ➔ Computer Peripherals

# **Pin Configuration**



SOT23 and SC70 Package (Top View)

# Pin Description

Pin No	Pin Name	Description
1	В	Data Port
2	А	Common Output/Data Port
3	GND	Ground
4	S	Logic Control
5	VCC	Positive Power Supply

#### **Logic Function Table**

Logic Input(S)	Function(A to B)
0	OFF
1	ON

Notes:

See <u>https://www.diodes.com/guality/lead-free/</u> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>

<sup>1.</sup> No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.





# **Maximum Ratings**

Storage Temperature	65℃ to +150℃
Ambient Temperature with Power Applied	40°℃ to +85°℃
Supply Voltage V <sub>CC</sub>	0.5V to +7.0V
DC Switch Voltage V <sub>S</sub>	0.5V to +7.0V
DC Input Voltage V <sub>IN</sub>	0.5V to +7.0V
DC Output Current V <sub>OUT</sub>	128mA
DC V <sub>CC</sub> or Ground Current I <sub>CC</sub> /I <sub>GND</sub>	±100mA
Junction Temperature under Bias (TJ)	150°C
Junction Lead Temperature (TL)	
(Soldering, 10 seconds)	
ESD (HBM)	4KV
Power Dissipation (PD) @ +85°C	SOT23 250mW
	SC70 200mW

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **Recommended Operating Conditions**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CC</sub>	Operating Voltage	-	1.65	-	5.5	V
V <sub>IN</sub>	DC Input Voltage	-	0	-	V <sub>CC</sub>	V
Vs	Switch Input Voltage	-	0	-	5.5	V
V <sub>OUT</sub>	Output Voltage	-	0	-	V <sub>CC</sub>	V
T <sub>A</sub>	Operating Temperature	-	-40	25	85	°C
t <sub>r</sub> , t <sub>f</sub> Input Rise and F	Input Diss and Fall Time	Control Input $Vcc = 2.7V$ to $3.6V$	0	-	10	ns/V
	Input Rise and Fan Time	Control Input $VCC = 4.5V$ to $5.5V$	0	-	5	ns/V

Note: Control input must be held HIGH or LOW; it must not float.

### **DC Electrical Characteristics**

#### $(T_A = -40^{\circ}C \text{ to } 85^{\circ}C, \text{ unless otherwise noted.})$

Parameter	Description	Test Conditions	Supply Voltage	Min	Тур	Max	Units	
V <sub>IAR</sub>	Analog Input Signal Range	-	V <sub>CC</sub>	0	-	VCC	V	
		$I_{\rm A} = 100 {\rm mA},  V_{\rm B} = 0 {\rm V}$		-	0.7	1.1		
		$I_A = 100 \text{mA}, V_B = 2.4 \text{V}$	4.5V	-	0.6	1.0		
		$I_A = 100 \text{mA}, V_B = 4.5 \text{V}$		-	0.8	1.2		
		$I_{\rm A} = 100 {\rm mA},  V_{\rm B} = 0 {\rm V}$	2.01/	-	0.8	1.3		
R <sub>ON</sub>	ON Resistance <sup>(1)</sup>	$I_A = 100 \text{mA}, V_B = 3.0 \text{V}$	3.0V	-	0.9	1.9	Ω	
		$I_{A} = 100 \text{mA}, V_{B} = 0 \text{V}$	2.3V	-	1.0	1.5		
		$I_A = 100 \text{mA}, V_B = 2.3 \text{V}$	2.5 V	-	1.2	1.8		
		$I_{\rm A} = 100 {\rm mA},  V_{\rm B} = 0 {\rm V}$	1.65V	-	1.3	1.9		
		$I_A = 100 \text{mA}, V_B = 1.65 \text{V}$		-	2.0	2.8		
	ON Resistance Flatness <sup>(2)</sup>	$I_A = 100 \text{mA}, V_B = 0 \text{V}, 2.4 \text{V}, 4.5 \text{V}$	4.5V	-	0.2	0.4		
D		$I_A = 100 \text{mA}, V_B = 0V, 1.5V, 3.3V$	3.3V	-	0.2	0.4	0	
R <sub>ONF</sub>		$I_A = 100 \text{mA}, V_B = 0 \text{V}, 1.1 \text{V}, 2.5 \text{V}$	2.5V	-	0.4	0.6	Ω	
		$I_A = 100 \text{mA}, V_B = 0V, 0.7V, 1.8V$	1.8V	-	1.0	1.4		
			$V_{CC} = 1.65 V$	1	-	-		
	Input High Voltage		$V_{CC} = 2.3 V$	1.2	-	-		
V <sub>IH</sub>		Logic High Level	$V_{CC} = 3V$	1.3	-	-	V	
			$V_{CC} = 4.2V$	1.5	-	-		
			$V_{CC} = 5.5 V$	1.8	-	-		



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PI5A3166

			$V_{CC} = 1.65 V$	-	-	0.4	
			$V_{CC} = 2.3 V$	-	-	0.6	
V <sub>IL</sub>	Input Low Voltage	Logic Low Level	$V_{CC} = 3V$	-	-	0.8	V
			$V_{CC} = 4.2V$	-	-	1	
			$V_{\rm CC} = 5.5 V$	-	-	1.2	
I	Source Off Leakage	$V_{CC}$ =5.5V, VA=1V, 4.5V	$V_{CC} = 3V$	-20	_	+20	
$I_{OFF(B)}$	Current	$V_B = 1V, 4.5V$	$\mathbf{v}_{\rm CC} = \mathbf{J} \mathbf{v}$	-20	-	720	nA
I	Channel On Leakage		$V_{CC} = 1.65$ to	-40	_	+40	IIA
I <sub>NC(A, B)</sub>	Current		5.5V	-40	-	740	
т	Input Leakage Current for	$0 \leq V_A \leq 5.5 V$	$V_{CC} = 0V$	-5	_	5	۸
I <sub>PWROFF</sub>	Power off	$0 \leq V_B \leq 5.5 V$	$v_{\rm CC} = 0 v$	-5	-	5	μA
I <sub>CC</sub>	Quiacaant Supply Current	All channels ON or OFF,	$V_{CC} = 3.6V$	-	0.002	0.1	۸
	Quiescent Supply Current	$V_B = V_{CC}$ or GND, $I_{OUT}=0$	$V_{\rm CC} = 5.5 V$	-	0.002	0.1	μA

Notes:

1. Measured by voltage drop between A and B pins at the indicated current through the device. ON resistance is determined by the lower of the voltages on two ports (A or B).

2. Flatness is defined as difference between maximum and minimum value of ON resistance over the specified range of conditions. Guaranteed by design.

# **Capacitance**<sup>(1)</sup>

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
C <sub>IN</sub>	Control Input		-	3.5	-	
C <sub>IO-B</sub>	For B Port, Switch OFF	$V_{CC} = 5.0V, f = 1 MHz, T_A = 25^{\circ}C$	-	15.0	-	pF
C <sub>IOA-ON</sub>	For A Port, Switch ON		-	34.0	-	r

Notes:

1. Capacitance is characterized but not tested in production

### Switch and AC Characteristics<sup>(1)</sup>

Parameter	Description	Test Conditions	Supply Voltage	Min	Тур	Max	Units
+	π	0	$V_{\rm CC} = 2.7  \text{V}$ to 3.6 V	-	3	-	
t <sub>on</sub>	Turn on Time	See Figure 1	$V_{\rm CC} = 4.5 V$ to 5.5 V	-	2	-	
t	Turn off Time	See Figure 1	$V_{\rm CC} = 2.7  \text{V}$ to 3.6 V	-	9	-	ns
t <sub>off</sub>	I um on Time	See Figure 1	$V_{\rm CC} = 4.5 V$ to 5.5 V	-	5	-	
		$C_{\rm L} = 1 \rm{nF}, V_{\rm GEN} = 0 \rm{V},$	$V_{CC} = 5.0V$	-	35	-	G
Q Charge Injection		R <sub>GEN</sub> =0Ω. See Figure 2	$V_{CC} = 3.3V$	-	25	-	pC
O <sub>IRR</sub>	Off Isolation	$R_L=50\Omega$ , $V_{GEN}=0V$ , $R_{GEN}=0\Omega$ , f=1MHz. See Figure 3 <sup>(2)</sup>	$V_{CC} = 1.65 V$ to 5.5V	-	-70	-	dB
f3dB	-3dB Bandwidth	See Figure 6	$V_{\rm CC}$ =1.65V to 5.5V	-	200	-	MHz
T <sub>HD</sub>	Total Harmonic Distortion	R <sub>L</sub> =600Ω, V <sub>IN</sub> =0.5Vpp, f=20Hz to 20kHz See Figure 7	$V_{CC}$ =2.7V to 4.2V	-	0.015	-	%

Notes:

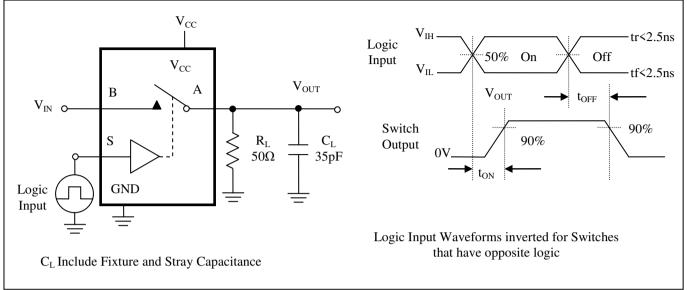
1. Guaranteed by design.

2. Off Isolation =  $20 \text{ Log}_{10}[V_B/V_A]$  and is measured in dB.

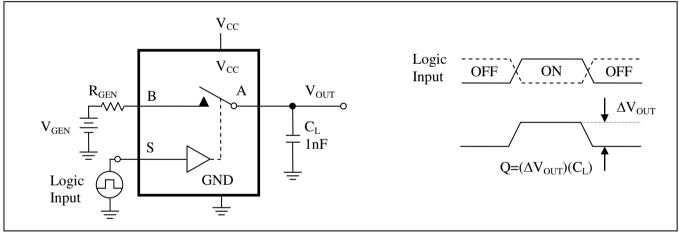


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# **Test Circuits and Timing Diagrams**



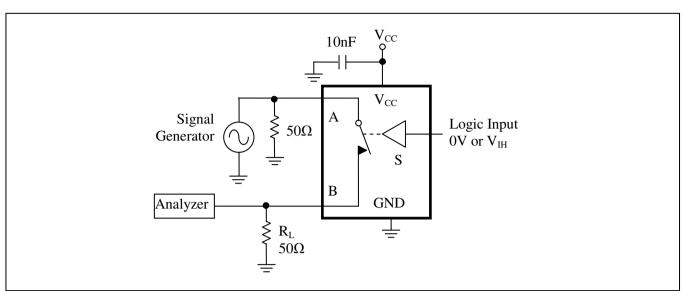
#### Figure 1. Turn ON/OFF Timing



#### Figure 2. Charge Injection Test







## Figure 3. Off Isolation

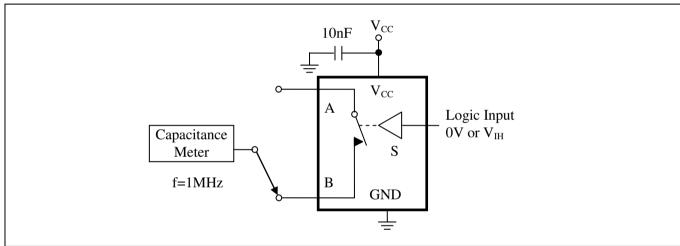


Figure 4. Channel Off Capacitance

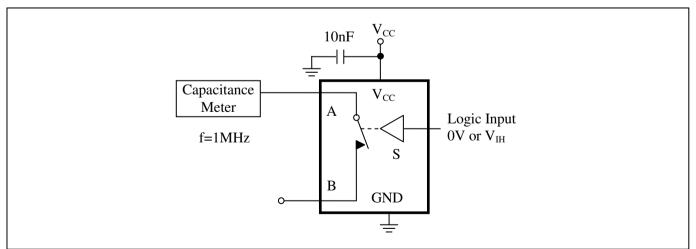
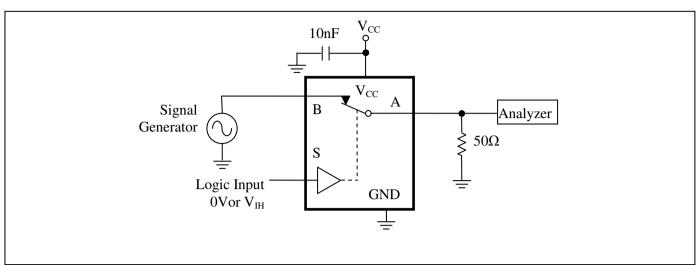


Figure 5. Channel On Capacitance







#### Figure 6. Bandwidth

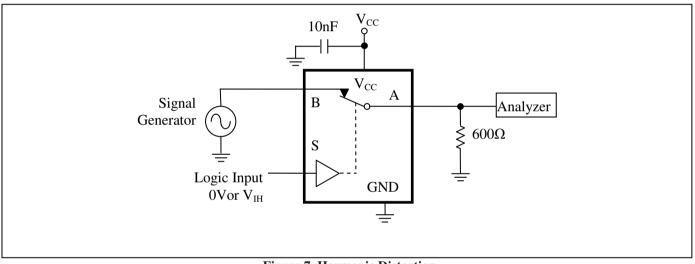


Figure 7. Harmonic Distortion

# **Part Marking**

TA Package pT: PI5A3166TAE



Y: Year W: Workweek Bar above "T" means Fab3 of MGN C Package

pTXX

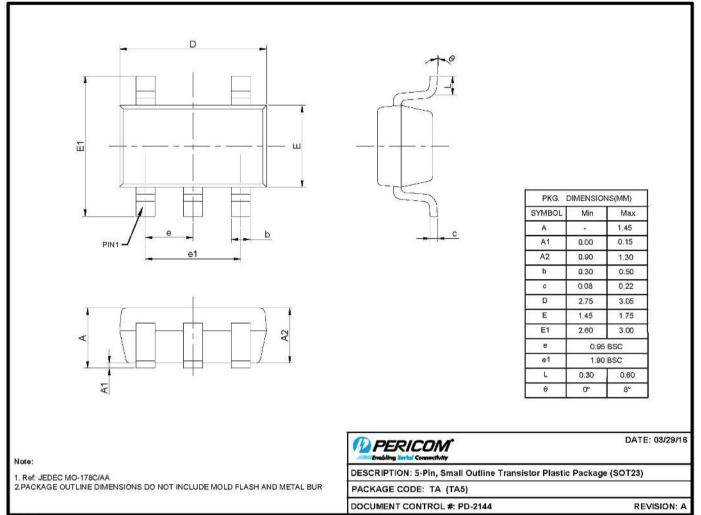
pT: Top Mark XX: Date Code (Year & Work Week) Bar above "T" means Fab3 of MGN





# **Packaging Mechanical**

SOT23-5 (TA)

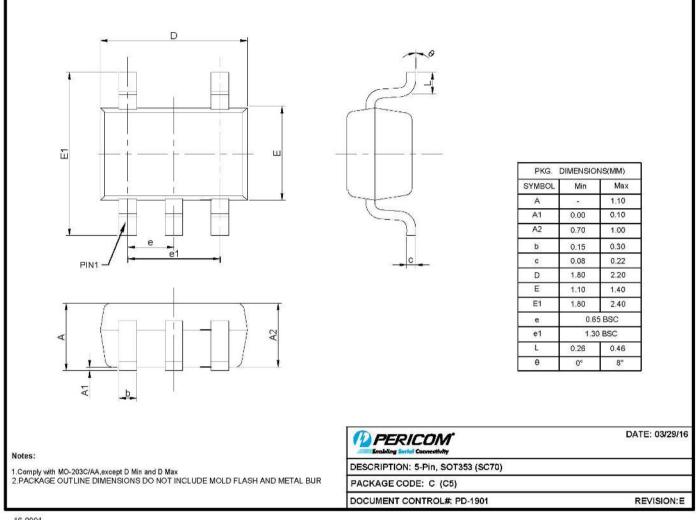


16-0081





#### SC70-5 (C)



16-0091

#### For latest package information:

Please see http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/.

# **Ordering Information**

Part Number	Package Code	Description
PI5A3166CEX	С	5-Pin, SOT353 (SC70)
PI5A3166TAEX	ТА	5-Pin, Small Outline Transistor Plastic Package (SOT23)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain  $\leq$ 900ppm bromine,  $\leq$ 900ppm chlorine ( $\leq$ 1500ppm total Br + Cl) and  $\leq$ 1000ppm antimony compounds.

4. E = Pb-free and Green

5. X suffix = Tape/Reel





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