# Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 20<mark>10</mark> Renesas Electronics Corporation

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#### Notice

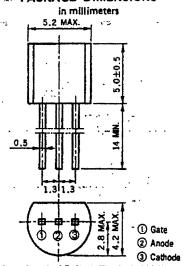
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# O3P2M,O3P4M

## 0.47 A(R.M.S.)ALL DIFFUSED MOLD TYPE SCR

#### PACKAGE DIMENSIONS



#### DESCRIPTION

The 03P2M and 03P4M are P-gate all diffused mold type SCR rated at 0.47 Amps RMS maximum on-state current, with rated voltages up to 400 volts.

#### **FEATURES**

- Plastic TO-92 package.
- 200 μA gate sensitivity.
- 5 mA holding current.
- 8 A surge current.

#### **APPLICATIONS**

Cassette tape recorder, Television

Automobile equipment

Photoflash

Automatic gas lighter,

Solid-state relay

Light display equipment

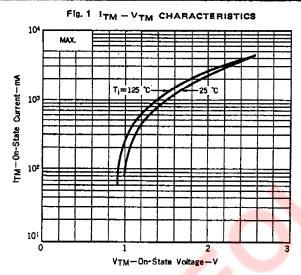
Motor, solenoid and temperature control etc.

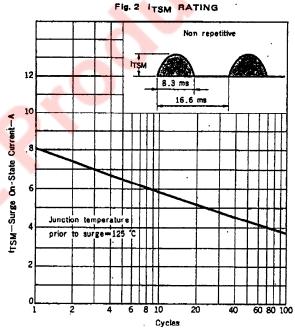
### MAXIMUM RATINGS ( $R_{GK} = 1 \text{ k}\Omega$ )

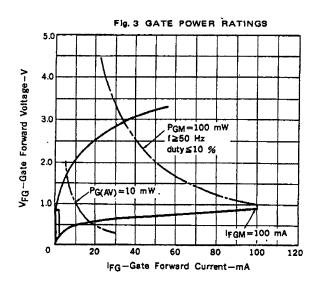
ÎTEM	SYMBOL	03P2M 03P4M		UNIT
Non-Repetitive Peak Reverse Voltage	VRSM	300 500		٧
Non-Repetitive Peak Off-State Voltage	VDSM	300 500		v
Repetitive Peak Reverse Voltage	VRRM	200 400		v
Repetitive Peak Off-State Voltage	VDRM	200 400		ν
Average On-State Current	<sup>[</sup> T(AV)	0.3 (Ta=30 °C, Sin	A	
RMS On-State Current	IT(RMS)	0.47		A
Surge On-State Current	itsm	8 (f=50 Hz, 1 cycle)		A
Fusing Current	∫i⊤²dt	0.15(1 ms ≤ t ≤ 10 ms)		A²s
Peak Gate Power Dissipation	PGM	0.1 (f ≥ 50 Hz, duty ≤ 10 %)		W
Average Gate Power Dissipation	PG(AV)	0.01		W
Peak Gate Forward Current	<sup>1</sup> FGM	0.1 (f ≥ 50 Hz, duty ≤ 10 %)		A
Peak Gate Reverse Voltage	VRGM	6		٧
Junction Temperature	T <sub>j</sub>	-40 to +125		°c
Storage Temperature	T <sub>stg</sub>	-40 to +150		°c

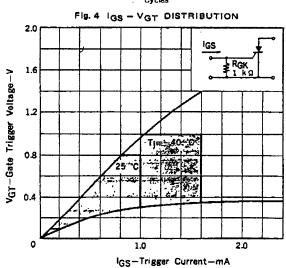
## ELECTRICAL CHARACTERISTICS (T<sub>j</sub> = 25 °C, R<sub>GK</sub> = 1 k $\Omega$ )

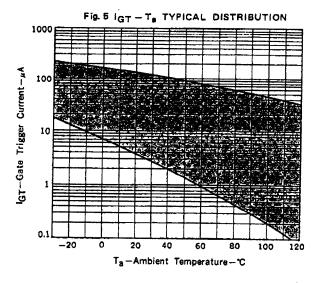
ITEM	SYMBOL	TEST CONDITIONS		MIN.	TYP	MAX.	UNIT
Repetitive Peak Reverse Current	IRRM	.VRM = VRRM	Tj = 25 °C	-	_	10	μΑ
			T <sub>j</sub> = 125 °C	_		100	
Repetitive Peak Off-State Current	IDRM	V <sub>DM</sub> = V <sub>DRM</sub>	Tj = 25°C	_		10	μΑ
			T <sub>j</sub> = 125 °C	-	-	100	
Critical Rate of Rise of Off-State Voltage	dv/dt	VDM = 2 VDRM, Tj = 125 °C		_	40	-	V/μ <b>s</b>
On-State Voltage	VTM	I <sub>TM</sub> = 4 A				2.5	٧
Gate Trigger Current	IGT	V <sub>DM</sub> = 6 V, R <sub>L</sub> = 100 Ω		_		200	μΑ
Gate Trigger Voltage	VGT	V <sub>DM</sub> = 6 V, R <sub>L</sub> = 100 Ω		_	_	0.8	V
Gate Non-Trigger Voltage	VGD	$V_{DM} = \frac{1}{2} V_{DRM}, T_j = 125 °C$		0.1	-	-	v
Holding Current	lн	V <sub>DM</sub> = 24 V, I <sub>TM</sub> = 4 A		-	-	5	mA
Commutating Turn-Off Time	tq	$I_{TM} = 200 \text{ mA}, \text{ dit/dt} = 15 \text{ A/}\mu\text{s}$ $V_{RM} \ge 25 \text{ V}, V_{DM} = \frac{2}{3} \text{ VDRM}$ $dv/dt = 20 \text{ V/}\mu\text{s}, T_j = 125 ^{\circ}\text{C}$		_	25	-	μs
Thermal Resistance	R <sub>th(j-c)</sub>	Junction to Case (flat side of case is temperature reference point)		<del>-</del>	-	125	°C/W
	R <sub>th(j-a)</sub>	Junction to Ambient		-		230	1

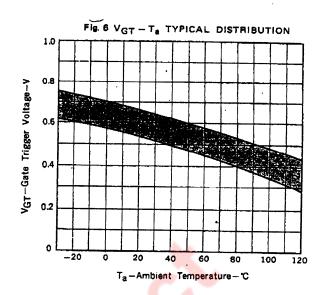


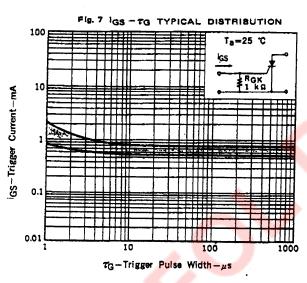


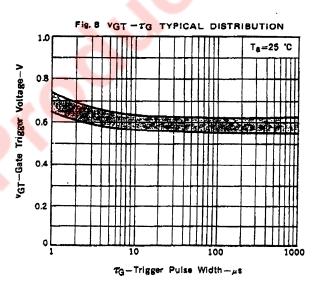


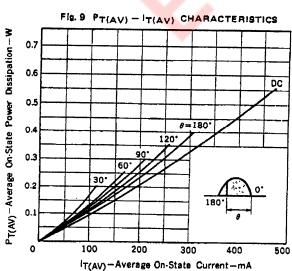


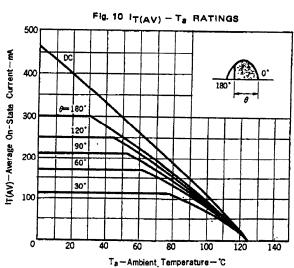


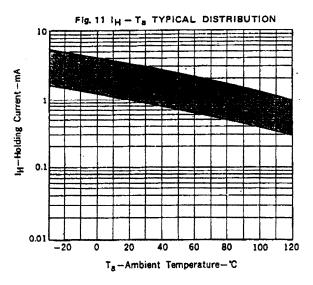


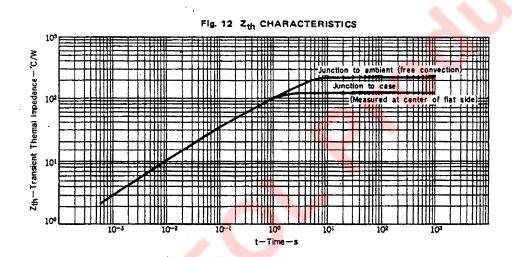












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