

ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		Unit
			Typ.	Max.	
Wavelength at Peak Emission I _F = 10mA	λ _{peak}	Super Bright Orange	610	-	nm
Dominant Wavelength I _F = 10mA	λ _{dom} ^[1]	Super Bright Orange	605	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	Super Bright Orange	29	-	nm
Capacitance	C	Super Bright Orange	15	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	Super Bright Orange	2.0	2.35	V
Reverse Current (V _R = 5V)	I _R	Super Bright Orange	-	10	μA

Notes:

1. The dominant wavelength (λ_d) above is the setup value of the sorting machine. (Tolerance λ_d : ±1nm.)

2. Forward voltage: ±0.1V.

3. Wavelength value is traceable to CIE127-2007 standards.

4. Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Symbol	Value	Unit
Power Dissipation	P _D	75	mW
Reverse Voltage	V _R	5	V
Junction Temperature	T _j	115	°C
Operating Temperature	T _{op}	-40 to +85	°C
Storage Temperature	T _{stg}	-40 to +85	°C
DC Forward Current	I _F	30	mA
Peak Forward Current	I _{FM} ^[1]	195	mA
Electrostatic Discharge Threshold (HBM)	-	3000	V

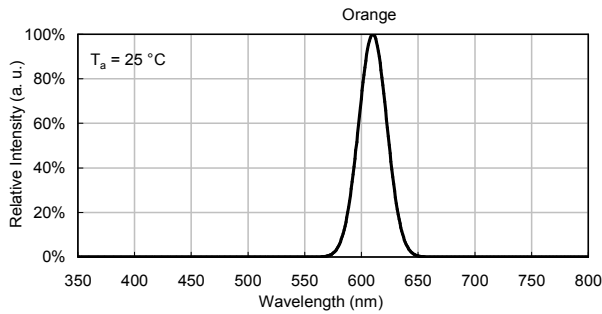
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

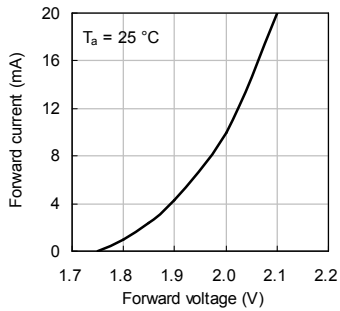
TECHNICAL DATA

RELATIVE INTENSITY vs. WAVELENGTH

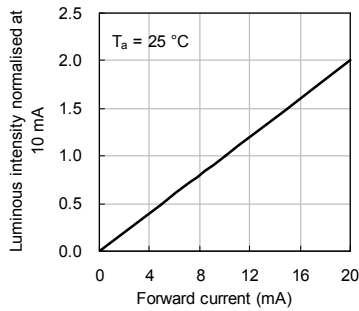


SUPER BRIGHT ORANGE

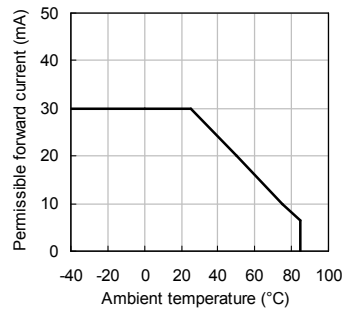
Forward Current vs. Forward Voltage



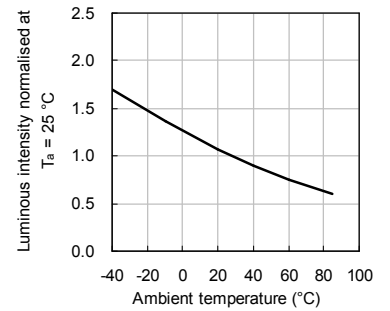
Luminous Intensity vs. Forward Current



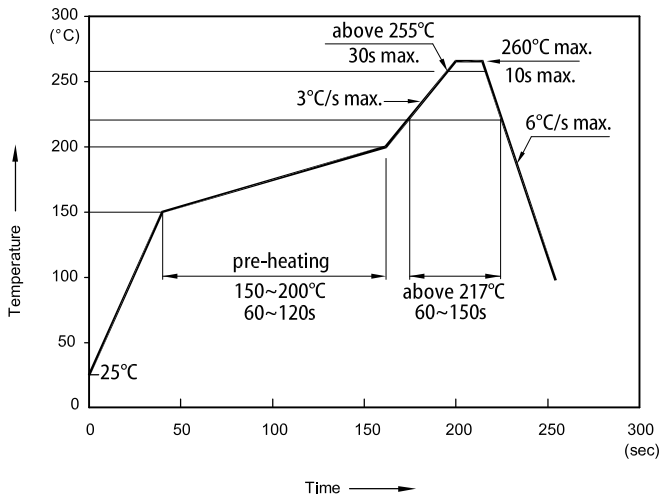
Forward Current Derating Curve



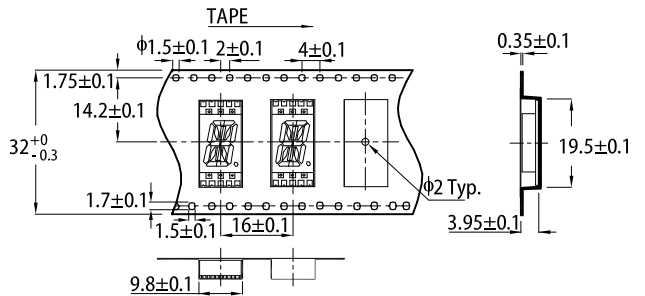
Luminous Intensity vs. Ambient Temperature



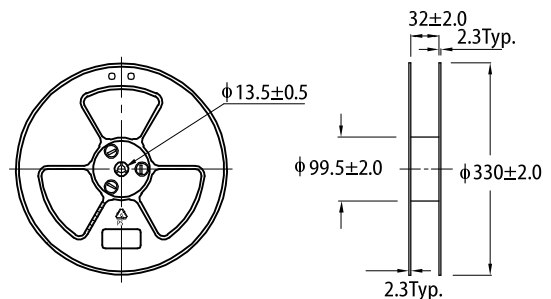
REFLOW SOLDERING PROFILE for LEAD-FREE SMD PROCESS



TAPE SPECIFICATIONS (units: mm)

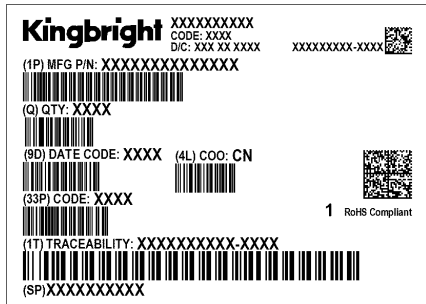
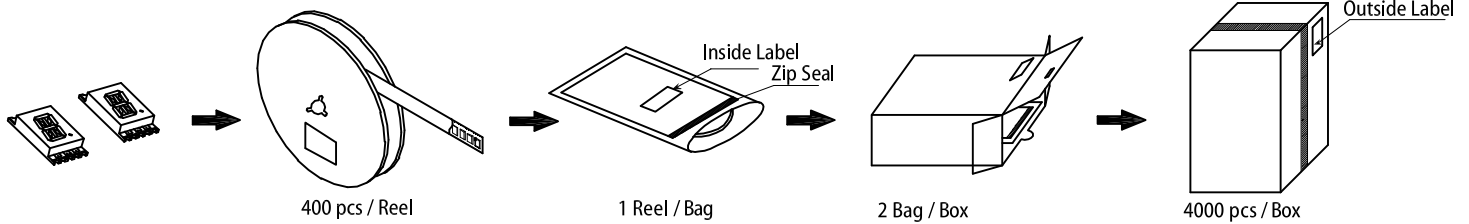


REEL DIMENSION (units: mm)



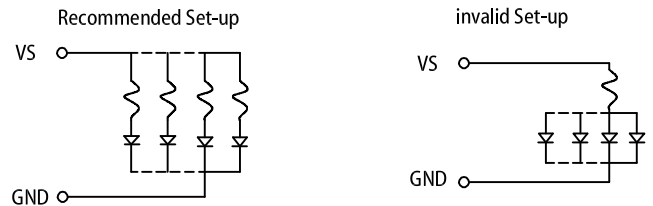
- Notes:
1. Don't cause stress to the LEDs while it is exposed to high temperature.
 2. The maximum number of reflow soldering passes is 2 times.
 3. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.

PACKING & LABEL SPECIFICATIONS



CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.
3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.



PRECAUTIONARY NOTES

1. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
2. The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
3. When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
4. The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
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6. When any special process such as potting is required for LED assembly, please consult with Kingbright representative before proceeding.
7. All design applications should refer to Kingbright application notes available at <https://www.KingbrightUSA.com/ApplicationNotes>