

DMNH6069SFVWQ

60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
	50mΩ @ V _{GS} = 10V	18A
60V	63mΩ @ V _{GS} = 4.5V	15A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Power-Management Functions
- DC-DC Converters

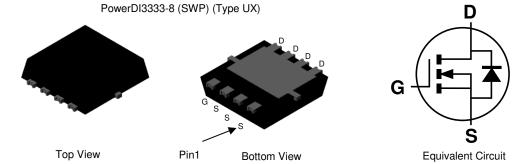
Features and Benefits

- Rated to +175°C—Ideal for High Ambient Temperature
 Environments
- 100% Unclamped Inductive Switch (UIS) Test in Production
- Low R_{DS(ON)}—Ensures Minimal On-State Losses
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies Just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- Wettable Flank for Improved Optical Inspections
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMNH6069SFVWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208()
- Weight: 0.072 grams (Approximate)



Ordering Information (Note 4)

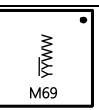
Part Number	Case	Packaging
DMNH6069SFVWQ-7	PowerDI3333-8 (SWP) (Type UX)	2,000/Tape & Reel
DMNH6069SFVWQ-13	PowerDI3333-8 (SWP) (Type UX)	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\frac{M69}{YY} = Product Type Marking Code$ $\frac{YY}{YY} WW = Date Code Marking$ $\frac{YY}{YY} = Last Two Digits of Year (ex: 21 = 2021)$ WW = Week Code (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	60	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current $V_{GS} = 10V$ (Note 5)	Steady State	T _A = +25°C T _A = +100°C	ID	5.0 3.5	А
Continuous Drain Current VGS = 10V (Note 6)	Steady State	Tc = +25°C T _C = +100°C	lo	18 12	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			Ідм	72	А
Maximum Continuous Body Diode Forward Current (Note 5)			ls	5.0	А
Pulsed Source Current (380µs Pulse, Duty Cycle = 1%)			lsм	72	А
Avalanche Current , L = 0.1mH			las	12	А
Repetitive Avalanche Energy, $L = 0.1 \text{mH}$			E _{AS}	7.2	mJ

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	3.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	50	°C/W
Total Power Dissipation (Note 6)		PD	38	W
Thermal Resistance, Junction to Case (Note 6)	Steady State	Rejc	3.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

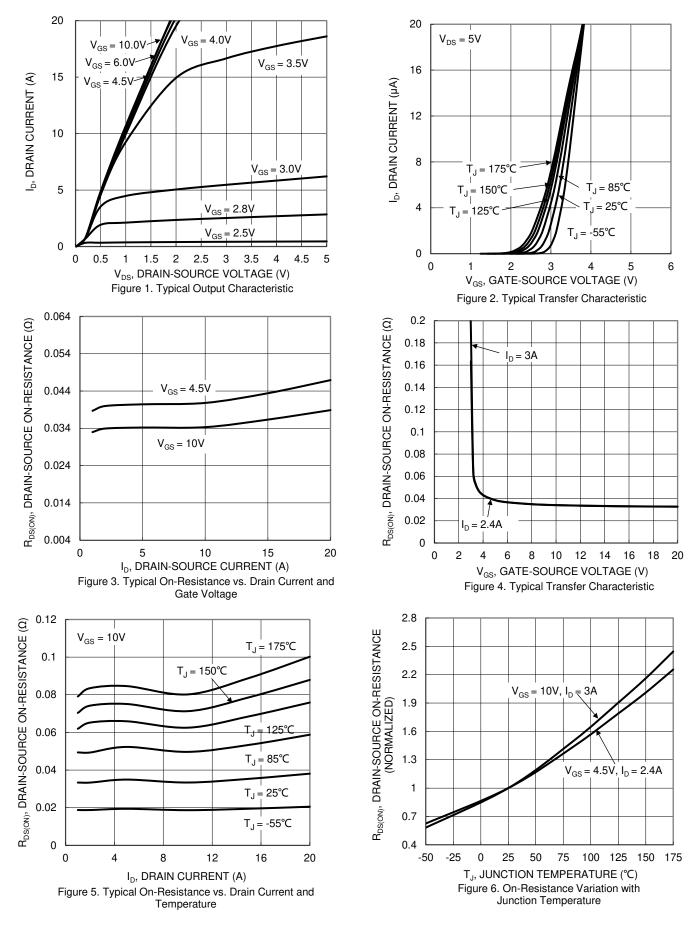
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)			71	-			
Drain-Source Breakdown Voltage	BV _{DSS}	60	—	_	V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	—	1	μA	$V_{DS} = 60V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS}=\pm 20V, \ V_{DS}=0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	VGS(TH)	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	Pro/on	_	35	50	mΩ	$V_{GS} = 10V, I_D = 3A$	
	RDS(ON)	_	41	63		$V_{GS} = 4.5V, I_D = 2.4A$	
Diode Forward Voltage	Vsd	_	0.8	1.1	V	$V_{GS} = 0V$, $I_S = 2.5A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		740		pF	− V _{DS} = 30V, V _{GS} = 0V, − f = 1.0MHz	
Output Capacitance	Coss		40	—	pF		
Reverse Transfer Capacitance	Crss		28	—	pF		
Gate Resistance	Rg	—	2.2	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	6.4	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	14	_	nC		
Gate-Source Charge	Qgs	_	2.8		nC	$V_{DS} = 30V, I_D = 12A$	
Gate-Drain Charge	Q _{gd}		2.3	_	nC		
Turn-On Delay Time	tD(ON)	_	3.6		ns		
Turn-On Rise Time	tR	_	5.0		ns		
Turn-Off Delay Time	td(OFF)		12	_	ns	$V_{GS} = 10V, R_G = 6.0\Omega$	
Turn-Off Fall Time	tF	_	3.3	_	ns		
Body Diode Reverse Recovery Time	trr	_	11		ns		
Body Diode Reverse Recovery Charge	QRR		5.1		nC	IF = 4.5A, di/dt = 100A/µs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:



DMNH6069SFVWQ





DMNH6069SFVWQ

 $I_D = 1mA$

100 125 150 175

 \mathbf{C}_{iss}

C_{oss}

 C_{rss}

50

 $P_w = 1 \mu s$

P_w= 10µs 60

 $I_{D} = 250 \mu A$

50

30

40

75

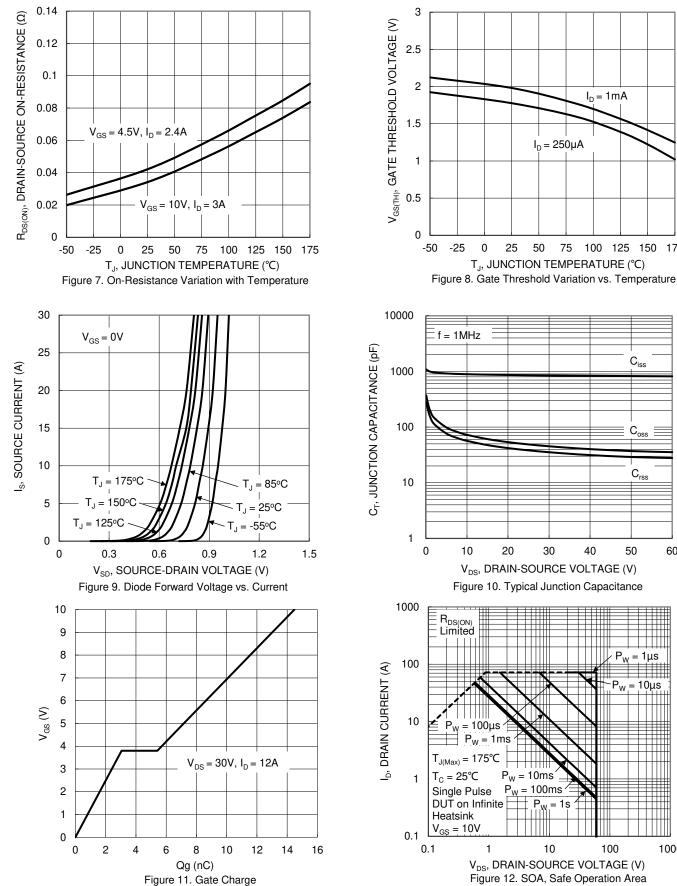


Figure 12. SOA, Safe Operation Area

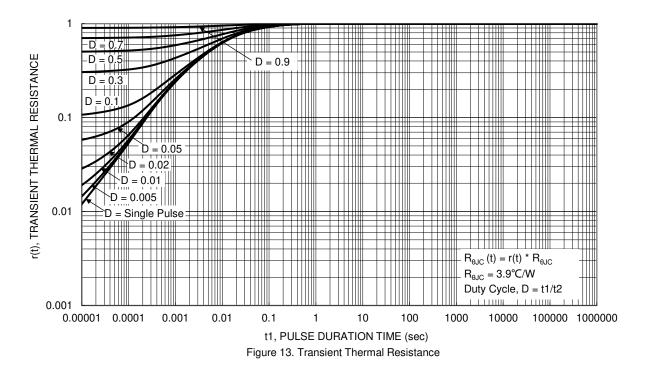
100

Р = 1s

10

1000



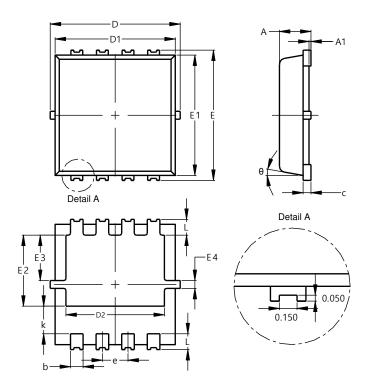




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (SWP) (Type UX)

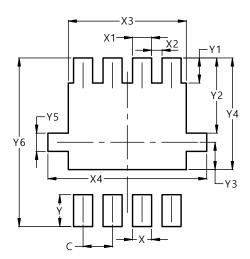


PowerDI3333-8 (SWP)						
(Type UX)						
Dim	Min	Тур				
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
С	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	_	0.65			
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All I	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (SWP) (Type UX)



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
X3	2.600
X4	3.500
Y	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700



IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

and 5 Diodes products are provided subject to Diodes' Standard Terms Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com