

TPA2010D1
Audio Power Amplifier
Evaluation Module

User's Guide

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Exceeding the specified input range may cause unexpected operation and/or irreversible damage to the EVM. If there are questions concerning the input range, please contact a TI field representative prior to connecting the input power.

Applying loads outside of the specified output range may result in unintended operation and/or possible permanent damage to the EVM. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative.

During normal operation, some circuit components may have case temperatures greater than 85°C. The EVM is designed to operate properly with certain components above 85°C as long as the input and output ranges are maintained. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors. These types of devices can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during operation, please be aware that these devices may be very warm to the touch.

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This book may contain cautions and warnings.

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A caution statement describes a situation that could potentially damage your software or equipment.

This is an example of a warning statement.

A warning statement describes a situation that could potentially cause harm to you.

The information in a caution or a warning is provided for your protection. Please read each caution and warning carefully.

Related Documentation From Texas Instruments

- ***TI Plug-N-Play Audio Amplifier Evaluation Platform*** (literature number SLOU011) provides detailed information on the evaluation platform and its use with TI audio evaluation modules.
- ***TPA2010D1 2.5-W Mono Filter-Free Class-D Audio Power Amplifier*** (literature number SLOS417) This is the data sheet for the TPA2010D1 audio amplifier integrated circuit.

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Introduction

This chapter provides an overview of the Texas Instruments (TI) TPA2010D1 NanoFree™ WCSP audio amplifier evaluation module (TPA2010D1YZF EVM). It includes a list of EVM features, a brief illustrated description of the module, and a list of EVM specifications.

The TPA2010D1 EVM is completely Pb-free. The device, all the components, and the printed-circuit board are all Pb-free. In addition, no lead is used in the manufacture or assembly of the module. Project collateral discussed in this user's guide can be downloaded from the following URL: <http://www.ti.com/lit/zip/SLOU166>.

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1.1 Feature Highlights

The TI TPA2010D1 audio amplifier evaluation module and the TI Plug-N-Play audio amplifier evaluation platform include the following features:

- TPA2010D1 Low-Voltage Audio Power Amplifier Evaluation Module
 - 1.45 mm x 1.45 mm NanoFree™ WCSP
 - Fully-differential filter-free Class-D amplifier
 - Single channel, bridge-tied load (BTL)
 - 2.5-V to 5.5-V operation
 - 2.5-W output power into 4 Ω at 5 V, BTL
 - Internal depop and quick start-up circuitry
 - Internal thermal and short-circuit protection
 - Module gain is set at 2 V/V
 - Completely Pb-free
- Quick and Easy Configuration With the TI Plug-N-Play Audio Amplifier Evaluation Platform
 - Evaluation module is designed to simply plug into the platform, automatically making all signal, control, and power connections.
 - Platform provides flexible power options.
 - Jumpers on the platform select power and module control options.
 - Switches on the platform route signals.
 - Platform provides quick and easy audio input and output connections.
- Platform Power Options
 - Onboard 9-V battery
 - External 5-V to 15-V (V_{CC}) supply inputs
 - External regulated V_{DD} supply input
 - Socket for onboard 5-V/3.3-V V_{DD} voltage regulator EVM
 - Onboard overvoltage and reverse polarity power protection
- Platform Audio Input and Output Connections
 - Left and right RCA phono jack inputs
 - Miniature stereo phone jack input
 - Left and right RCA phono jack outputs
 - Left and right compression speaker terminal outputs
 - Miniature stereo headphone jack output

1.2 Description

The TPA2010D1 audio power amplifier evaluation module is a complete, low-power single-channel audio power amplifier. It consists of the TI TPA2010D1 2.5-W low-voltage Class-D audio power amplifier IC in a very small NanoFree™ WCSP package, along with a small number of other parts mounted on a circuit board that is approximately one and a quarter inches square.

Single in-line header pins are mounted to the underside of the module circuit board. These pins allow the EVM to be plugged into the TI Plug-N-Play audio amplifier evaluation platform or to be wired directly into existing circuits and equipment when used stand-alone.

The platform, which has room for a pair of TPA2010D1 evaluation modules, is a convenient vehicle for demonstrating TI's audio power amplifier and related evaluation modules. The EVMs simply plug into the platform, which automatically provides power to the modules, interconnects them correctly, and connects them to a versatile array of standard audio input and output jacks and connectors. Easy-to-use configuration controls allow the platform and EVMs to quickly model many possible end-equipment configurations.

There is nothing to build, nothing to solder, and nothing but the speakers included with the platform to hook up.

1.3 TPA2010D1 EVM Specifications

Supply voltage range, V_{DD}	2.5 V to 5.5 V
Power supply current rating required	1 A
Continuous output power, P_O : 4- Ω BTL, $V_{DD} = 5$ V	2.5 W
Audio input voltage, V_I	0 V to V_{DD} , max
Minimum load impedance, Z_L	4 Ω



Operation

Follow the steps in this chapter to prepare the TPA2010D1 audio amplifier EVM for use. Using the TPA2010D1 EVM with the TI Plug-N-Play audio amplifier evaluation platform is a quick and easy way to connect power, signal and control inputs, and signal outputs to the EVM, using standard connectors. However, the audio amplifier evaluation module can be used stand-alone by making connections directly to the module pins, and it can be wired directly into existing circuits or equipment.

The platform switch and jumper settings shown in Table 2–1 are typical for the TPA2010D1 EVM. They cause the TPA2010D1 amplifier IC on the EVM to shut down when a plug is inserted into platform headphone jack J10.

Table 2–1. Typical TI Plug-N-Play Platform Jumper and Switch Settings for the TPA2010D1 EVM

EVM	JP6	JP7	JP8	S2	S3
TPA2010D1	Mute	X	Lo	See Note 2	X

Notes: 1) X = Don't care
 2) Set S2 to On when signal conditioning board is installed in U1; set S2 to Off when no signal conditioning board is installed.

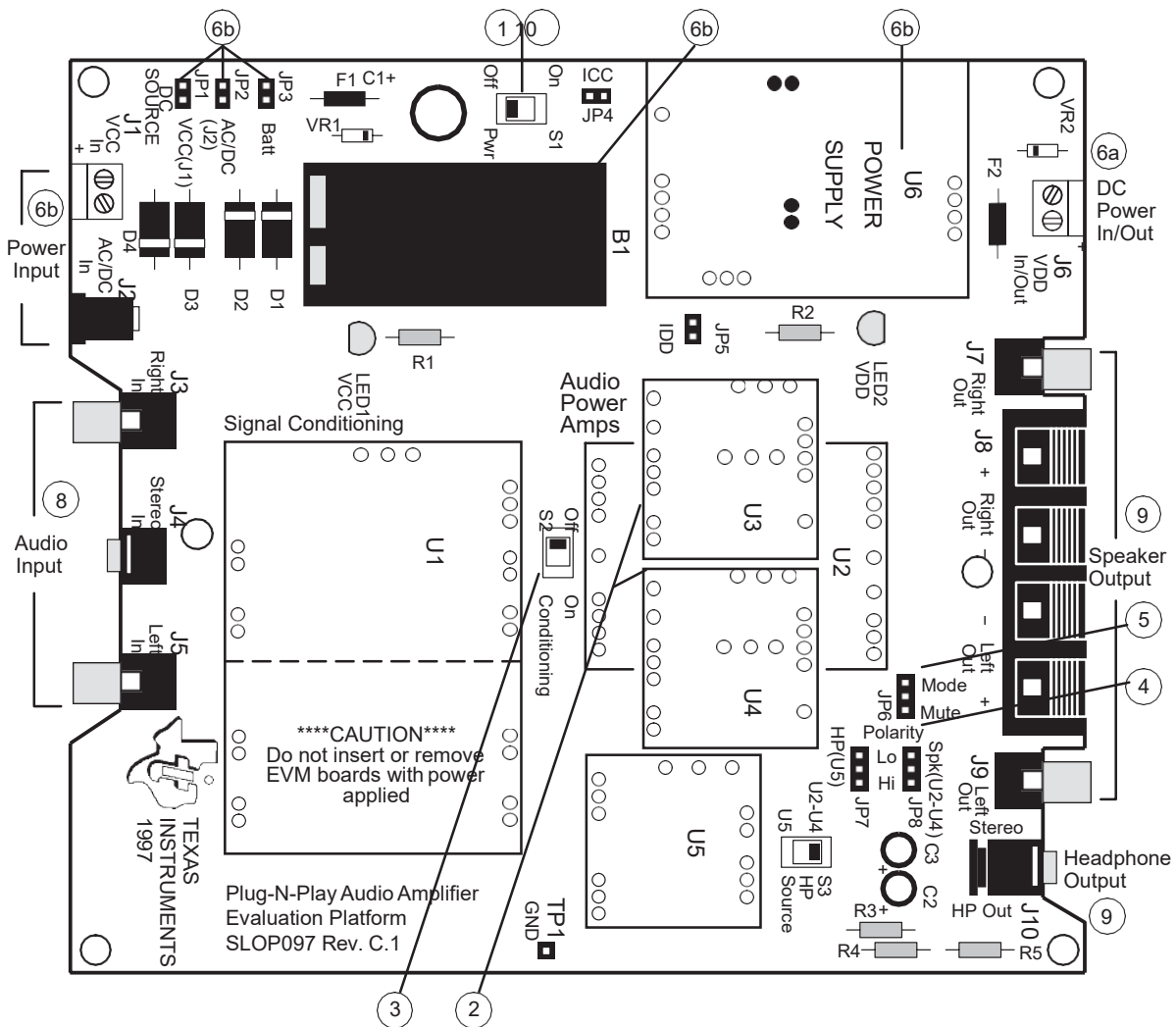
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2.1 Precautions

Power Supply Input Polarity and Maximum Voltage
 Always ensure that the polarity and voltage of the external power connected to V_{CC} power input connector J1, J2, and/or V_{DD} power input connector J6 are correct. Overvoltage or reverse-polarity power applied to these terminals can open onboard soldered-in fuses and cause other damage to the platform, installed evaluation modules, and/or the power source.

Inserting or Removing EVM Boards
 Do not insert or remove EVM boards with power applied—damage to the EVM board, the platform, or both may result.

Figure 2–1. Quick Start Platform Map



2.2 Quick Start List for Platform

Follow these steps when using the TPA2010D1 EVM with the TI Plug-N-Play audio amplifier evaluation platform (see the platform user's guide, SLOU011, for additional details).

□ Platform preparations

- 1) Ensure that all external power sources are set to OFF and that the platform power switch S1 is set to Off.
- 2) Install a TPA2010D1 module in platform sockets U3 and U4 for stereo operation (or a module in either U3 or U4 for single channel operation), taking care to align the module pins correctly.
- 3) Use switch S2 to select or bypass the signal conditioning EVM (U1).
- 4) Set control signal polarity jumper JP8 to Lo.
- 5) Set jumper JP6 to select the mute control input. Jumper, J1, on the TPA2010D1 EVM must be open to allow the TPA2010D1 to shutdown if a plug is inserted into platform headphone jack J10.

Table 2–2. Platform Jumper and Switch Settings for the TPA2010D1 EVM

EVM	JP6	JP7	JP8	S2	S3
TPA2010D1	Mute	X	Lo	See Note 2	X

Notes: 1) X = Don't care

2) Set S2 to On when signal conditioning board is installed in U1; set S2 to Off when no signal conditioning board is installed.

□ Power supply

- 6) Select and connect the power supply:
 - a) Connect an external regulated power supply set to a voltage between 2.5 V and 5.5 V to platform V_{DD} power input connector J6, taking care to observe marked polarity,
 - or
 - b) Install a voltage regulator EVM (SLVP097 or equivalent.) in platform socket U6. Install a 9-V battery in B1 or connect a 7 V–12 V power source to a platform V_{CC} power input J1 or J2 and jumper the appropriate power input (see platform user's guide).

□ Inputs and outputs

- 7) Ensure that signal source level is set to minimum.
- 8) Connect the audio source to left (J5) or right (J3) RCA phono jacks, or both if using two TPA2010D1 modules, or to stereo miniature phone jack (J4).
- 9) Connect 4- Ω to 32- Ω speakers to left (J9) or right (J7) RCA jacks, or both if using two TPA2010D1 modules, or to stripped wire connector (J8).

□ Power Up

- 10) Verify correct voltage and input polarity and set the external power supply to ON. If V_{CC} and an on-board regulator EVM are used to provide V_{DD} , set platform power switch S1 to On.

Platform LED2 lights, indicating the presence of V_{DD} , and the evaluation modules installed on the platform begin operation.

- 11) Adjust the signal source level as needed.

2.3 Quick Start List for Stand-Alone

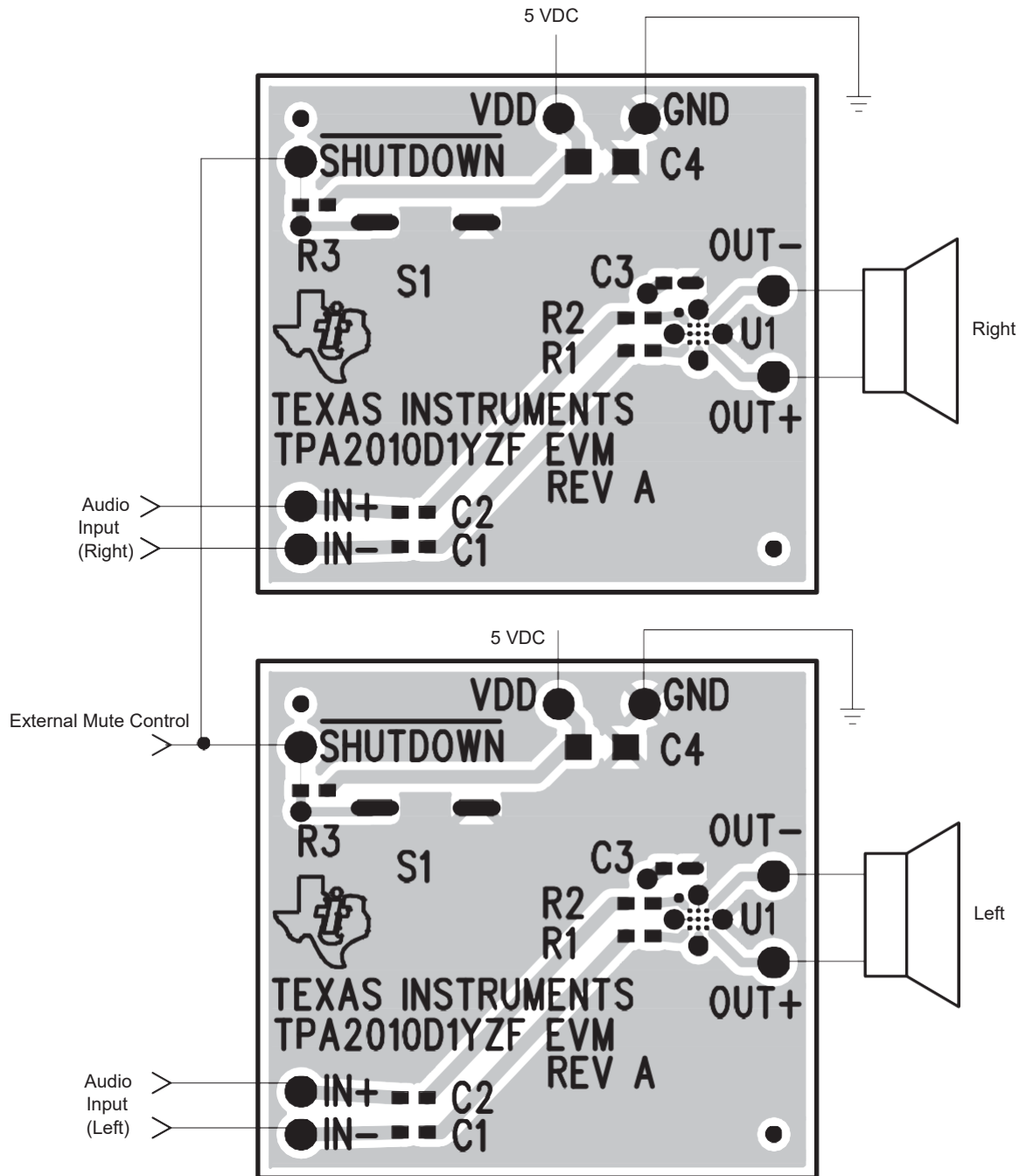
Follow these steps to use the TPA2010D1 EVM stand-alone or when connecting it into existing circuits or equipment. Connections to the TPA2010D1 module header pins can be made via individual sockets, wire-wrapping, or soldering to the pins, either on the top or the bottom of the module circuit board.

- Power supply**
 - 1) Ensure that all external power sources are set to OFF.
 - 2) Connect an external regulated power supply set to 5 V to the module V_{DD} and GND pins, taking care to observe marked polarity.
- Inputs and outputs**
 - 3) Ensure that the signal source level is set to minimum.
 - 4) Connect the positive lead from the audio source to the module IN+ pin and the negative lead to the IN- pin.
 - 5) $\overline{\text{SHUTDOWN}}$ can be enabled by either depressing switch S1, or by driving the $\overline{\text{SHUTDOWN}}$ EVM pin with TTL level logic.
 - 6) Connect a 4- Ω to 32- Ω speaker to the module OUT+ and OUT- pins.
- Power Up**
 - 7) Verify correct voltage and input polarity and set the external power supply to ON.
The EVM begins operation.
 - 8) Adjust the signal source level as needed.

2.4 References

2.4.1 TPA2010D1 EVM Connected for Stereo BTL Output

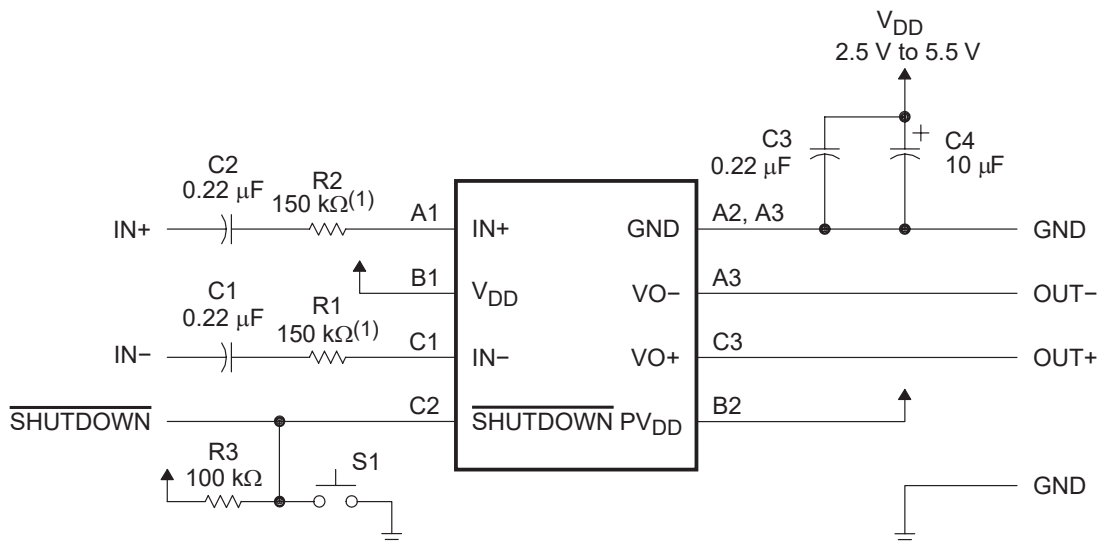
Figure 2–2. TPA2010D1 EVM Connected for Stereo BTL Output



Note: Due to the very small size of the NanoFree™ WCSP IC package, the standard part number TPA2010D1 is replaced with the code AKO (Pb-free version).

2.4.2 TPA2010D1 EVM Schematic Diagram

Figure 2–3. TPA2010D1 EVM Schematic Diagram



(1) Values shown are for EVM revision A only. For non-rev A EVMs, $R1 = R2 = 60.4 \text{ k}\Omega$, $A_V = 5 \text{ V/V}$.

2.4.3 TPA2010D1 Audio Power Amplifier Evaluation Module Parts List

Table 2–3. TPA2010D1 EVM Parts List

Qty.	Reference	Description	SMD Size	Manufacturer/ Part Number	Digi-Key Number
3	C1, C2, C3	Capacitor, 0.22 μF , 80%/-20%, nonpolarized	0603	Panasonic ECJ-1VF1C224Z	Digi-Key ⁽²⁾ PCC1790TR-ND
1	C4	Capacitor, 10 μF , 6.3 V	A	Panasonic ECS-TOJY106R	Digi-Key PCS1106CT-ND
2	R1, R2(1)	Resistor, 150 $\text{k}\Omega$, 1/16 W, 1%	0603	Panasonic ERJ-3EKF6042V	Digi-Key ⁽²⁾ P60.4KHTR-ND
1	R3	Resistor, 100 $\text{k}\Omega$, 1/16 W, 10%	0603	Panasonic ERJ-3EKF1003V	Digi-Key ⁽²⁾ P100KHTR-ND
1	S1	Momentary switch		Panasonic P8048SCT-ND	Digi-Key P8048SCT-ND
7		Terminal post headers		Sullins PTC36SABN or SAMTEC TSW-19-8-G-S	Digi-Key S1022-36-ND
1	U1	IC, TPA2010D1, Mono Class-D audio amplifier	1.45 mm x 1.45 mm WCSP package	TI TPA2010D1YZF	
1	PCB1	Printed-circuit board, TPA2010D1 EVM			

(1) Values shown are for EVM revision A only. For non-rev A EVMs, $R1 = R2 = 60.4 \text{ k}\Omega$, $A_V = 5 \text{ V/V}$.

(2) Call Digi-Key for Pb-free information.

Note: Due to the very small size of the NanoFree™ WCSP IC package, the standard part number TPA2010D1 is replaced with the code AKO (Pb-free version).

Note: All items are SMD except terminal posts. All components are Pb free.

2.4.4 TPA2010D1 EVM PCB Layers

The following illustrations depict the TPA2010D1 EVM PCB layers and silk-screen. These drawings are not to scale. Gerber plots can be obtained from any TI sales office.

Figure 2-4. TPA2010D1 EVM Top Layer

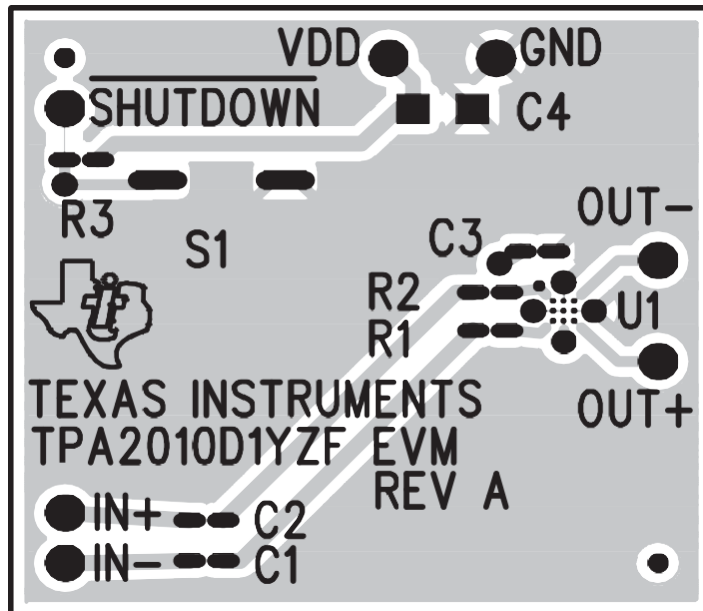
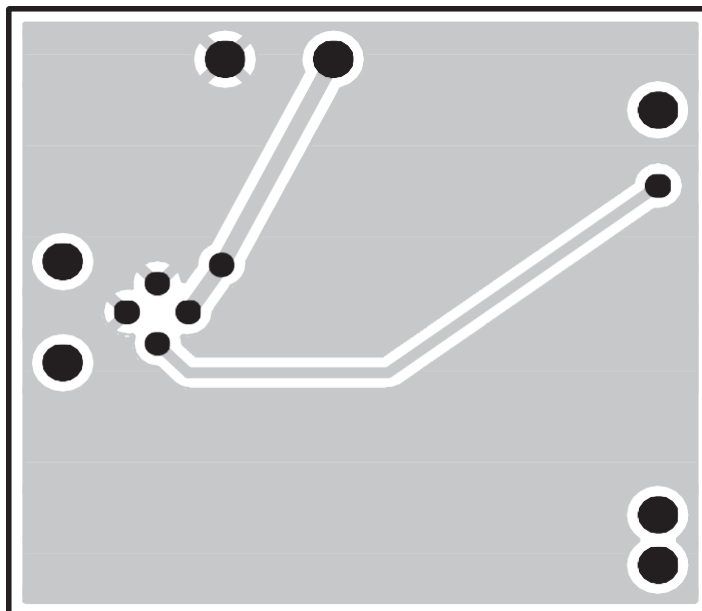


Figure 2-5. TPA2010D1 EVM Bottom Layer





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