Ordering number : ENA0775A

Discontinued

SANYO Semiconductors
DATA SHEET

N-Channel Silicon MOSFET

2SK4097LS — General-Purpose Switching Device Applications

Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- Adoption of high reliability HVP process.
- Attachment workability is good by Mica-less package.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		500	V
Gate-to-Source Voltage	V_{GSS}		±30	V
Drain Current (DC)	I_{Dc}*1	Limited only by maximum temperature	9.5	A
	I_{Dpack}*2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	8.3	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	38	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C (SANYO's ideal heat dissipation condition)*3	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C
Avalanche Energy (Single Pulse) *4	E_{AS}		280	mJ
Avalanche Current *5	I_{AV}		9.5	A

*1 Shows chip capability

*2 Package limited

*3 SANYO's condition is radiation from backside.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

*4 V_{DD}=99V, L=5mH, I_{AV}=9.5A

*5 L≤5mH, single pulse

Marking : K4097

Discontinued

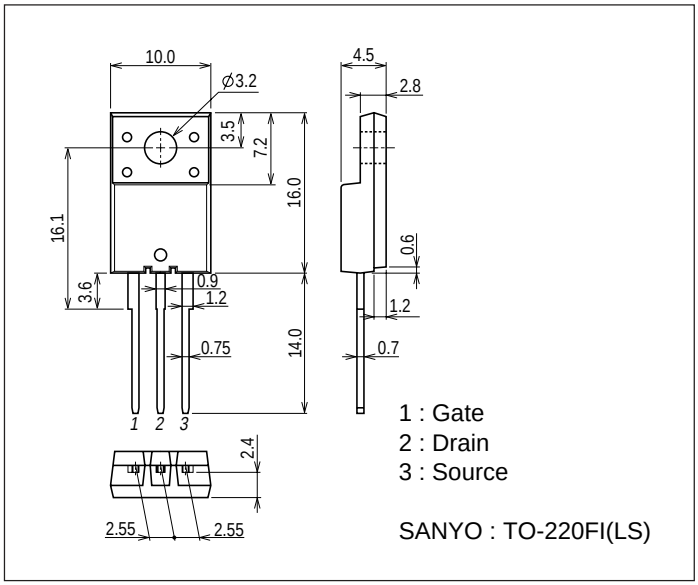
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=10mA, V_{GS}=0V	500			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=400V, V_{GS}=0V			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±30V, V_{DS}=0V			±100	nA
Cutoff Voltage	V_{GS}(off)	V_{DS}=10V, I_D=1mA	3		5	V
Forward Transfer Admittance	|yfs|	V_{DS}=10V, I_D=5A	3	6		S
Static Drain-to-Source On-State Resistance	R_{DS}(on)	I_D=5A, V_{GS}=10V		0.5	0.65	Ω
Input Capacitance	Ciss	V_{DS}=30V, f=1MHz		750		pF
Output Capacitance	Coss	V_{DS}=30V, f=1MHz		150		pF
Reverse Transfer Capacitance	Crss	V_{DS}=30V, f=1MHz		35		pF
Turn-ON Delay Time	t_d(on)	See specified Test Circuit.		16		ns
Rise Time	t_r	See specified Test Circuit.		44		ns
Turn-OFF Delay Time	t_d(off)	See specified Test Circuit.		102		ns
Fall Time	t_f	See specified Test Circuit.		47		ns
Total Gate Charge	Qg	V_{DS}=200V, V_{GS}=10V, I_D=9.5A		30		nC
Gate-to-Source Charge	Qgs	V_{DS}=200V, V_{GS}=10V, I_D=9.5A		5.2		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=200V, V_{GS}=10V, I_D=9.5A		17		nC
Diode Forward Voltage	V_{SD}	I_S=9.5A, V_{GS}=0V		0.9	1.2	V

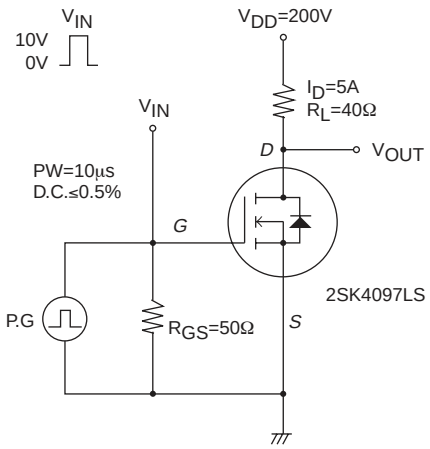
Package Dimensions

unit : mm (typ)

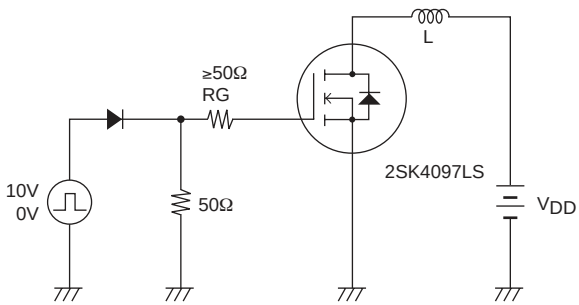
7509-002



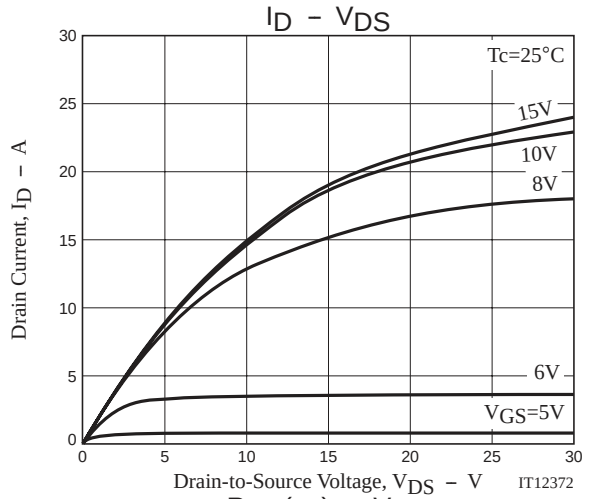
Switching Time Test Circuit

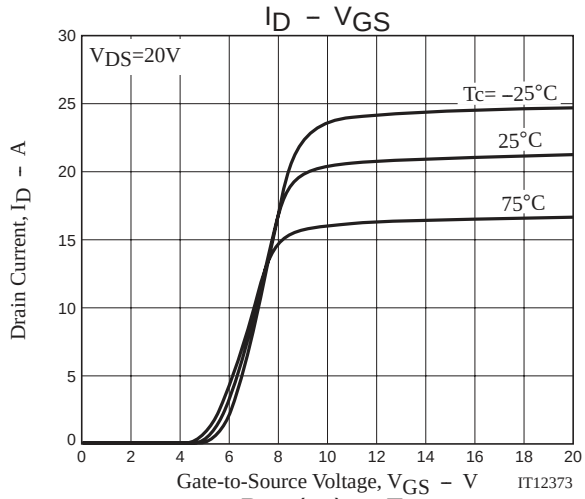


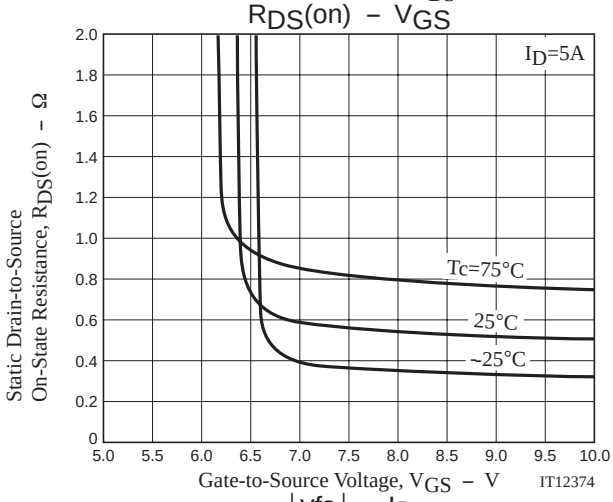
Avalanche Resistance Test Circuit

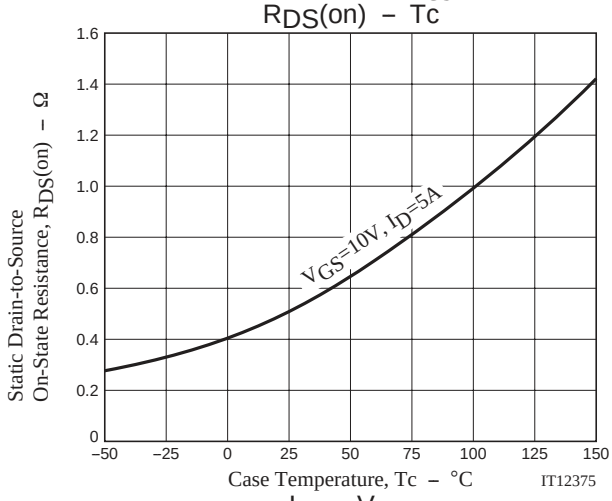


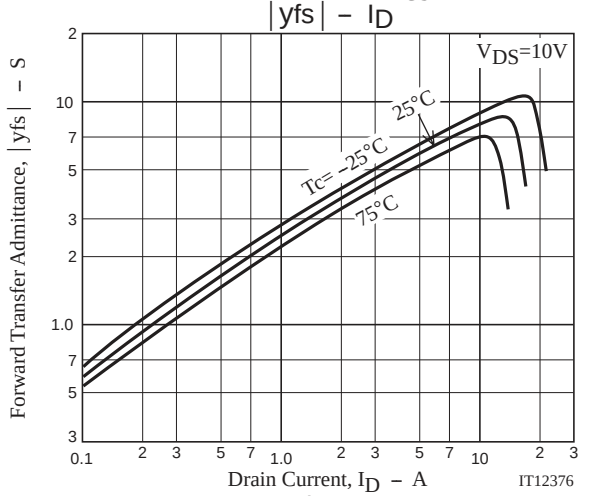
Discontinued

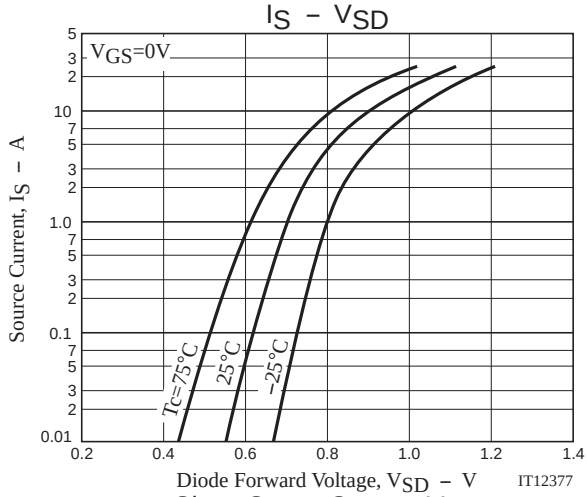


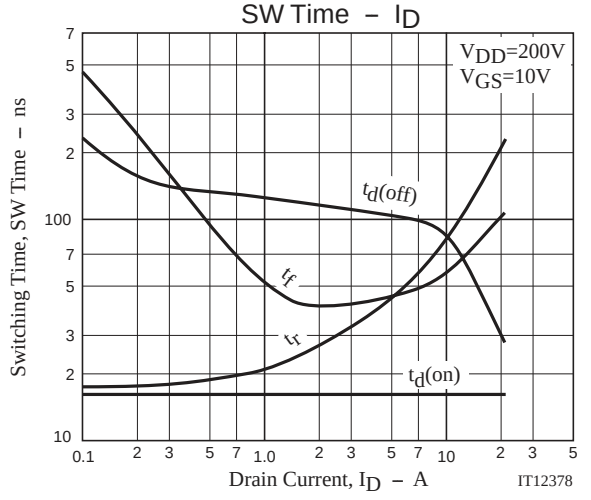


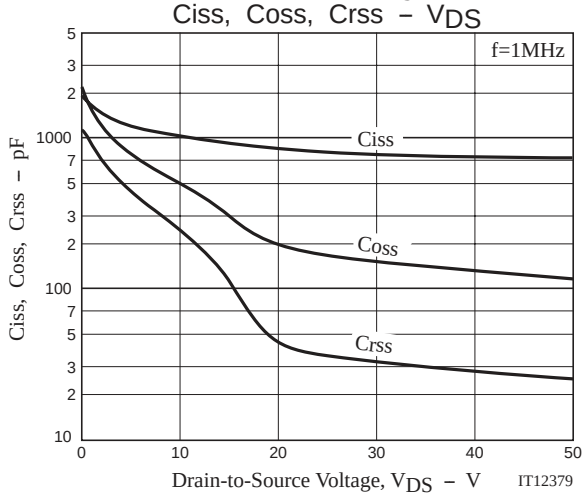


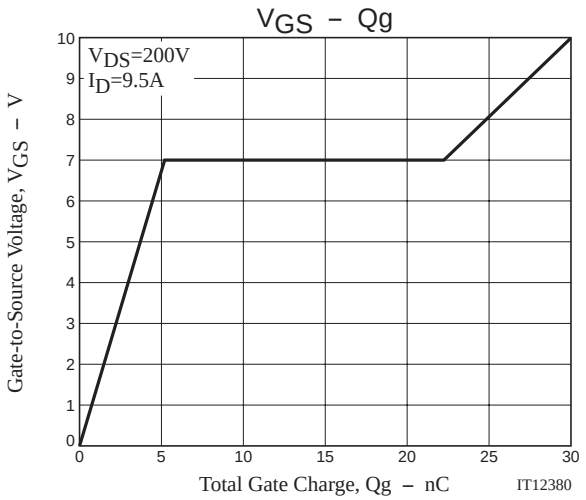


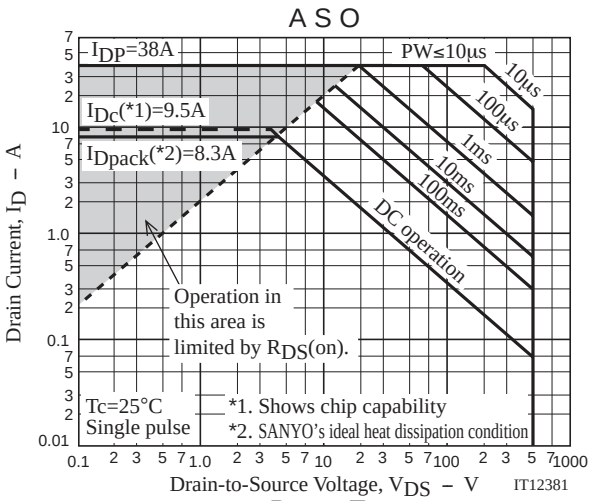


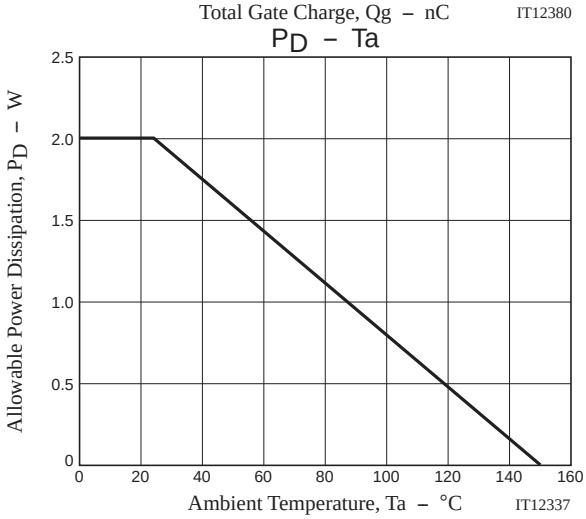


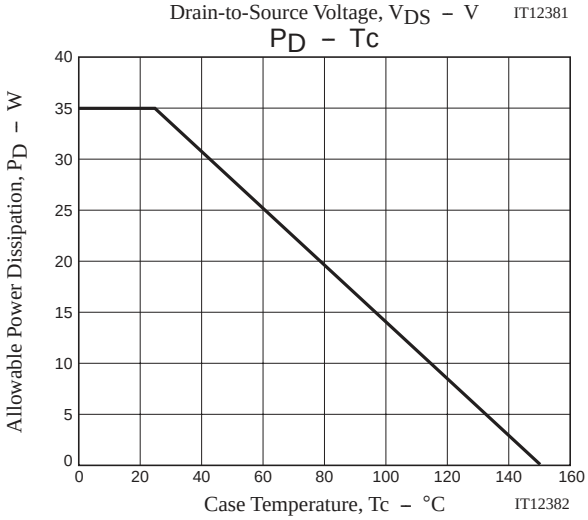


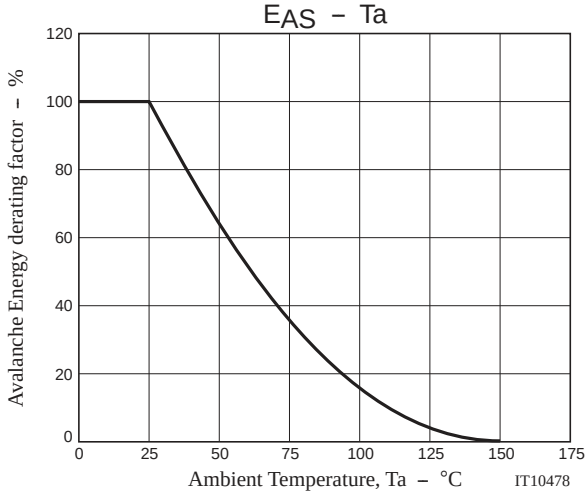



VGS – Qg
VDS=200V
ID=9.5A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT12380


A S O
IDP=38A
PW≤10μs
10μs
100μs
1ms
10ms
100ms
DC operation
IDC(*1)=9.5A
IDpack(*2)=8.3A
Operation in this area is limited by RDS(on).
Tc=25°C
Single pulse
*1. Shows chip capability
*2. SANYO's ideal heat dissipation condition
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT12381


PD – Ta
Allowable Power Dissipation, PD – W
Ambient Temperature, Ta – °C
IT12337


PD – Tc
Allowable Power Dissipation, PD – W
Case Temperature, Tc – °C
IT12382


EAS – Ta
Avalanche Energy derating factor – %
Ambient Temperature, Ta – °C
IT10478

Note on usage : Since the 2SK4097LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

This catalog provides information as of October, 2007. Specifications and information herein are subject to change without notice.