

#### **Features**

- RoHS compliant\*
- Convex terminal style
- 2/4 isolated elements available
- Resistance tolerance: 5 %
- Sulfur-resistant design
- Resistance range: 3  $\Omega$  to 1 M $\Omega$  and zero jumper
- AEC-Q200 compliant

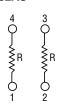
# **CAY10A-AS Series – Sulfur-resistant Thick Film Chip Arrays**

#### **Electrical Characteristics**

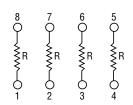
Characteristic	Model No.				
Characteristic	CAY10A-xxxJ2AS	CAY10A-xxxJ4AS			
Number of Elements (Isolated)	2	4			
Power Rating @ 70 °C per Resistor	63 mW				
Resistor Tolerance	5 %				
Resistor Range & TCR (E24 for 5 %) plus zero ohm jumper	5 %, 10 ~ 1 MΩ 250 ppm/°C 5 %, 3 ~ 9, 1 Ω 500 ppm/°C				
Maximum Overload Voltage	50 V				
Maximum Working Voltage	25 V				
Operating Temperature Range	-55 to -	+125 °C			
Rating Temperature	+70	) °C			
Packaging	10,000 pied	ces per reel			
Zero Ohm Jumper Current Rating / Max. Resistance (per element)	1 A / 2.5 A /	50 mΩ max.			

### **Isolated Circuit**

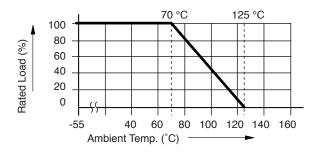
#### CAY10A-xxxJ2AS



#### CAY10A-xxxJ4AS



#### **Derating Curve**



#### **Additional Information**

Click these links for more information:



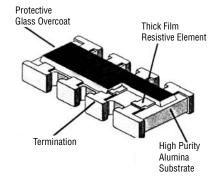








Construction



#### **Typical Part Marking**



#### CAY10A-x2AS No part marking



#### CAY10A-x4AS ±5 % (E24)

3 digits; first two digits are significant, third digit is the number of zeroes to

EX:  $472 = 4700 \Omega = 4.7 \text{K} \Omega$ 

#### **Storage Conditions**

5~35 °C, 40~75 % RH, 2 years

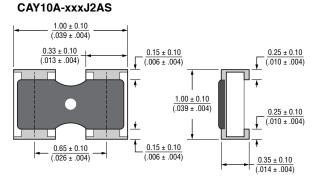


**WARNING Cancer and Reproductive Harm** 

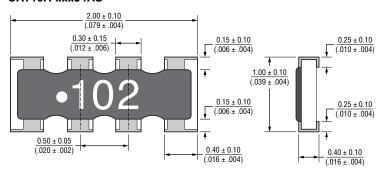
www.P65Warnings.ca.gov

# CAY10A-AS Series – Sulfur-resistant Thick Film Chip Arrays **FOURNS**

#### **Product Dimensions**

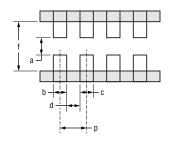


#### CAY10A-xxxJ4AS



DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

#### **Recommended Pad Layout**



Model	а	b	С	d	р	f	
CAY10A-xxxJ2AS	0.5 (.020)	$\frac{0.35 \sim 0.40}{(.014 \sim .016)}$	$\frac{0.35 \sim 0.40}{(.014 \sim .016)}$		<u>0.65</u> (.026)	$\frac{1.4 \sim 1.5}{(.055 \sim .059)}$	
CAY10A-xxxJ4AS	<u>0.4</u> (.016)	0.525 (.021)	<u>0.25</u> (.010)	<u>0.25</u> (.010)		1.4 (.055)	

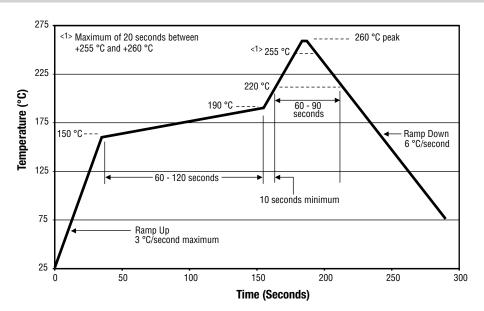
DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

#### How to Order

	CA	Y 10	Α -	103	J 4	I AS
Series —						
CA = Chip Array						
Type —						
Y = Convex						
Model						
10 = 04 Package Width						
Feature —						
A = AEC-Q200 Compliant						
Resistance Code —						
For 5 % Tolerance: (E24)						
First two digits are significant, third digit represents the number of zeroes to follow (example: 103 = 1M $\Omega$ ).						
Resistance Tolerance						
J = ±5 %						
Number of Resistors						
2 = 2 Resistors						
4 = 4 Resistors						
Special Characteristics —						

AS = Sulfur-resistant, Tin-plated Terminations (RoHS Compliant)

#### **Soldering Profile**



# **CAY10A-AS Series – Sulfur-resistant Thick Film Chip Arrays**

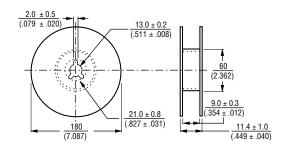
#### **Performance Characteristics (AEC-Q200)**

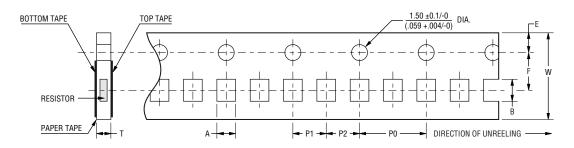
Test	Procedure	Test Limits			
Short Time Overload	2.5 X rated voltage for 5 sec.	$\pm$ (2.0 % + 0.1 Ω ) 0 Ω : 50 mΩ or less			
High Temperature Exposure (Storage)	1000 hrs. @ T=125 °C. Unpowered.  Measurement at 24 ±2 hours after test conclusion.	5 %: ± (2.0 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
Temperature Cycling	1000 Cycles (-55 °C to +125 °C) Measurement at 24 ±4 hours after test conclusion. 30 min. maximum dwell time at each temperature extreme. 1 min. maximum transition time.	$\pm$ (2.0 % + 0.1 Ω ) 0 Ω : 50 mΩ or less			
Moisture Resistance	T=24 hours / Cycle,10 Cycles. Notes: Steps 7a & 7b not required. Unpowered.	$\pm$ (2.0 % + 0.1 Ω ) 0 Ω : 50 mΩ or less			
Biased Humidity	1000 hours 85 °C / 85 % RH. Note: Specified conditions: 10 % of operating power (not exceeding max. working voltage). Measurement at 24 ±2 hours after test conclusion.				
Operational Life	1000 hours Ta=125 °C at 35 % rated power.  Measurement at 24 ±4 hours after test conclusion.	$\pm$ (3 % + 0.1 Ω) 0 Ω: 100 mΩ or less			
Mechanical Shock	Wave Form: Tolerance for half sine shock pulse. Peak value is 100 g's. Normal duration (D) is 6 ms.	$\pm$ (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
Vibration	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Test from 10-2000 Hz.	$\pm$ (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
Resistance to Soldering Heat	Condition B: Immerse the specimens in an eutectic solder at 260 ±5 °C for 10 ±1 s.	$\pm$ (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
Thermal Shock	-55 °C / +155 °C. Note: Number of cycles required: 300, Maximum transfer time: 20 seconds, dwell time: 15 minutes. Air to Air.	$\pm$ (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
ESD	Verify the voltage setting at 500 V	± (2 % + 0.1 Ω)			
Solderability	Method B, aging 4 hours at 155 °C dry heat Lead-free solder bath at 235 ±3 °C Dipping time: 3 ±0.5 seconds				
Flammability	V-0 or V-1 are acceptable. Electrical test not required.	V-0 or V-1			
Board Flex (Bending)	The duration of the applied forces shall be 60 (+ 5) sec.	$\pm$ (1 % + 0.1 Ω) 0 Ω: 50 mΩ or less			
Terminal Strength (SMD)	Force of 1.8 kg for 60 seconds.	$\pm$ (1 % + 0.05 Ω) 0 Ω: 50 mΩ or less			
Sulfuration Test	Sulfur (Sulfur Vapor) 1000 hours, 105 ±2 °C, unpowered	1 %: $\pm$ (1.0 % + 0.05 $\Omega$ ) 5 %: $\pm$ (2.0 % + 0.05 $\Omega$ ) 0 $\Omega$ : 100 m $\Omega$ or less			

## **CAY10A-AS Series – Sulfur-resistant Thick Film Chip Arrays**

### BOURNS®

#### **Packaging Dimensions**





Model	А	В	w	F	E	P1	P2	P0	Т
CAY10A-xxxJ2AS	1.2 ± 0.15	$\frac{1.2 \pm 0.1}{(.047 \pm .004)}$	8.0 ± 0.2	3.50 ± 0.05	1.75 ± 0.1	2.0 ± 0.1	2.0 ± 0.05	4.0 ± 0.1	$\frac{0.45 \pm 0.1}{(.018 \pm .004)}$
CAY10A-xxxJ4AS	(.047 ± .006)	$\frac{2.2 \pm 0.2}{(.087 \pm .008)}$	(.315 ± .008)	(.138 ± .002)	(.069 ± .004)	(.079 ± .004)	(.079 ± .002)	(.157 ± .004)	$\frac{0.64 \pm 0.1}{(.025 \pm .004)}$

DIMENSIONS:  $\frac{MM}{(INCHES)}$ 

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