# SF11G – SF18G

Taiwan Semiconductor

# 1A, 50V - 600V Super Fast Rectifier

# FEATURES

- AEC-Q101 qualified available
- Super Fast, low V<sub>F</sub>
- High current capability
- High reliability
- Low power loss
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

# APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

# **MECHANICAL DATA**

- Case: DO-204AL (DO-41)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.350g (approximately)

#### **KEY PARAMETERS** PARAMETER VALUE UNIT $I_{F}$ 1 А 50 - 600 ٧ $V_{RRM}$ 30 А I<sub>FSM</sub> °С $T_{JMAX}$ 150 Package DO-204AL (DO-41) Configuration Single die







ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	SF	UNIT							
PARAMETER	STMBOL	11G	12G	13G	14G	15G	16G	17G	18G	
Marking code on the device		SF 11G	SF 12G	SF 13G	SF 14G	SF 15G	SF 16G	SF 17G	SF 18G	
Repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	35	70	105	140	210	280	350	420	V
Forward current	I <sub>F</sub>					1				Α
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I <sub>FSM</sub>				3	0				А
Junction temperature	TJ				-55 to	+150				°C
Storage temperature	T <sub>STG</sub>				-55 to	+150				°C





THERMAL PERFORMANCE			
PARAMETER	SYMBOL	ТҮР	UNIT
Junction-to-lead thermal resistance	$R_{\Theta JL}$	20	°C/W
Junction-to-ambient thermal resistance	R <sub>eJA</sub>	80	°C/W

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward up the so (1)	SF11G SF12G SF13G SF14G	− I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C –	V <sub>F</sub>	-	0.95	V
Forward voltage <sup>(1)</sup>	SF15G SF16G			-	1.30	V
	SF17G SF18G			-	1.70	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>		$T_J = 25^{\circ}C$	1	-	5	μA
		T <sub>J</sub> = 125°C	I <sub>R</sub>	-	100	μA
SF11G SF12G SF13G SF14G		1MHz, V <sub>R</sub> = 4.0V	6	20	-	pF
Junction capacitance	SF15G SF16G SF17G SF18G	$10002, V_{\rm R} = 4.0V$	CJ	10	-	pF
Reverse recovery time		$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t <sub>rr</sub>	-	35	ns

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE <sup>(1)(2)</sup>	PACKAGE	PACKING		
SF1xG	DO-204AL (DO-41)	5,000 / Tape & Reel		
SF1xG A0G	DO-204AL (DO-41)	3,000 / Ammo box		
SF1xGH	DO-204AL (DO-41)	5,000 / Tape & Reel		
SF1xGHA0G	DO-204AL (DO-41)	3,000 / Ammo box		

#### Notes:

1. "x" defines voltage from 50V (SF11G) to 600V (SF18G)

2. "H" means AEC-Q101 qualified



# **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

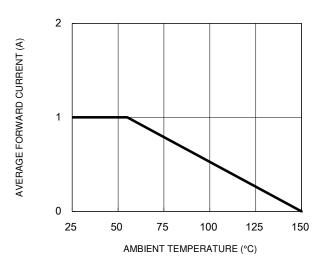
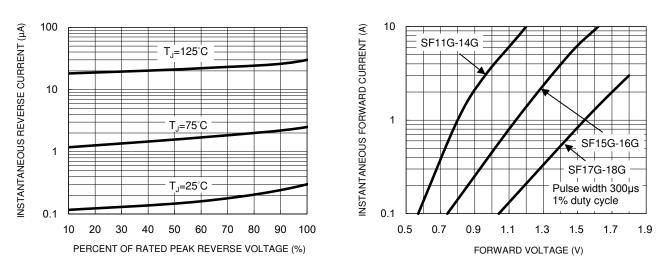


Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**



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### Fig.5 Maximum Non-Repetitive Forward Surge Current

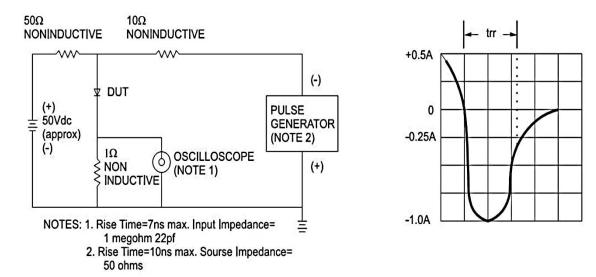
#### **Fig.2 Typical Junction Capacitance**

**Fig.4 Typical Forward Characteristics** 



# **CHARACTERISTICS CURVES**

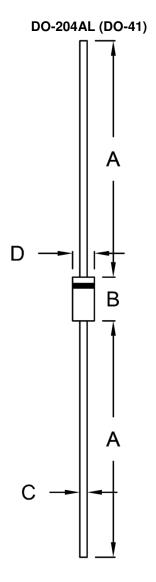
 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 



### Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



# **PACKAGE OUTLINE DIMENSIONS**



DIM.	Unit	(mm)	Unit (inch)		
	Min.	Max.	Min.	Max.	
А	25.40	-	1.000	-	
В	4.20	5.20	0.165	0.205	
С	0.71	0.86	0.028	0.034	
D	2.00	2.70	0.079	0.106	

### **MARKING DIAGRAM**



P/N	= Marking Code
G	= Green Compound
YWW	= Date Code
F	= Factory Code



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