

General Description

The MAX4004/MAX4006 precision, high-side, current monitors are specifically designed for monitoring photodiode current in fiber applications. They offer a connection point for the reference current and a monitor output that produces a signal proportional to the reference current. The monitor output of the MAX4004 is a current proportional to the reference current. The monitor output of the MAX4006 is a voltage proportional to the reference current. The current monitors have six decades of dynamic range and monitor reference current of 250nA to 2.5mA with better than 5% accuracy. The photodiode current can be monitored from 10nA to 10mA with reduced accuracy.

The MAX4004/MAX4006 accept a +2.7V to +22V supply voltage, suitable for PIN photodiode applications. (For higher voltage applications, refer to the MAX4007/MAX4008 data sheet.) Internal current limiting (20mA, typ) protects the device against short-circuit-to-ground or excessive current conditions. A clamp diode protects the monitor output from overvoltage. Additionally, these devices feature thermal shutdown if the die temperature reaches +150°C.

The MAX4004/MAX4006 are available in tiny, space-saving 6-pin SOT23 packages, and operate over the extended temperature range of -40°C to +85°C.

_Applications

Photodiode Current-Monitoring Systems

Portable Instrumentation

Medical Instrumentation

Laboratory Instrumentation

Consumer Electronics

Current-to-Voltage Conversion

Level Translation

Selector Guide

PART	PIN-PACKAGE	INTERNAL RESISTOR	TYPICAL ACCURACY	
MAX4004EUT-T	6 SOT23-6	None	5%	
MAX4006EUT-T	6 SOT23-6	10kΩ	1%	

Typical Operating Circuit appears at end of data sheet.

Features

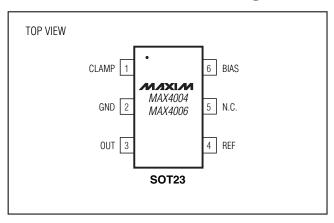
- ♦ Wide Reference Current Dynamic Range Guaranteed 250nA to 2.5mA with 5% Accuracy Extended 10nA to 10mA with 10% Monitor Accuracy
- ♦ Current (MAX4004) or Voltage (MAX4006) Monitor Output
- ♦ Reference Current-Limit Protection (20mA, typ)
- Voltage Clamp Protects Subsequent Output Circuitry
- ♦ +2.7V to +22V Wide Voltage Range Operation
- ♦ 6-Pin SOT23 Packages

Ordering Information

PART	PIN-PACKAGE	TOP MARK	PKG CODE	
MAX4004EUT-T	6 SOT23-6	ABNL	U6S-3	
MAX4006EUT-T	6 SOT23-6	ABNN	U6S-3	

Note: All devices are specified over the -40°C to +85°C operating temperature range.

Pin Configuration



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ABSOLUTE MAXIMUM RATINGS

CLAMP to GND	0.3V to +25V
BIAS, REF to GND	0.3V to +25V
OUT to GND	$0.3V$ to $(V_{CLAMP} + 0.6V)$
Short-Circuit, REF to GND	Continuous
Current into any Pin	±30mA
Continuous Power Dissipation (T _A = +	
6-Pin SOT23 (derate 8.7mW/°C abo	ove +70°C)696mW

Operating Temperature Range	e40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering,	10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{BIAS} = 5V, GND = 0V, REF = Open, V_{OUT} = 0V (MAX4004), CLAMP = Open, T_A = -40^{\circ}C to +85^{\circ}C.$ Typical values are at $T_A = +25^{\circ}C$, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS		
Bias Voltage Range	V _{BIAS}	Inferred from power- rejection test	2.7		22	V		
Di Ordinarat Ordinarat		I _{REF} = 250nA			40	100	μA	
Bias Quiescent Current	I _{BIAS}	I _{REF} = 2.5mA			3.6	4	mA	
Reference Voltage	V _{REF}	I _{REF} = 2.5mA		V _{BIAS} - 1.1	V _{BIAS} - 0.8		V	
Thermal Shutdown Engage Temperature	T _{SHDN}	REF shorted to GND temperature rising	REF shorted to GND, junction temperature rising		150		°C	
Trip-Point Temperature Hysteresis	T _{HYS}	REF shorted to GND, junction temperature falling			5		°C	
Input Current Limit	ILIM	V _{REF} = V _{BIAS} - 5V	V _{REF} = V _{BIAS} - 5V		20		mA	
		f = 0.1Hz to 10Hz	I _{REF} = 250nA		0.3			
MAX4004			$I_{REF} = 2.5 \text{mA}$		63		pARMS	
Output Current Noise	inout	f 0.411-+-40141-	I _{REF} = 250nA		9.2			
		f = 0.1Hz to $10kHz$	I _{REF} = 2.5mA		1240			
MAX4006 Output Voltage Noise	0110117	f = 0.1Hz to $10Hzf = 0.1Hz$ to $10kHz$	I _{REF} = 250nA		0.041			
			$I_{REF} = 2.5 \text{mA}$		0.63		11//20140	
	eNOU1		$I_{REF} = 250 nA$		1.3		μV _{RMS}	
		T = 0. THZ to TOKHZ	$I_{REF} = 2.5 \text{mA}$		12.5			
MAX	MAX4004	$I_{REF} = 250 nA$		50		GΩ		
Output Resistance	Output Resistance R _{OUT}	IVIAX4004	$I_{REF} = 2.5 \text{mA}$	5			МΩ	
		MAX4006		10		kΩ		
Output Leakage		REF = open			1		рА	
Output Voltage Range	Vouт	$V_{BIAS} = 2.7V$ to 22V, $I_{REF} = 0$ to 1mA, MAX4004: $\Delta I_{OUT}/I_{OUT} = \pm 1\%$, MAX4006: $\Delta V_{OUT}/I_{OUT} = \pm 1\%$		0 to V _{BIAS} - 1.5	0 to V _{BIAS} - 0.85		V	

ELECTRICAL CHARACTERISTICS (continued)

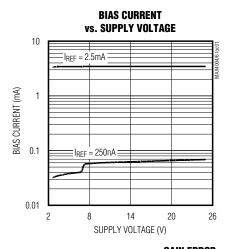
($V_{BIAS} = 5V$, GND = 0V, REF = Open, $V_{OUT} = 0V$ (MAX4004), CLAMP = Open, $T_A = -40^{\circ}C$ to +85°C. Typical values are at $T_A = +25^{\circ}C$, unless otherwise noted.) (Note 1)

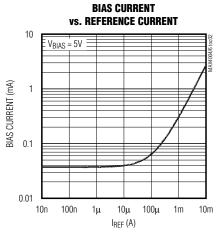
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
Output Clamp Voltage	V _{OUT} - V _{CLAMP}				0.6		V	
Output Clamp Leakage		V _{CLAMP} = 0 to 22V			1		рА	
		I _{REF} = 250nA		0.09	0.099	0.11		
MAX4004 Current Gain	IOUT/IREF	I _{REF} = 2.5mA		0.094	0.095	0.106	mA/mA	
		I _{REF} = 10mA, V _{BIAS} = 12V			0.098			
MAN/4000		I _{REF} = 250nA		0.9	0.995	1.1	V/mA	
MAX4006 Transimpedance Gain	V _{OUT} /I _{REF}	I _{REF} = 2.5mA		0.95	0.998	1.05		
Transimpedance dain		I _{REF} = 10mA, V _{BIAS} = 12V			0.992			
	Power-Supply Rejection $(\Delta I_{OUT}/I_{OUT})$	MAX4004: V _{BIAS} = 2.7V to 22V	I _{REF} = 250nA		50	1000		
Power-Supply Rejection			I _{REF} = 1mA		50	1000]	
Ratio (PSRR)	(ΔV _{OUT} /V _{OUT}) /ΔV _{BIAS}	MAX4006: V _{BIAS} = 2.7V to 22V	I _{REF} = 1mA		50	1000	- ppm/V	
Power-Up Settling Time	ts	I _{OUT} settles within 0.1%, C _{IN} = 10nF between	I _{REF} = 250nA		7.5		ms	
1 ower op cetting fille	.5	REF and GND	$I_{REF} = 2.5 mA$		90		μs	

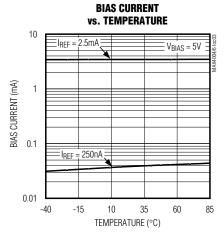
Note 1: All devices are 100% tested at room temperature (TA = +25°C). All temperature limits are guaranteed by design.

Typical Operating Characteristics

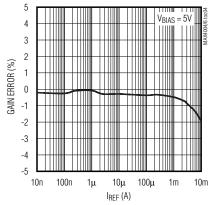
 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

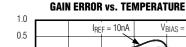


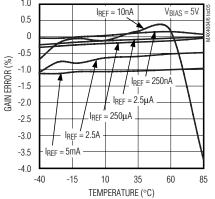




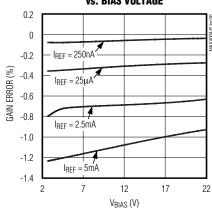




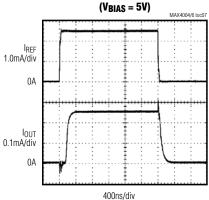




GAIN ERROR vs. BIAS VOLTAGE

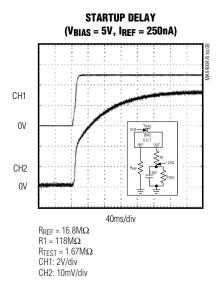


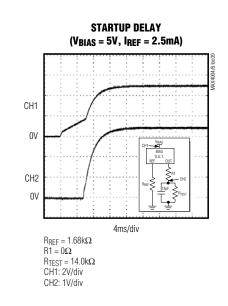
TRANSIENT RESPONSE

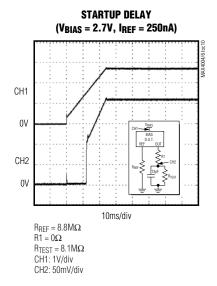


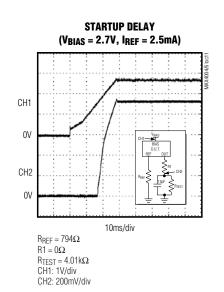
Typical Operating Characteristics (continued)

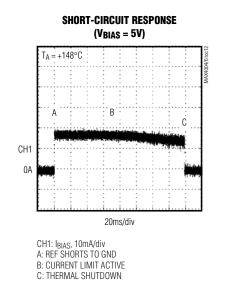
 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$







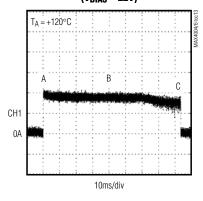




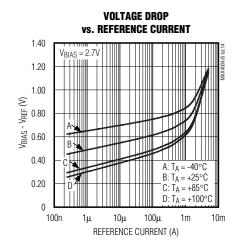
Typical Operating Characteristics (continued)

 $(T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

SHORT-CIRCUIT RESPONSE (V_{BIAS} = 22V)



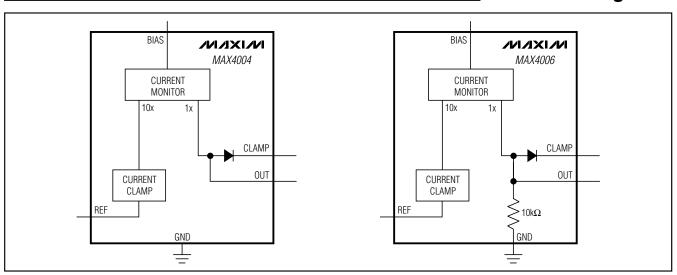
CH1: I_{BIAS}, 10mA/div A: REF SHORTS TO GND B: CURRENT LIMIT ACTIVE C: THERMAL SHUTDOWN



Pin Description

F	PIN		FUNCTION		
MAX4004	MAX4006	NAME	FUNCTION		
1	1	CLAMP	Clamp Voltage Input. External potential used for voltage clamping of V _{OUT} .		
2	2	GND Ground			
3	_	OUT	Current-Monitor Output. OUT sources a current of 1/10th IREF.		
4	4	REF Reference Current Output. REF provides the source the cathode of the photodiode.			
5	5	N.C.	No Connection. Not internally connected.		
6	6	BIAS	Bias Voltage Input. Bias voltage for photodiode.		
_	3	OUT	Current-Monitor Output. OUT presents a voltage proportional to IREF at 1V/mA. An internal $10k\Omega$ resistor connects OUT to GND (see Functional Diagram).		

Functional Diagrams



Detailed Description

The MAX4004/MAX4006 are versatile current monitors intended for monitoring DC photodiode current in fiber applications (see the *Functional Diagram*). The MAX4004 output is a current that is exactly one-tenth of the reference current. The MAX4006 outputs a voltage that is proportional to the reference current with a transimpedance gain of 1V/mA achieved by a factory-trimmed, internal $10k\Omega$ resistor.

Both current devices have six decades of dynamic range and monitor reference current ranging from 250nA to 2.5mA, the nominal operating range, with better than 5% accuracy across the entire reference current, bias voltage, and temperature ranges. The corresponding monitor outputs produce 25nA to 0.25mA (MAX4004) and 0.25mV to 2.5V (MAX4006). When the reference currents are extended to the broader range of 10nA to 10mA, an accuracy of less than 10% is maintained. Internal current limiting (20mA, typ) protects the device against short-circuit-to-ground conditions, and a thermal shutdown feature reduces both the reference current and the monitor current to zero if the die temperature reaches +150°C.

The MAX4004/MAX4006 accept a +2.7V to +22V supply voltage, suitable for PIN photodiode applications. A clamping diode, shown in the *Functional Diagram*, is provided to protect subsequent output circuitry from an overvoltage condition.

Applications Information

Clamping the Monitor Output Voltage

CLAMP provides a means for diode clamping the voltage at OUT, thus V_{OUT} is limited to V_{CLAMP} + 0.6V. CLAMP can be connected to either an external supply, to BIAS, or can be left floating if voltage clamping is not required.

Using PIN Photodiodes in Fiber Applications

When using the MAX4004/MAX4006 to monitor PIN photodiode currents in fiber applications, several issues must be addressed. In applications where the photodiode must be fully depleted, keep track of voltages budgeted for each component with respect to the available supply voltage(s). The current monitor requires as much as 1.2V between BIAS and REF, which must be considered as part of the overall voltage budget.

Additional voltage margin can be created if a negative supply is used in place of a ground connection, as long as the overall voltage drop experienced by the MAX4004/MAX4006 is less than or equal to 22V. For this type of application, the MAX4004 is suggested so the output can be referenced to "true" ground and not the negative supply. The MAX4004's output current can be referenced as desired with either a resistor to ground or a transimpedance amplifier. Take care to ensure that output voltage excursions do not interfere with the required margin between BIAS and OUT.

In many fiber applications, OUT is connected directly to an ADC that operates from a supply voltage that is less than the voltage at BIAS. Connecting the MAX4004/MAX4006's clamping diode output, CLAMP, to the ADC power supply helps avoid damage to the ADC. Without this protection, voltages can develop at OUT that can destroy the ADC. This protection is less critical when OUT is connected directly to subsequent transimpedance amplifiers (linear or logarithmic) that have low-impedance, near-ground-referenced inputs. If a transimpedance amp is used on the low side of the photodiode, its voltage drop must also be considered. Leakage from the clamping diode is most often insignificant over nominal operating conditions, but grows with temperature.

To maintain low levels of wideband noise, lowpass filtering the output signal is suggested in applications where only DC measurements are required. Determining the required filtering components is straightforward, as the MAX4004 exhibits a very high output impedance (>5M Ω), while the MAX4006 exhibits an output resistance of 10k Ω .

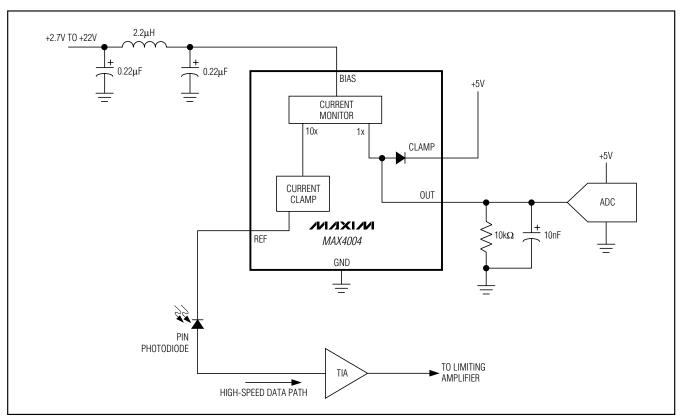
In some applications where pilot tones are used to identify specific fiber channels, higher bandwidths are desired at OUT to detect these tones. Consider the minimum and maximum currents to be detected, then consult the frequency response and noise typical operating curves. If the minimum current is too small, insufficient monitor bandwidth could result, while too high of a current could result in excessive monitor noise across the desired bandwidth.

Bypassing and External Components

In applications where power-supply noise can interfere with DC diode measurements, additional filtering is suggested. Such noise is commonly seen when switching power supplies are used to generate the photodiode bias voltage. As shown in the typical operating circuit, a pi filter (two 0.22µF capacitors and one 2.2µH inductor) greatly suppress power-supply switching noise. If such a filter is already present in the bias-generating circuit, only a simple bypass capacitor at the BIAS pin is suggested. The output lowpass filter, a $10\text{k}\Omega$ resistor and a 10nF capacitor, further reduce permeating power-supply noise as well as other wideband noise that might otherwise restrict measurements at low signal levels. Again, reducing the bandwidth of the OUT signal can affect performance of pilot-tone systems.

To restrict high-frequency photodiode signals from affecting the current monitor and BIAS power supply, an RF choke and 10nF capacitor can be added. The capacitance presented to REF should not exceed 10nF; larger values increase startup time and could cause the thermal shutdown circuit to activate during startup.

Typical Operating Circuit

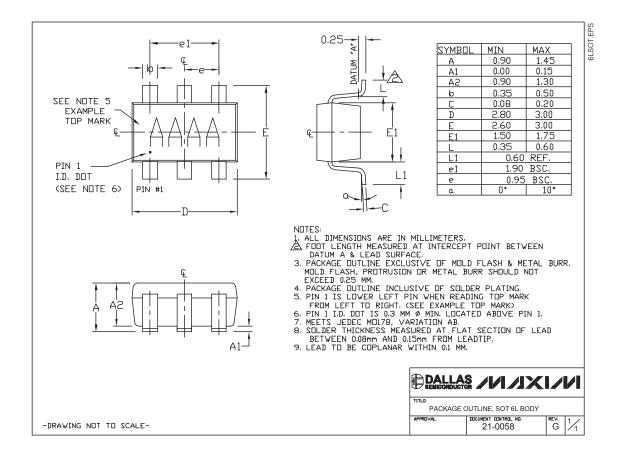


_Chip Information

TRANSISTOR COUNT: 195 PROCESS: BICMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



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