



N-Channel Reduced Q_g , Fast Switching MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.0085 at V _{GS} = 10 V	15		
	0.0125 at V _{GS} = 4.5 V	12		

SO-8 S 1 S 2 F 7 D S 3 G 4 Top View

Ordering Information: Si4384DY-T1-E3 (Lead (Pb)-free) Si4384DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

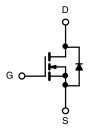
FEATURES

- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Gen II Power MOSFETs
- PWM Optimized
- 100 % R_g Tested

ROHS COMPLIANT HALOGEN FREE Available

APPLICATIONS

- High-Side DC/DC Conversion
 - Notebook
 - Desktop
 - Server



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	30		V
Gate-Source Voltage		V _{GS}	± 20		V
0 " D : 0 . (T . 150.00)3	T _A = 25 °C	- I _D	15	10	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		12	8	
Pulsed Drain Current		I _{DM}	± 50		Α
Continuous Source Current (Diode Conduction) ^a		I _S	2.8	1.3	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	25 31		
Avalanche Energy	L = U. I IIII	E _{AS}			mJ
M	T _A = 25 °C	- P _D	3.1	1.47	14/
Maximum Power Dissipation ^a	T _A = 70 °C		2	0.95	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian In the Architect (MOCFET)	t ≤ 10 s	- R _{thJA}	34	40	°C/W
Maximum Junction-to-Ambient (MOSFET) ^a	Steady State		71	85	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	20	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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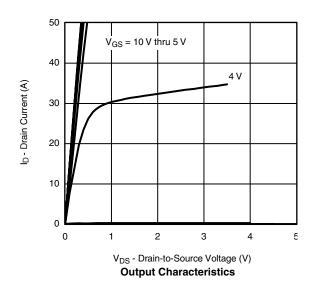
MOSFET SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	ymbol Test Conditions		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0		3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zara Cata Valtaga Drain Current		V _{DS} = 30 V, V _{GS} = 0 V		1			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 70 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
	В	V _{GS} = 10 V, I _D = 15 A		0.007	0.0085	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 12 \text{ A}$	12 A 0.0105 0.01		0.0125	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		56		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 2.8 A, V _{GS} = 0 V		0.75	1.1	V	
Dynamic ^b				•	•		
Total Gate Charge	Q_g			12	18		
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 15 \text{ A}$		5.9		nC	
Gate-Drain Charge	Q_{gd}			4.0			
Gate Resistance	R_{g}		0.8	1.7	2.5	Ω	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		13	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong\text{1 A, V}_\text{GEN}=\text{10 V, R}_\text{g}=\text{6}~\Omega$		45	70	ns	
Fall Time	t _f			13	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.8 A, dI/dt = 100 A/μs		25	50		

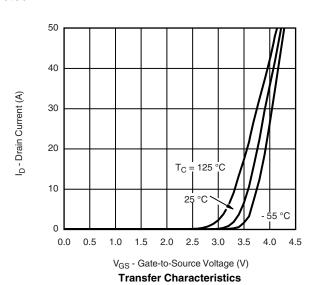
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



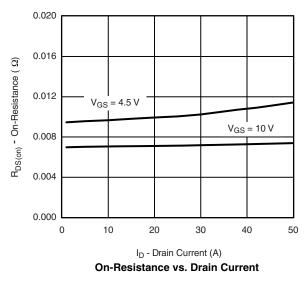


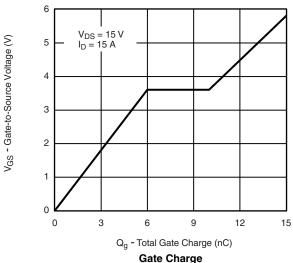


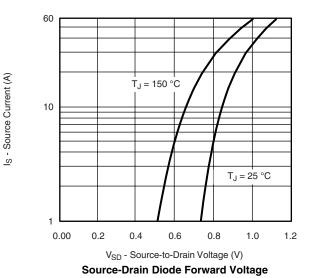


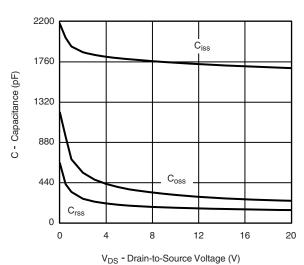


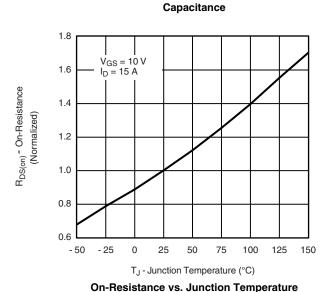
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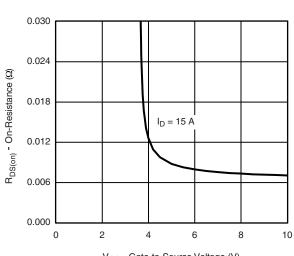










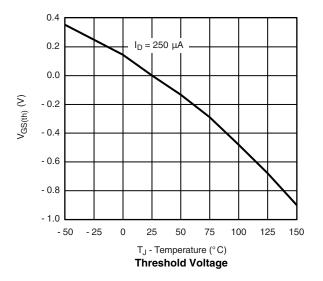


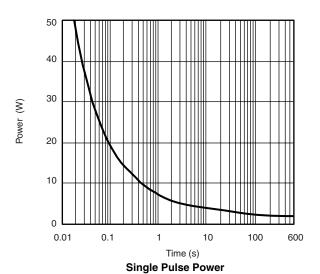
 $\mbox{V}_{\mbox{GS}}$ - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Voltage

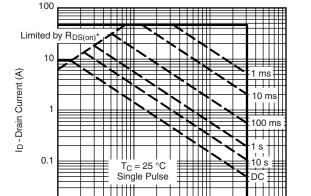
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



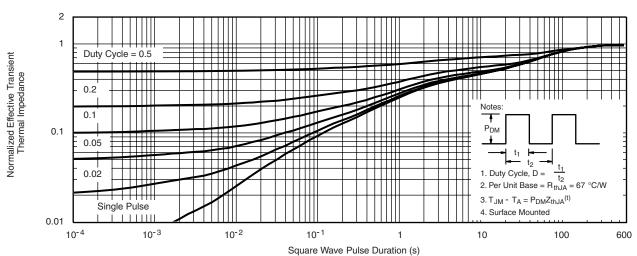




 $V_{DS} \text{ - Drain-to-Source Voltage (V)} \\ ^*V_{GS} \text{ > minimum } V_{GS} \text{ at which } R_{DS(on)} \text{ is specified}$

100

Safe Operating Area, Junction-to-Case

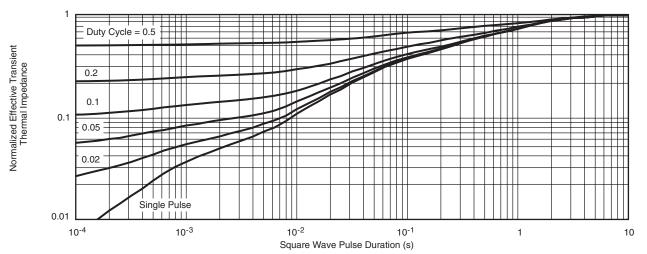


0.01 L 0.1

Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

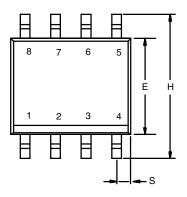


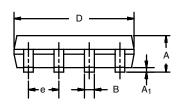
Normalized Thermal Transient Impedance, Junction-to-Foot

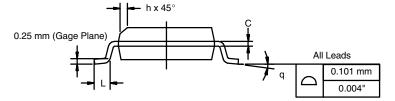
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06

APPLICATION NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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