



## STN83003

High voltage fast-switching  
NPN power transistor

### Features

- High voltage capability
- Very high switching speed

### Application

- Electronics ballasts for fluorescent lighting

### Description

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA. The STN83003 is expressly designed for a new solution to be used in compact fluorescent lamps, where it is coupled with the STN93003, its complementary PNP transistor.

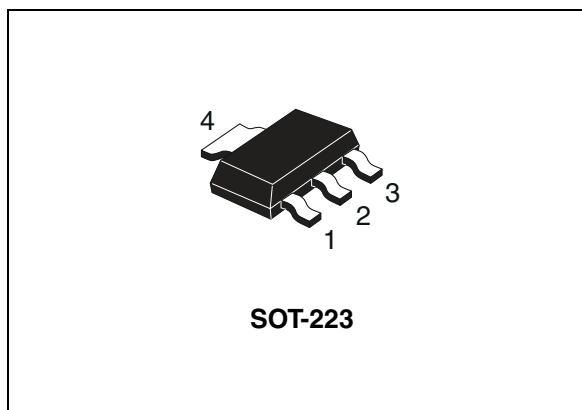


Figure 1. Internal schematic diagram

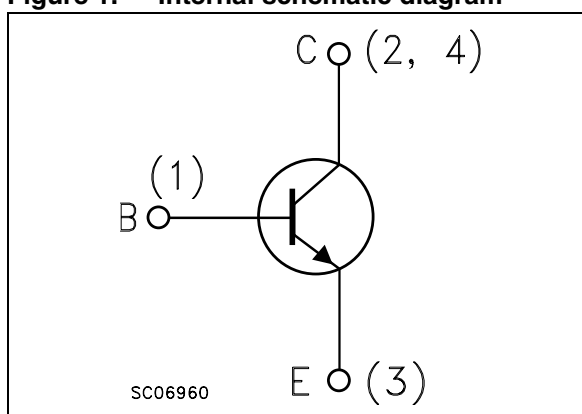


Table 1. Device summary

Part number	Marking	Package	Packaging
STN83003	N83003	SOT-223	Tape and reel

# Contents

<b>1</b>	<b>Electrical ratings</b> .....	<b>3</b>
<b>2</b>	<b>Electrical characteristics</b> .....	<b>4</b>
2.1	Electrical characteristics (curves) .....	5
2.2	Test circuits .....	7
<b>3</b>	<b>Package mechanical data</b> .....	<b>8</b>
<b>4</b>	<b>Revision history</b> .....	<b>10</b>

# 1 Electrical ratings

**Table 2. Absolute maximum ratings**

Symbol	Parameter	Value	Unit
$V_{CES}$	Collector-emitter voltage ( $V_{BE} = 0$ )	700	V
$V_{CEO}$	Collector-emitter voltage ( $I_B = 0$ )	400	V
$V_{EBO}$	Emitter-base voltage ( $I_C = 0$ , $I_B = 0.75$ A, $t_P < 10$ $\mu$ s)	$V_{(BR)EBO}$	V
$I_C$	Collector current	1.5	A
$I_{CM}$	Collector peak current ( $t_P < 5$ ms)	3	A
$I_B$	Base current	0.75	A
$I_{BM}$	Base peak current ( $t_P < 5$ ms)	1.5	A
$P_{TOT}$	Total dissipation at $T_a = 25$ °C	1.6	W
$T_{STG}$	Storage temperature	-65 to 150	°C
$T_J$	Max. operating junction temperature	150	°C

**Table 3. Thermal data**

Symbol	Parameter	Value	Unit
$R_{thJA}$	Thermal resistance junction-ambient <sup>(1)</sup> max	78	°C/W

1. Device mounted on PCB area of 1 cm<sup>2</sup>.

## 2 Electrical characteristics

$T_{\text{case}} = 25\text{ °C}$  unless otherwise specified.

**Table 4. Electrical characteristics**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$I_{\text{CES}}$	Collector cut-off current ( $V_{\text{BE}} = 0$ )	$V_{\text{CE}} = 700\text{ V}$ $V_{\text{CE}} = 700\text{ V}$ $T_{\text{C}} = 125\text{ °C}$			1 5	mA mA
$V_{(\text{BR})\text{EBO}}$	Emitter-base breakdown voltage ( $I_{\text{C}} = 0$ )	$I_{\text{E}} = 10\text{ mA}$	12		18	V
$V_{\text{CE(sus)}}^{(1)}$	Collector-emitter sustaining voltage ( $I_{\text{B}} = 0$ )	$I_{\text{C}} = 10\text{ mA}$	400			V
$V_{\text{CE(sat)}}^{(1)}$	Collector-emitter saturation voltage	$I_{\text{C}} = 0.35\text{ A}$ $I_{\text{B}} = 50\text{ mA}$ $I_{\text{C}} = 0.5\text{ A}$ $I_{\text{B}} = 0.1\text{ A}$			1 0.5	V V
$V_{\text{BE(sat)}}^{(1)}$	Base-emitter saturation voltage	$I_{\text{C}} = 0.5\text{ A}$ $I_{\text{B}} = 0.1\text{ A}$			1	V
$h_{\text{FE}}$	DC current gain	$I_{\text{C}} = 10\text{ mA}$ $V_{\text{CE}} = 5\text{ V}$ $I_{\text{C}} = 0.35\text{ A}$ $V_{\text{CE}} = 5\text{ V}$ $I_{\text{C}} = 1\text{ A}$ $V_{\text{CE}} = 5\text{ V}$	10 16 4	25	32	
$t_{\text{r}}$ $t_{\text{s}}$ $t_{\text{f}}$	Resistive load Rise time Storage time Fall time	$I_{\text{C}} = 0.35\text{ A}$ $V_{\text{CC}} = 125\text{ V}$ $I_{\text{B1}} = - I_{\text{B2}} = 70\text{ mA}$ $t_{\text{p}} \geq 25\text{ }\mu\text{s}$	1.5	100 2.2 0.2	2.9	ns $\mu\text{s}$ $\mu\text{s}$
$t_{\text{s}}$ $t_{\text{f}}$	Inductive load Storage time Fall time	$I_{\text{C}} = 0.5\text{ A}$ $I_{\text{B1}} = 0.1\text{ A}$ $V_{\text{BE(off)}} = -5\text{ V}$ $L = 10\text{ mH}$ $V_{\text{Clamp}} = 300\text{ V}$		450 90		ns ns

1. Pulse test: pulse duration  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

## 2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

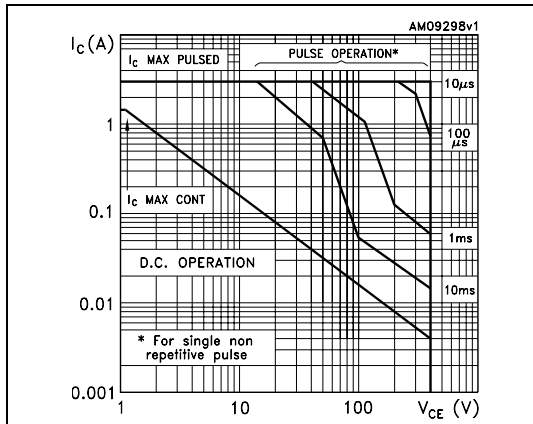


Figure 3. Derating curve

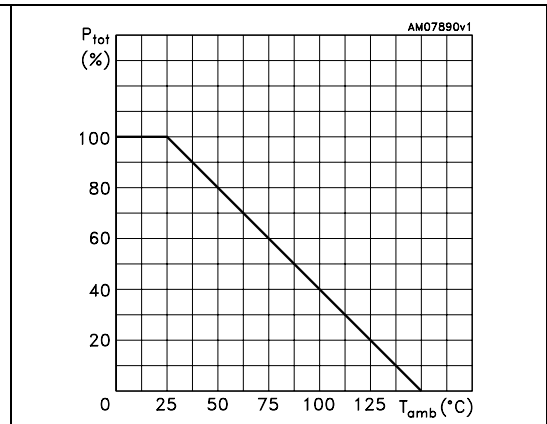


Figure 4. DC current gain ( $V_{CE} = 5 V$ )

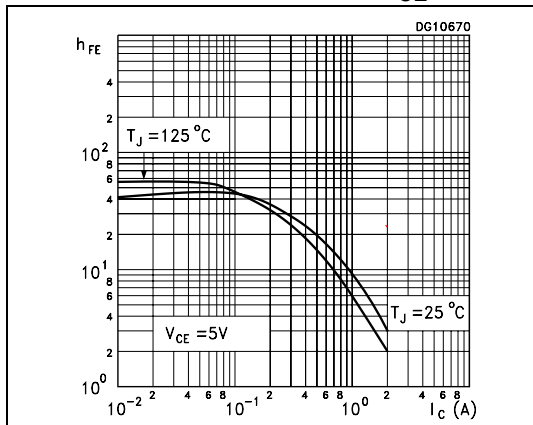


Figure 5. DC current gain ( $V_{CE} = 1 V$ )

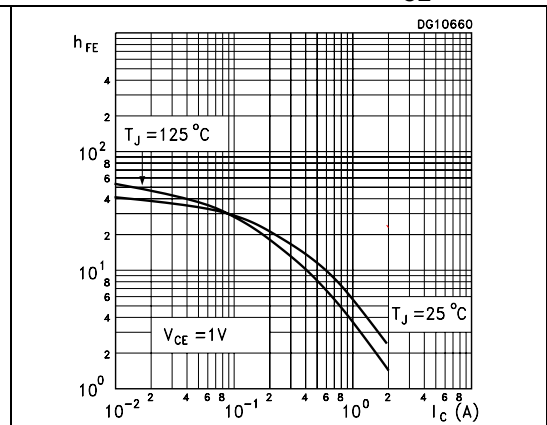


Figure 6. Collector-emitter saturation voltage

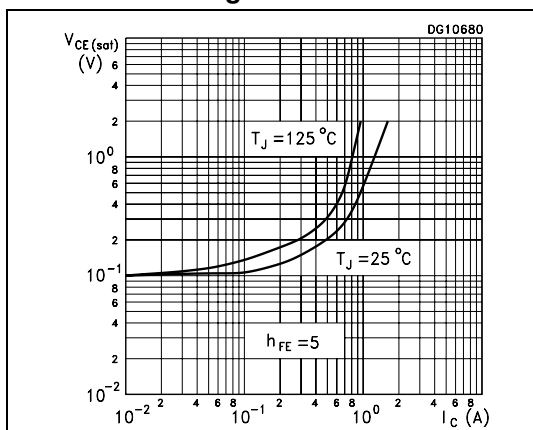


Figure 7. Base-emitter saturation voltage

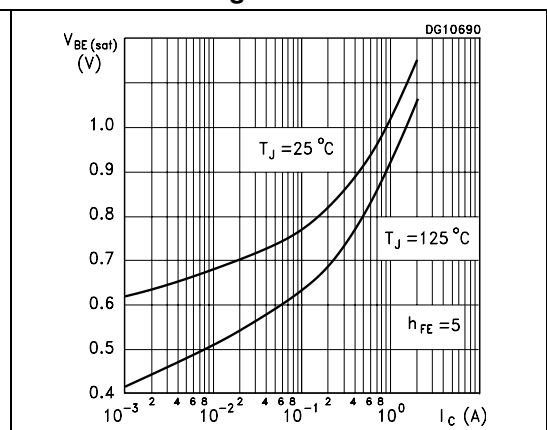


Figure 8. Resistive load fall time

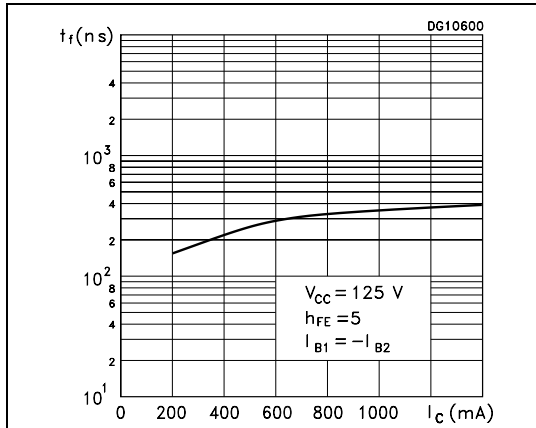


Figure 9. Resistive load storage time

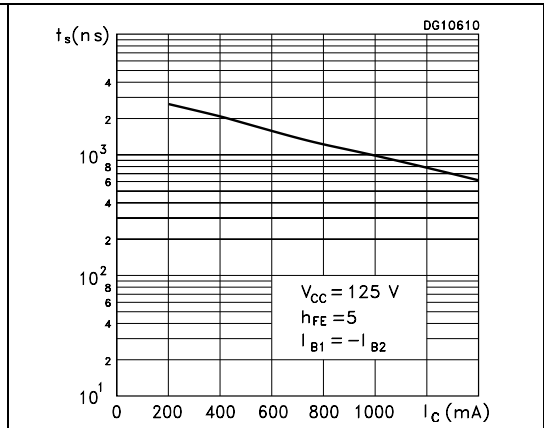


Figure 10. Inductive load fall time

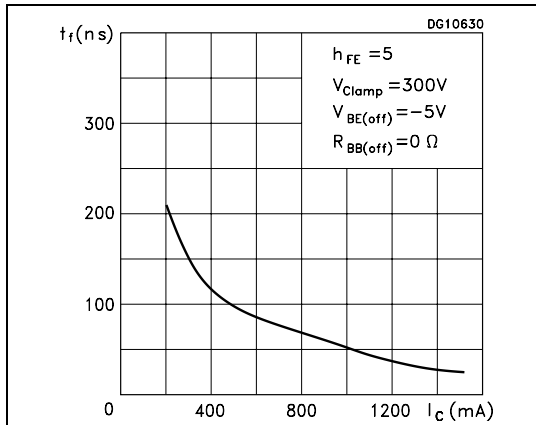


Figure 11. Inductive load storage time

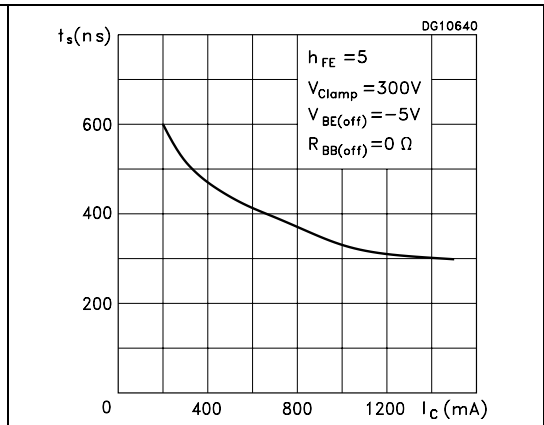
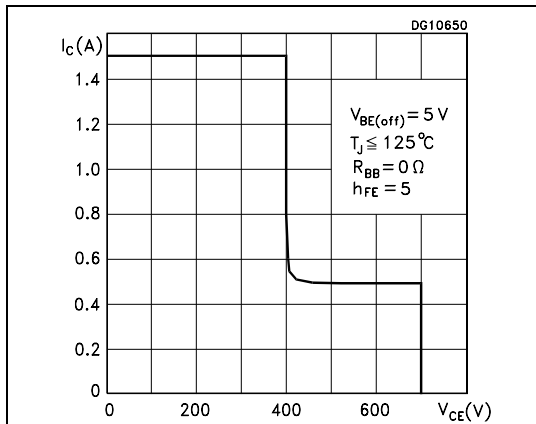
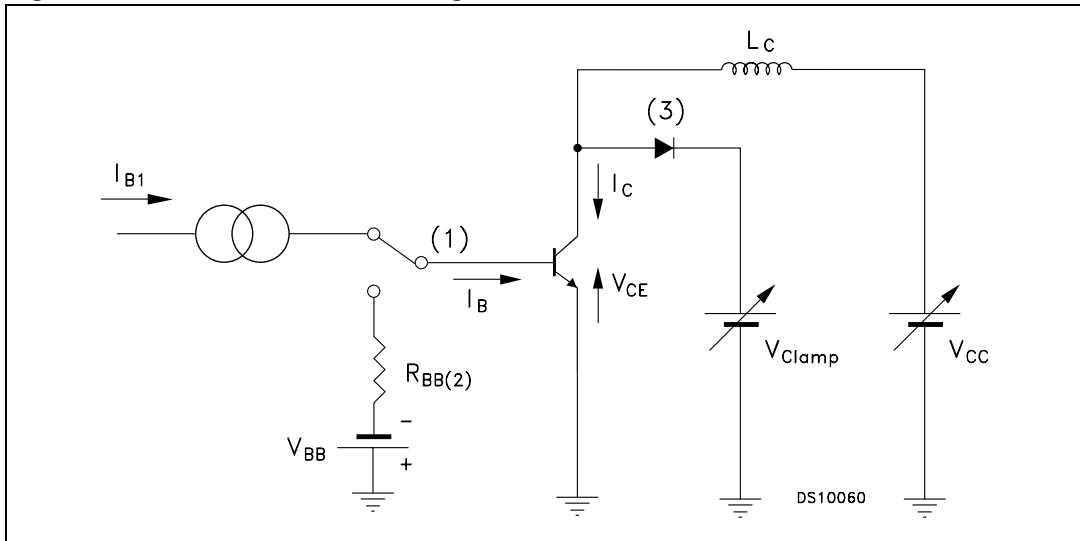


Figure 12. Reverse biased SOA



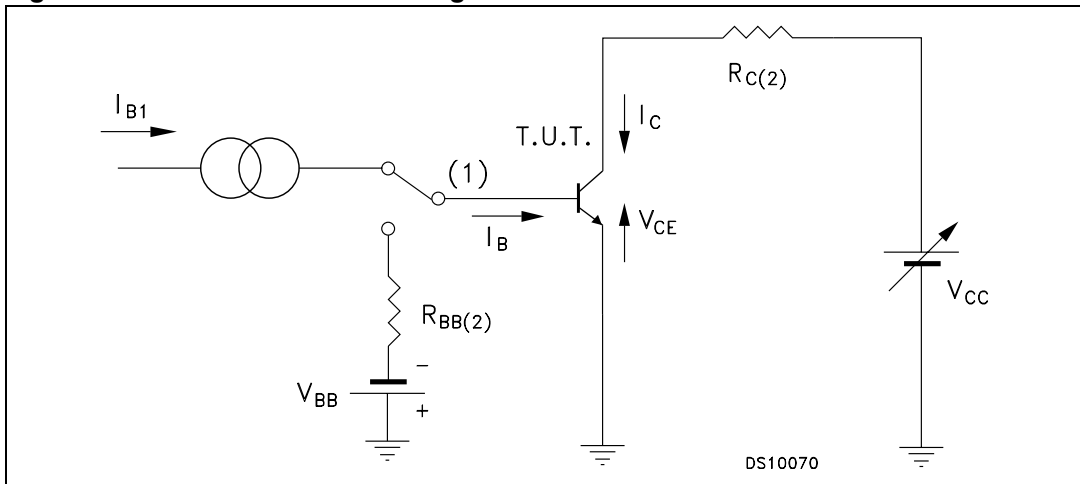
## 2.2 Test circuits

**Figure 13. Inductive load switching test circuit**



1. Fast electronic switching
2. Non-inductive resistor
3. Fast recovery rectifier

**Figure 14. Resistive load switching test circuit**



1. Fast electronic switching
2. Non-inductive resistor

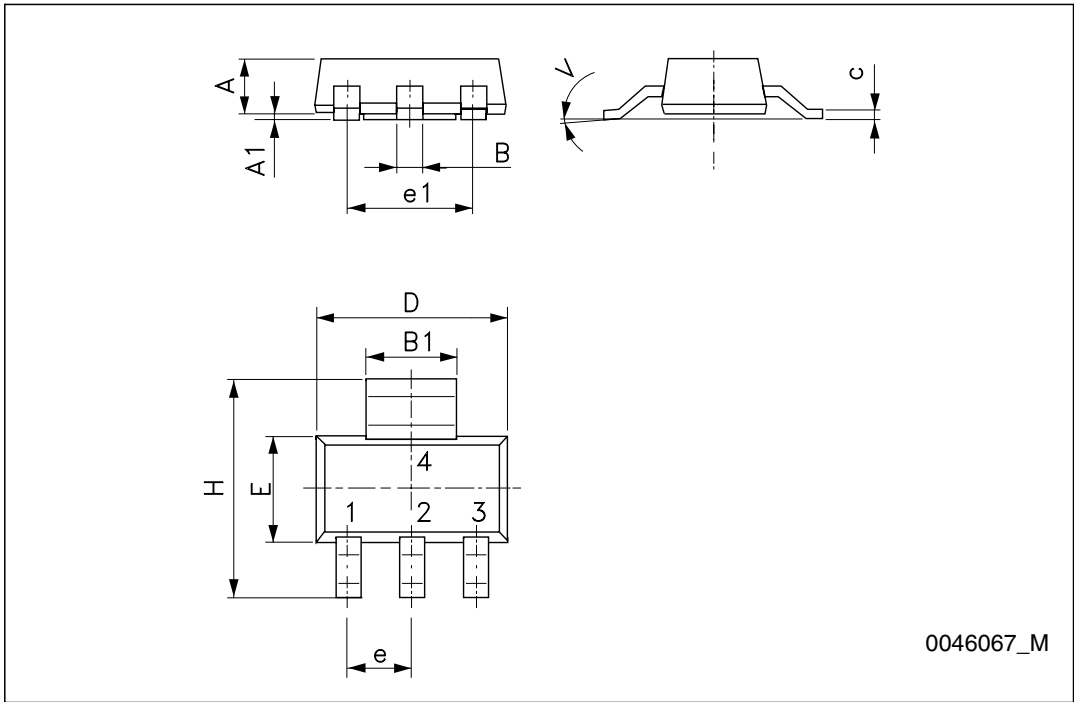
### 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK<sup>®</sup> is an ST trademark.



**SOT-223 mechanical data**

Dim.	mm.		
	Min.	Typ.	Max.
A			1.80
A1	0.02		0.1
B	0.60	0.70	0.85
B1	2.90	3.00	3.15
c	0.24	0.26	0.35
D	6.30	6.50	6.70
e		2.30	
e1		4.60	
E	3.30	3.50	3.70
H	6.70	7.00	7.30
V			10 °



0046067\_M

## 4 Revision history

**Table 5. Document revision history**

Date	Revision	Changes
09-May-2006	1	Initial release.
17-Jan-2007	2	The device's safe operating area curve has been added on page 5.
13-Dec-2010	3	Updated package mechanical data <a href="#">on page 9</a> .

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