

### 30V N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BVDSS	Rds(on) Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	28mΩ @ V <sub>GS</sub> = 10V	7.0A
30V	32mΩ @ V <sub>GS</sub> = 4.5V	6.5A

# **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions

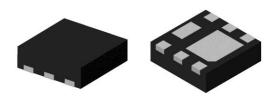
### **Features and Benefits**

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <a href="https://www.diodes.com/quality/product-definitions/">https://www.diodes.com/quality/product-definitions/</a>

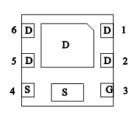
## **Mechanical Data**

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 Per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.007 grams (Approximate)

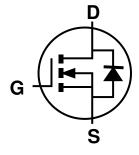
U-DFN2020-6 (Type F)







Pin Out Bottom View



Internal Schematic

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3042LFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMN3042LFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/



# **Marking Information**

Site 1



S7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Jale Code Ney												
Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н		J	K	L	М	N	0	Р	R
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Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



S7 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2016	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	6	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	X	Υ	Z



### **Maximum Ratings** (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	30	V	
Gate-Source Voltage		$V_{GSS}$	±12	V	
Continuous Drain Current (Note 6) Vgs = 10V	lo	7.0 5.6	А		
Maximum Continuous Body Diode Forward Curre	nt (Note 6)		Is	1.5	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1	%)	I <sub>DM</sub>	35	Α	
Avalanche Current (L = 0.1mH) (Note 7)		las	13	Α	
Avalanche Energy (L = 0.1mH) (Note 7)			Eas	9	mJ

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	0.7	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	177	°C/W	
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	R <sub>0</sub> JA	124	G/VV	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	$P_{D}$	2.1	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State		61	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	43	C/VV	
Thermal Resistance, Junction to Case	Steady State	$R_{\theta JC}$	9.3	°C/W	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

### **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)					•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	1	_	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	Igss		-	±100	nA	$V_{GS} = \pm 12V$ , $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	0.6		1.4	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$
			17	28		$V_{GS} = 10V, I_{D} = 4.0A$
Static Drain-Source On-Resistance	Dagger		20	32	mΩ	$V_{GS} = 4.5V, I_D = 4.0A$
Static Diain-Source On-Nesistance	Rds(on)	l	24	42	11152	$V_{GS} = 3.0V, I_{D} = 4.0A$
		_	28	50		$V_{GS} = 2.5V, I_{D} = 4.0A$
Diode Forward Voltage	$V_{SD}$	_	0.7	1.2	V	$V_{GS} = 0V$ , $I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C <sub>iss</sub>		570	_		V 45V V 0V
Output Capacitance	Coss	1	63	_	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		53	_		1 = 1.0WH12
Gate Resistance	$R_g$	l	3.2	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$
Total Gate Charge (VGS = 10V)	$Q_g$	l	13.3	_		
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_g$		6.1	_	nC	Vps = 15V. lp = 6.9A
Gate-Source Charge	Qgs	l	1.0	_	110	VDS = 15V, ID = 6.9A
Gate-Drain Charge	$Q_{gd}$		1.6	_		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	1.5	_		
Turn-On Rise Time	t <sub>R</sub>	_	3.3	_	no	$V_{GS} = 10V, V_{DD} = 15V, R_g = 3\Omega,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	13.9		ns	I <sub>D</sub> = 6.9A
Turn-Off Fall Time	tF	_	4.9	_		
Body Diode Reverse Recovery Time	trr	_	7.8	_	ns	Is = 5A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Qrr		1.9	_	nC	Is = 5A, dI/dt = 100A/µs

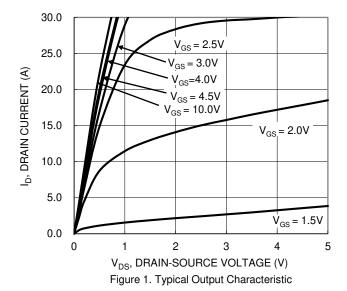
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

<sup>7.</sup>  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_{J} = +25$ °C.

<sup>8.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>9.</sup> Guaranteed by design. Not subject to product testing.





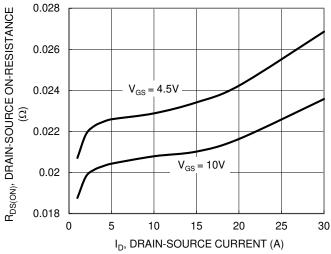


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

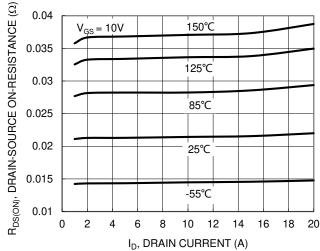
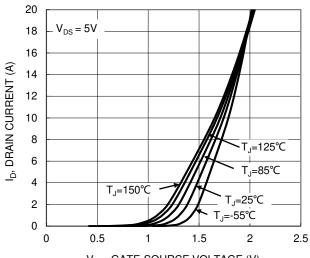


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

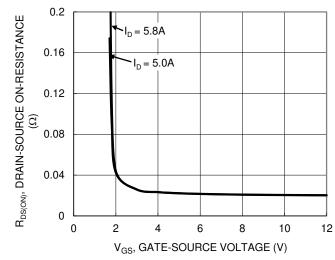


Figure 4. Typical Transfer Characteristic

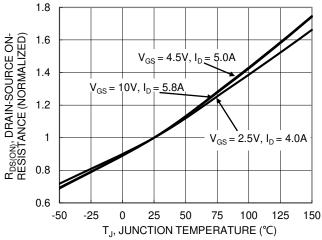


Figure 6. On-Resistance Variation with Temperature



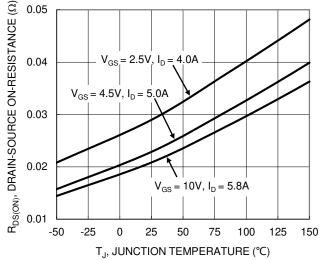


Figure 7. On-Resistance Variation with Temperature

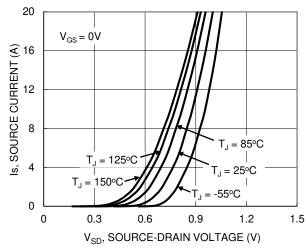
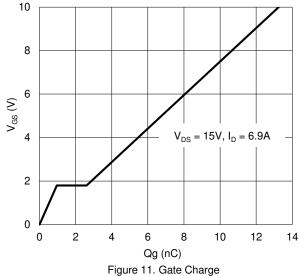


Figure 9. Diode Forward Voltage vs. Current



1.2  $V_{GS(TH)}$ , GATE THRESHOLD VOLTAGE (V) 1.1 1  $I_D = 1mA$ 0.9 0.8  $I_D = 250 \mu A$ 0.7 0.6 0.5 0.4 0.3 -50 -25 50 75 100 125 150

 $\rm T_{J},\,JUNCTION\,TEMPERATURE\,(^{C})$  Figure 8. Gate Threshold Variation vs. Junction Temperature

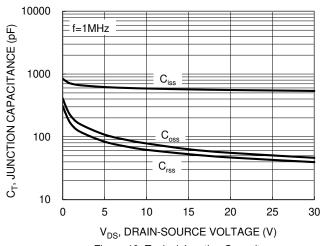
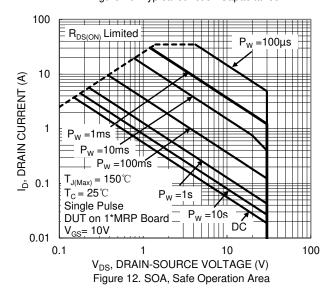


Figure 10. Typical Junction Capacitance





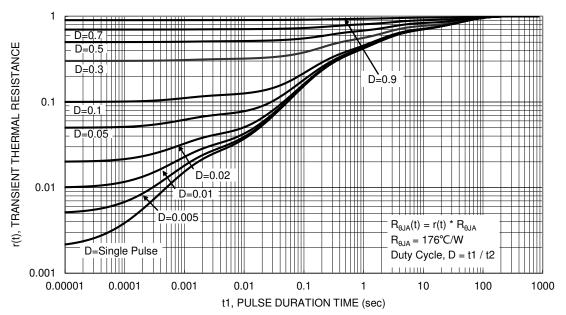


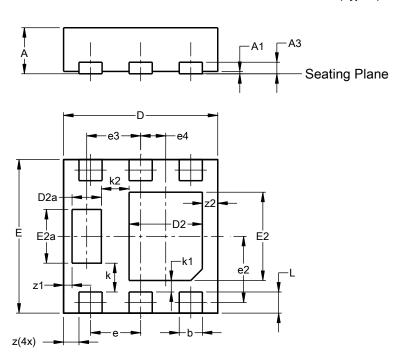
Figure 13. Transient Thermal Resistance



# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### U-DFN2020-6 (Type F)

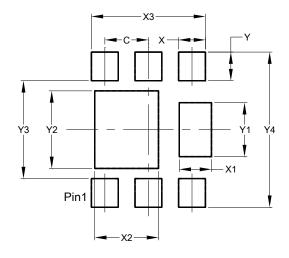


U-DFN2020-6							
	(Тур	oe F)					
Dim	Min	Max	Тур				
Α	0.57	0.63	0.60				
A1	0.00	0.05	0.03				
A3	-	-	0.15				
b	0.25	0.35	0.30				
D	1.95	2.05	2.00				
D2	0.85	1.05	0.95				
D2a	0.33	0.43	0.38				
Е	1.95	2.05	2.00				
E2	1.05	1.25	1.15				
E2a	0.65	0.75	0.70				
е		0.65 BS	С				
e2	(	).863 BS	SC SC				
е3		0.70 BS	С				
e4	(	).325 BS	SC SC				
k		0.37 BS	С				
k1		0.15 BS	С				
k2		0.36 BS					
L	0.225	0.325	0.275				
Z	0.20 BSC						
<b>z</b> 1		0.110 BSC					
z2		0.20 BS	C				
All C	)imens	ions in	mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### U-DFN2020-6 (Type F)



Dimensions	Value		
פווטופווסוטווס	(in mm)		
С	0.650		
X	0.400		
X1	0.480		
X2	0.950		
Х3	1.700		
Y	0.425		
Y1	0.800		
Y2	1.150		
Y3	1.450		
Y4	2.300		



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