

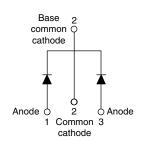
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Vishay Semiconductors

Schottky Rectifier, 2 x 10 A



 E_{AS}

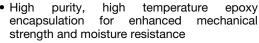


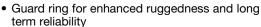
24 mJ

PRODUCT SUMMARY					
Package	TO-220AB				
I _{F(AV)} 2 x 10 A					
V_{R}	80 V, 90 V, 100 V				
V _F at I _F	0.65 V				
I _{RM} max.	6 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation







- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	SYMBOL CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform (per device)	20	А				
V _{RRM}		80 to 100	V				
I _{FRM}	T _C = 133 °C (per leg)	20	۸				
I _{FSM}	t _p = 5 μs sine	850	A				
V _F	10 A _{pk} , T _J = 125 °C	0.65	V				
T _J	Range	- 65 to 150	°C				

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS- MBR2080CTKPbF	VS- MBR2080CTK-N3	VS- MBR2090CTKPbF	VS- MBR2090CTK-N3	VS- MBR20100CTKPbF	VS- MBR20100CTK-N3	UNITS	
Maximum DC reverse voltage	V_{R}								
Maximum working peak reverse voltage	V _{RWM}	80	80	90	90	100	100	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average per leg		T _C = 133 °C, rated V _R		10		
forward current per device	I _{F(AV)}			20		
Peak repetitive forward current per leg	I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 133 °C		20		
Non-repetitive peak surge current	I _{ESM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	850	Α	
	Surge applied at rated load cor single phase, 60 Hz		nditions half wave,	150		
Peak repetitive reverse surge current	I _{RRM}	2.0 μs, 1.0 kHz		0.5		
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 12 \text{m}$	Н	24	mJ	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	ONDITIONS	VALUES	UNITS	
		10 A	T _{.1} = 25 °C	0.80	V	
Maximum farward valtage drap	V _{FM} ⁽¹⁾	20 A	- IJ = 25 C	0.95		
Maximum forward voltage drop	VFM (')	10 A	T _J = 125 °C	0.65		
		20 A	- IJ = 125 C	0.80		
Maximum instantaneous reverse	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.10	- mA	
current	IRM ('')	T _J = 125 °C	hated DC voltage	6		
Threshold voltage	V _{F(TO)}	T T manyimum		0.433	V	
Forward slope resistance	r _t	$T_J = T_J$ maximum		15.8	mΩ	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal rar	400	pF		
Typical series inductance	L _S	Measured from top of tern	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction temperature range	TJ		- 65 to 150	°C		
Maximum storage temperature range	T _{Stg}		- 65 to 175	C		
Maximum thermal resistance, junction to case per leg	R _{thJC}	DC operation	2.0			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W		
Approximate weight			2	g		
Approximate weight			0.07	OZ.		
Mounting torque		Non-link death of three de	6 (5)	kgf · cm		
Mounting torque maximum		Non-lubricated threads	12 (10)	(lbf \cdot in)		
			MBR2080CTK			
Marking device		Case style TO-220AB	MBR2090CTK			
			MBR20	100CTK		

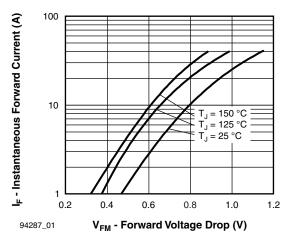


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

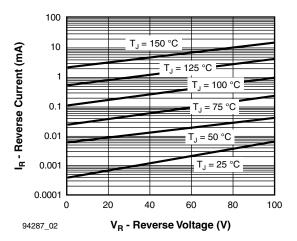


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

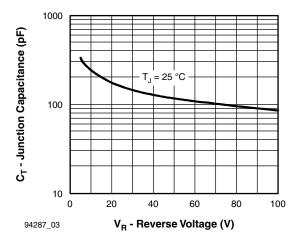


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

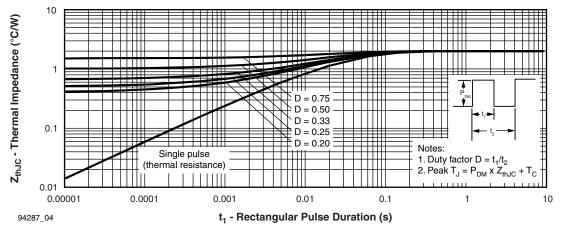
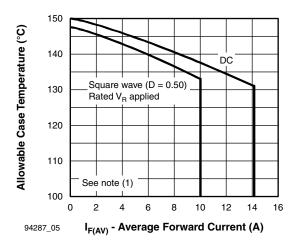
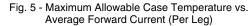


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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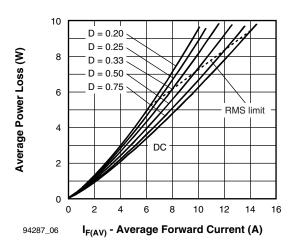


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

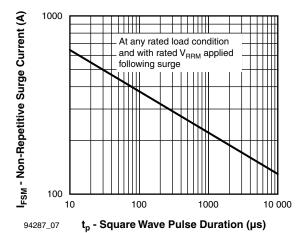


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

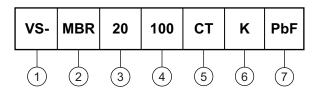
Note

 $\begin{array}{ll} \text{(1)} \ \ \text{Formula used:} \ T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \ \text{at } (I_{F(AV)}/D) \ \text{(see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \ \text{(1 - D)}; \ I_R \ \text{at } V_{R1} = \text{Rated } V_R \\ \end{array}$

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - MBR series

3 - Current rating (20 = 20 A) 80 = 80 V 4 - Voltage rating 90 = 90 V 100 = 100 V

5 - CT = Center tap (dual)

6 - K = Schottky generation
7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-MBR2080CTKPbF	50	1000	Antistatic plastic tube				
VS-MBR2080CTK-N3	50	1000	Antistatic plastic tube				
VS-MBR2090CTKPbF	50	1000	Antistatic plastic tube				
VS-MBR2090CTK-N3	50	1000	Antistatic plastic tube				
VS-MBR20100CTKPbF	50	1000	Antistatic plastic tube				
VS-MBR20100CTK-N3	50	1000	Antistatic plastic tube				

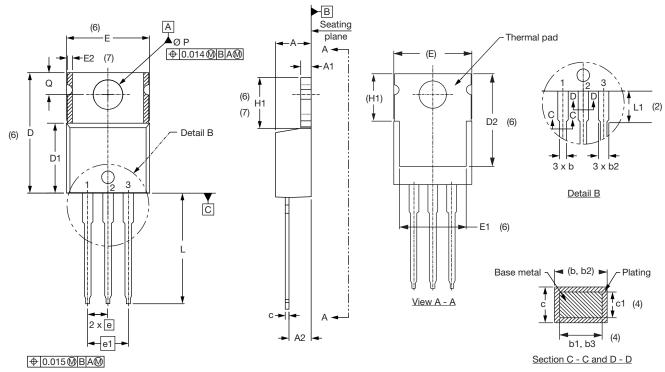
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95222</u>					
Deut annulin a information	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			



Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches



Lead tip

Lead assignments

Diodes

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIN	MILLIMETERS		HES	NOTES
STWIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Notes					

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° t	o 93°	
		•		•	

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- Controlling dimensions: inches
- Thermal pad contour optional within dimensions E, H1, D2 and
- Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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