

Vishay Siliconix

# N-Channel 80-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
80	0.013 at V <sub>GS</sub> = 10 V	17.3	35 nC			

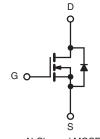
#### **FEATURES**

- · Halogen-free
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

# RoHS COMPLIANT

#### **APPLICATIONS**

- · Primary Side Switch
- Half Bridge
- · Intermediate Bus Converter



N-Channel MOSFET

		SO-8		
s s s	1 2 3 4		8 7 6 5	D D D
		Top View		

Ordering Information: Si4110DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATIN</b>	IGS T <sub>A</sub> = 25 °C,	unless otherwise	noted		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	80	V		
Gate-Source Voltage	V <sub>GS</sub>	± 20			
	T <sub>C</sub> = 25 °C		17.3		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	13.9		
Continuous Drain Current (1) = 130 °C)	T <sub>A</sub> = 25 °C	'D	11.7 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		9.4 <sup>b, c</sup>	Α	
Pulsed Drain Current		I <sub>DM</sub>	60		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		6.5		
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3 <sup>b, c</sup>		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	35		
Single Pulse Avalanche Energy		E <sub>AS</sub>	61.3	mJ	
	T <sub>C</sub> = 25 °C		7.8		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	5	W	
Maximum Fower Dissipation	T <sub>A</sub> = 25 °C	' D	3.6 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		2.3 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	$R_{thJA}$	29	35	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	13	16			

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under Steady State conditions is 80 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	,			7.			
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I 050 ·· A		84		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 9.8			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	2		4	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	,	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V			1		
	I <sub>DSS</sub>	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, I_D = 11.7 \text{ A}$		0.0108	0.0130	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 11.7 A		23		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			2205		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		260			
Reverse Transfer Capacitance	C <sub>rss</sub>			78			
Total Gate Charge	Qg			35	53		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 11.7 \text{ A}$		12.5		nC	
Gate-Drain Charge	$Q_{gd}$			8			
Gate Resistance	$R_{g}$	f = 1 MHz	0.22	1.1	2.2	Ω	
Turn-on Delay Time	t <sub>d(on)</sub>			18	27		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 40 V, $R_L$ = 4.3 $\Omega$		10	18	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 9.4$ A, $V_{GEN}=8$ V, $R_g=1$ $\Omega$		22	33		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			15	23	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 40 V, $R_L$ = 4.3 $\Omega$		9	18	- - -	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 9.4 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		22	33		
Fall Time	t <sub>f</sub>			7	14		
<b>Drain-Source Body Diode Characteristi</b>	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			6.5	Α	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				60	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 9.4 A		0.80	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			45	68	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 9.4 A, dl/dt = 100 A/μs, T <sub>J</sub> = 25 °C		82	123	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	0.4 /1, αι/αι - 100 /ν μο, 1 <sub>J</sub> - 20 0		34		200	
Reverse Recovery Rise Time	erse Recovery Rise Time t <sub>b</sub>			11		ns	

#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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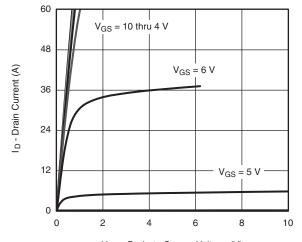
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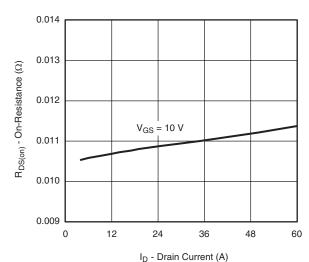
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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

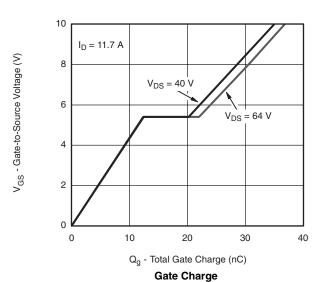


 $V_{\mbox{\scriptsize DS}}$  - Drain-to-Source Voltage (V)





On-Resistance vs. Drain Current

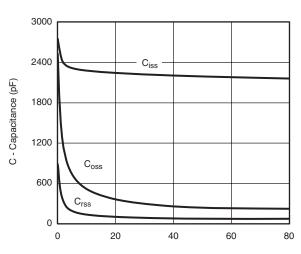


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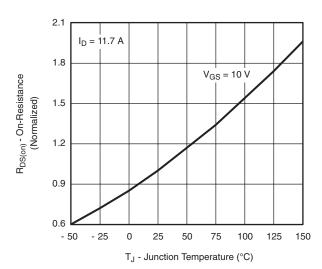
V<sub>GS</sub> - Gate-to-Source Voltage (V)

#### **Transfer Characteristics**



 $V_{\mbox{\footnotesize DS}}$  - Drain-to-Source Voltage (V)

#### Capacitance



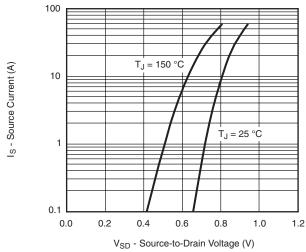
On-Resistance vs. Junction Temperature

## **Si4110DY**

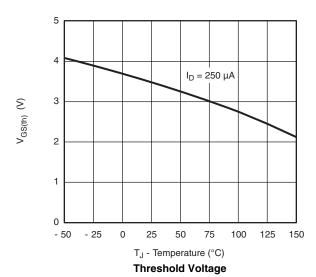
## Vishay Siliconix

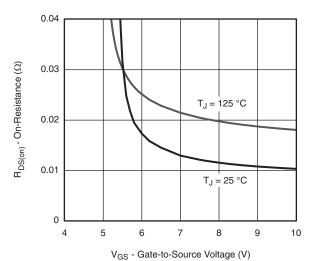
# VISHAY.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

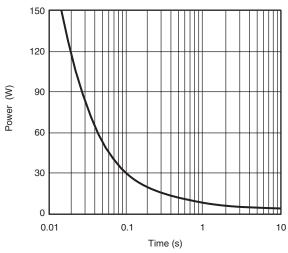


## Source-Drain Diode Forward Voltage

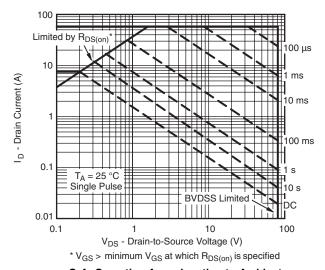




On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

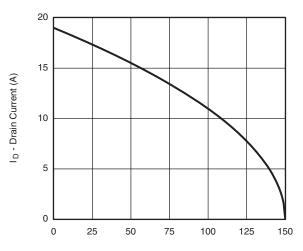


Safe Operating Area, Junction-to-Ambient



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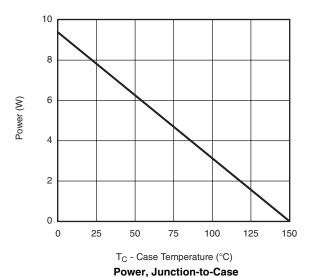
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

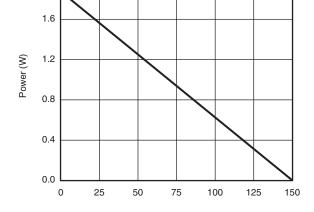


T<sub>C</sub> - Case Temperature (°C)

#### **Current Derating\***

2.0





T<sub>A</sub> - Ambient Temperature (°C) Power, Junction-to-Ambient

limit.

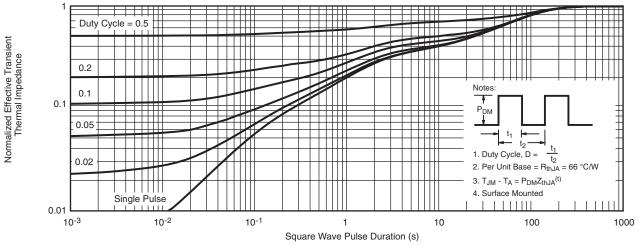
 $<sup>^*</sup>$  The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150  $^{\circ}$ C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package

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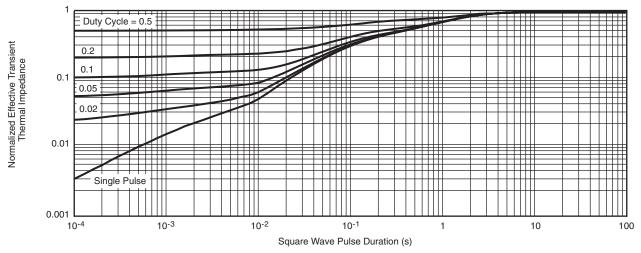
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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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