



120V PNP MEDIUM POWER DARLINGTON TRANSISTOR IN SOT23F

UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020

MIL-STD-202, Method 208 (93)

Weight: 0.012 grams (Approximate)

Case Material: Molded Plastic. "Green" Molding Compound.

Terminals: Finish - Matte Tin Plated Leads, Solderable per

Features

- BV_{CEO} > -120V
- I_C = -1A Continuous Collector Current
- Saturation Voltage V_{CE(SAT)} < -1.1V @ -1A
- hFE Characterized Up to -6A
- High h_{FE} (Min) = 3,000 @ -1A
- 1.5W Power Dissipation
- Complementary NPN Type: ZXTN04120HFF
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Applications

Mechanical Data

Case: SOT23F

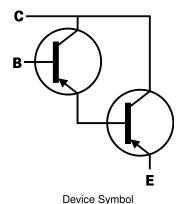
- Boost Converters
- MOSFET and IGBT Gate Drivers
- · Lamp and Relay Driver
- Motor Drive
- Siren Driver

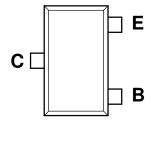
Description

This high performance PNP Darlington transistor is designed for applications requiring high gain and very low saturation voltage. The SOT23F package is pin compatible with the industry standard SOT23 footprint but offers lower profile and higher dissipation for applications where power density is of utmost importance.









Top View Pin Configuration

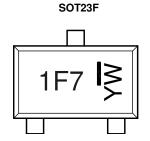
Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXTP05120HFFTA	AEC-Q101	1F7	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



1F7 = Product Type Marking Code YW = Date Code Marking

 $\underline{Y} = Year : 0~9$ $\overline{W} = Week : A~Z : 1~26$

a~z : 27~52

z represents 52 & 53 week



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-140	V
Collector-Emitter Voltage	$V_{\sf CEO}$	-120	V
Emitter-Base Voltage	V_{EBO}	-10	V
Continuous Collector Current	Ic	-1	Α
Peak Pulse Current	Ісм	-4	Α
Base Current	Ι _Β	-0.5	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 5)		0.84 6.72		
Power Dissipation	(Note 6)	D	1.34 10.72	W	
Linear Derating Factor	(Note 7)	P_{D}	1.50 12.0	mW/°C	
	(Note 8)		2.0 16.0		
	(Note 5)		149		
Thermal Desistance Junation to Ambient	(Note 6)		93	°C/W	
Thermal Resistance, Junction to Ambient	(Note 7)	$R_{\theta JA}$	83	3 C/VV	
	(Note 8)		60		
Thermal Resistance, Junction to Leads (Note 9)		$R_{\theta JL}$	43.8	°C/W	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

ESD Ratings (Note 10)

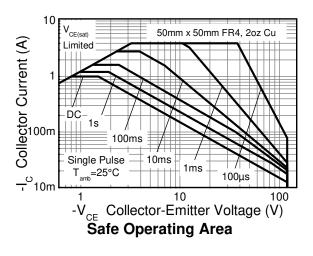
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge – Human Body Model	ESD HBM	2,000	V	2
Electrostatic Discharge – Machine Model	ESD MM	200	V	В

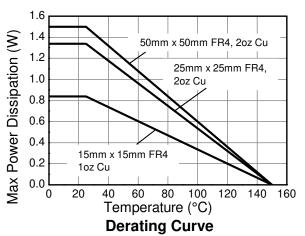
Notes:

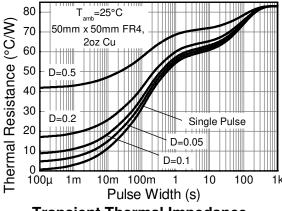
- 5. For a device mounted with the exposed collector pad on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
- Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
- 7. Same as Note 5, except the device is mounted on 50mm x 50mm 2oz copper.
 8. Same as Note 7, whilst measured at t < 5 seconds.
- 9. Thermal resistance from junction to solder-point (at the end of the leads).
- 10. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

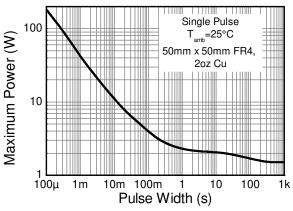


Thermal Characteristics and Derating Information









Transient Thermal Impedance

Pulse Power Dissipation



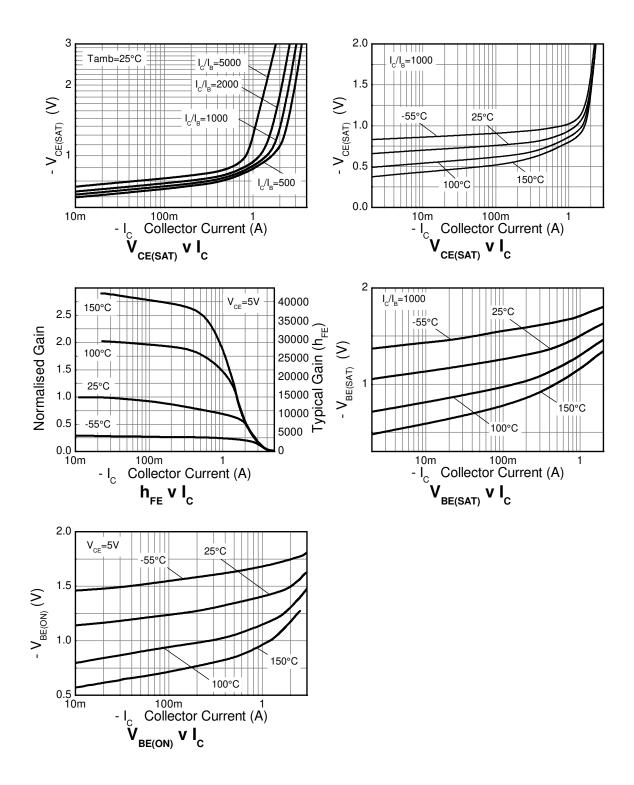
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	-140	-170	_	٧	$I_{C} = -100 \mu A$
Collector-Emitter Breakdown Voltage (Base Open) (Note 11)	BV _{CEO}	-120	-140	_	V	I _C = -10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	-10	-16	_	٧	$I_E = -100\mu A$
Collector-Base Cutoff Current	I _{CBO}	_	<-1 —	-100 -10	nΑ μΑ	V _{CB} = -120V V _{CB} = -120V, T _A = +100°C
Emitter-Base Cutoff Current	I _{CES}	_	<-0.1	-10	μΑ	V _{CB} = -120V
Emitter-Base Cutoff Current	I _{EBO}	_	<-1	-100	nA	V _{EB} = -5.6V
ON CHARACTERISTICS (Note 11)						
Static Forward Current Transfer Ratio	h _{FE}	3,000 3,000 3,000 2,000	14,000 11,000 11,000 8,000	30,000 —	I	$\begin{split} &I_{C} = -50 \text{mA}, \ V_{CE} = -5 V \\ &I_{C} = -500 \text{mA}, \ V_{CE} = -5 V \\ &I_{C} = -1 \text{A}, \ V_{CE} = -5 V \\ &I_{C} = -2 \text{A}, \ V_{CE} = -5 V \end{split}$
Collector-Emitter Saturation Voltage	V _{CE} (SAT)	_	-0.77 -0.9 -1.3	-0.9 -1.1 -2.0	V	$I_C = -250$ mA, $I_B = -0.25$ mA $I_C = -1$ A, $I_B = -1$ mA $I_C = -2$ A, $I_B = -2$ mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	-1.5	-1.7	V	$I_C = -1A$, $I_B = -1mA$
Base-Emitter On Voltage	V _{BE(ON)}	_	-1.4	-1.7	V	$I_C = -1A$, $V_{CE} = -5V$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f _T		150	_	MHz	$I_{C} = -100 \text{mA}, V_{CE} = -10 \text{V},$ f = 20 MHz
Input Capacitance	C _{IBO}	_	67	90	рF	$V_{EB} = -0.5V, f = 1MHz$
Output Capacitance	C _{OBO}	_	22	40	рF	$V_{CB} = -1V$, $f = 1MHz$
Delay Time	t _D		556	_	ns	101
Rise Time	t _R	_	212	_	ns	$V_{CC} = -10V$,
Storage Time	ts	_	681	_	ns	$-I_{C} = -0.5A,$ $-I_{B1} = I_{B2} = -0.5mA$
Fall Time	t _F	_	304	_	ns	181 – 182 – -0.3HA

Note: 11. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

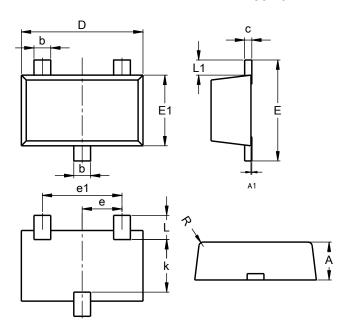




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F

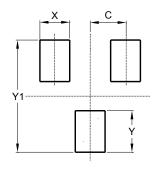


SOT23F						
Dim	Min	Max	Тур			
Α	0.80	1.00	0.90			
b	0.35	0.50	0.44			
C	0.10	0.20	0.16			
D	2.80	3.00	2.90			
е	0.95 REF					
e1	0.190 REF					
Е	2.30	2.50	2.40			
E1	1.50	1.70	1.65			
k	1.20					
L	0.30 0.65 0.50					
L1	0.30	0.50	0.40			
R	0.05	0.15	-			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23F



Dimensions	Value		
Dilliensions	(in mm)		
C	0.95		
Х	0.80		
Υ	1.110		
Y1	3 000		

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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