

FDD6030BL/FDU6030BL

30V N-Channel PowerTrench® MOSFET

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $R_{\rm DS(ON)}$, fast switching speed and extremely low $R_{\rm DS(ON)}$ in a small package.

Applications

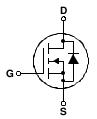
- DC/DC converter
- Motor drives

Features

- 42 A, 30 V $R_{DS(ON)} = 16 \text{ m}\Omega \text{ @ V}_{GS} = 10 \text{ V}$ $R_{DS(ON)} = 22 \text{ m}\Omega \text{ @ V}_{GS} = 4.5 \text{ V}$
- Low gate charge (22 nC typical)
- · Fast switching
- High performance trench technology for extremely low R_{DS(ON)}







Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Para	meter		Ratings	Units
V _{DSS}	Drain-Source Voltage			30	V
V _{GSS}	Gate-Source Voltage			±20	V
I _D	Continuous Drain Current	@T _C =25°C	(Note 3)	42	Α
		@T _A =25°C	(Note 1a)	10	
		Pulsed	(Note 1a)	100	
P _D	Power Dissipation	@T _C =25°C	(Note 3)	50	W
		@T _A =25°C	(Note 1a)	3.8	
		@T _A =25°C	(Note 1b)	1.6	
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +175	°C

Thermal Characteristics

R ₀ JC	Thermal Resistance, Junction-to-Case	(Note 1)	3.0	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	45	°C/W
R _{0JA}	Thermal Resistance, Junction-to-Ambient	(Note 1b)	96	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape width	Quantity
FDD6030BL	FDD6030BL	D-PAK (TO-252)	13"	12mm	2500 units
FDU6030BL	FDU6030BL	I-PAK (TO-251)	Tube	N/A	75

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-So	ource Avalanche Ratings (Not	e 2)				
W _{DSS}	Drain-Source Avalanche Energy	Single Pulse, V _{DD} = 15 V			130	mJ
lar	Drain-Source Avalanche Current				10	Α
Off Char	acteristics		ı		I	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \qquad I_{D} = 250 \mu\text{A}$	30			V
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A,Referenced to 25°C		22		mV/°C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			1	μΑ
Igssf	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
Igssr	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)		•			
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	1	1.6	3	V
ΔV _{GS(th)} ΔT _J	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}$, Referenced to 25°C		-4		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$ \begin{array}{llllllllllllllllllllllllllllllllllll$		12 17 19	16 22 26	mΩ
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, V_{DS} = 5 \text{ V}$	50			Α
g FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 10 \text{ A}$		29		S
Dynamic	Characteristics					
Ciss	Input Capacitance	V 45V V 6V		1143		pF
Coss	Output Capacitance	$V_{DS} = 15 \text{ V}, \qquad V_{GS} = 0 \text{ V},$ $f = 1.0 \text{ MHz}$		249		pF
C _{rss}	Reverse Transfer Capacitance	1 = 1.0 WHZ		107		pF
Switchin	g Characteristics (Note 2)		•			
t _{d(on)}	Turn-On Delay Time			6	12	ns
t _r	Turn-On Rise Time	$V_{DD} = 15 \text{ V}, \qquad I_D = 1 \text{ A},$		10	18	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		18	29	ns
t _f	Turn-Off Fall Time			5	12	ns
Qg	Total Gate Charge			22	31	nC
Q _{gs}	Gate-Source Charge	$V_{DS} = 15V,$ $I_{D} = 10 A,$ $V_{GS} = 10 V$		3		nC
Q _{qd}	Gate-Drain Charge	- 45		4		nC

Electrical Characteristics

T_A = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Se	Drain-Source Diode Characteristics and Maximum Ratings					
ls	Maximum Continuous Drain-Source Diode Forward Current				3.2	Α
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 3.2 \text{ A}$ (Note	2)	0.7	1.2	V

Notes

 R_{8UA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{8UC} is guaranteed by design while R_{8CA} is determined by the user's board design.



a) R_{e,JA} = 45°C/W when mounted on a 1in² pad of 2 oz copper



b) $R_{\theta JA} = 96^{\circ}\text{C/W}$ when mounted on a minimum pad.

Scale 1:1 on letter size paper

2. Pulse Test: Pulse Width < $300\mu s$, Duty Cycle < 2.0%

3. Maximum current is calculated as:

$$\sqrt{\frac{P_D}{R_{DS(ON)}}}$$

where P_D is maximum power dissipation at $T_C = 25^{\circ}C$ and $R_{DS(on)}$ is at $T_{J(max)}$ and $V_{GS} = 10V$. Package current limitation is 21A

Typical Characteristics

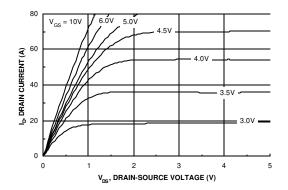


Figure 1. On-Region Characteristics

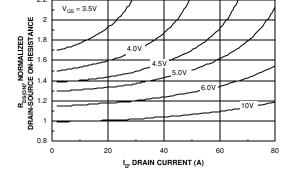


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

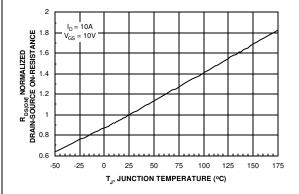


Figure 3. On-Resistance Variation with Temperature

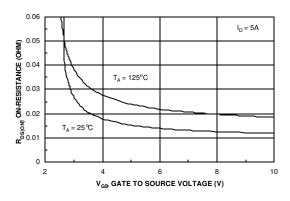


Figure 4. On-Resistance Variation with Gate-to-Source Voltage

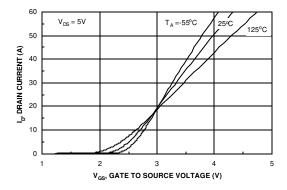


Figure 5. Transfer Characteristics

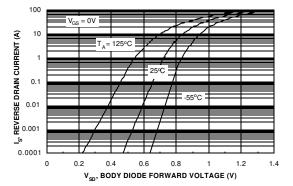
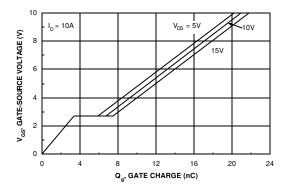


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Typical Characteristics



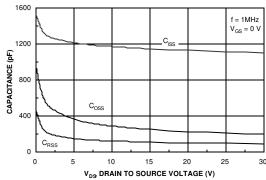


Figure 7. Gate Charge Characteristics

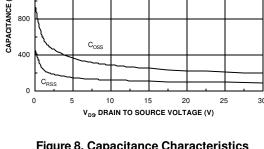
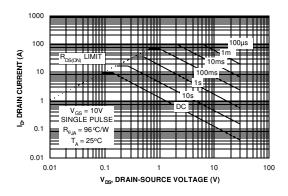


Figure 8. Capacitance Characteristics



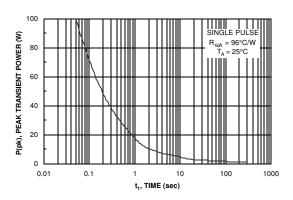


Figure 9. Maximum Safe Operating Area

Figure 10. Single Pulse Maximum **Power Dissipation**

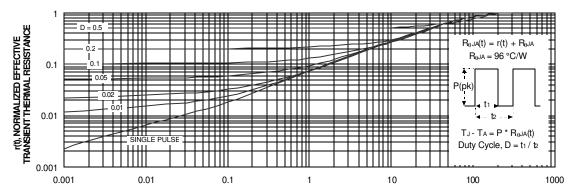


Figure 11. Transient Thermal Response Curve

Thermal characterization performed using the conditions described in Note 1b. Transient thermal response will change depending on the circuit board design.

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