



DMN90H8D5HCTI

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	Rds(on)	I _D Tc = +25°C	
900V	$7\Omega@V_{GS} = 10V$	2.5A	

Description

This new generation complementary dual MOSFET features low onresistance and fast switching, making it ideal for high efficiency power management applications.

Applications

- Motor controls
- Backlighting
- DC-DC converters
- Power management functions

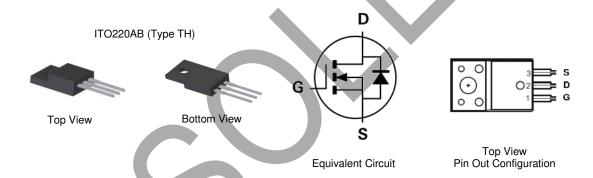
Features

- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

 https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: ITO220AB
- Package Material: Molded Plastic, "Green" Molding Compound, UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram Below
- Weight: 1.85 grams (Approximate)



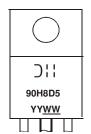
Ordering Information (Note 4)

Part Number	Pankago		Packing		
Part Number		Package	Qty.	Carrier	
DMN90H8D5HCTI	ITO22	0AB (Type TH)	50 Pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



☐ Hanufacturer's Marking
 90H8D5 = Product Type Marking Code
 YYWW = Date Code Marking
 YY or YY = Last Two Digits of Year (ex: 20 = 2020)
 WW or WW= Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Value	Unit
Drain-Source Voltage			900	V
Gate-Source Voltage			±30	V
Continuous Drain Current (Note 5) VGS = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	ΙD	2.5 1.5	А
Pulsed Drain Current (Note 6)			3	Α
Avalanche Current, L = 60mH (Note 7)		las	1.8	Α
Avalanche Energy, L = 60mH (Note 7)		Eas	97	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	3.3	V/ns

Thermal Characteristics

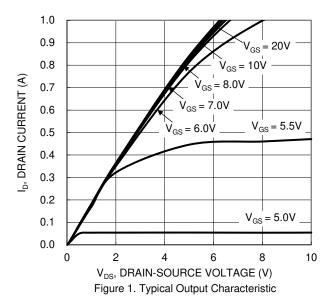
Characteristic			Max	Unit
Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	PD	30 12	W
Thermal Resistance, Junction to Case (Note 5)	Tc = +25°C	Rejc	4.2	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	900	_		V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	1		1	μΑ	$V_{DS} = 900V, V_{GS} = 0V$	
Gate-Source Leakage	Igss			100	nA	$V_{GS} = \pm 30V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	3.0		5.0	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	5.5	7.0	Ω	$V_{GS} = 10V$, $I_D = 1A$	
Diode Forward Voltage	Vsp			1.2	V	$V_{GS} = 0V$, $I_{S} = 2A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		470	_		V _{DS} = 25V, f = 1.0MHz, V _{GS} = 0V	
Output Capacitance	Coss	_	45	_	pF		
Reverse Transfer Capacitance	Crss		0.6	_			
Gate Resistance	Rg		1.2		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$	
Total Gate Charge	Qg	_	7.9	_		V _{DD} = 720V, I _D = 2A,	
Gate-Source Charge	Qgs	_	2.5	_	nC		
Gate-Drain Charge	Qgd	_	2.9	_		$V_{GS} = 10V$	
Turn-On Delay Time	t _{D(ON)}	_	16	_		$V_{DD} = 450V, R_G = 25\Omega, I_D = 2A, V_{GS} = 10V$	
Turn-On Rise Time	t _R	_	21	_	ns		
Turn-Off Delay Time	tD(OFF)		17.6	_			
Turn-Off Fall Time	tF		17	_			
Body Diode Reverse Recovery Time	trr		375		ns	$dI/dt = 100A/\mu s$, $V_{DS} = 100V$,	
Body Diode Reverse Recovery Charge	Qrr		2.9	_	μC	IF = 2A	

- 5. Device mounted on infinite heatsink. Drain current limited by maximum junction temperature.
- S. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.





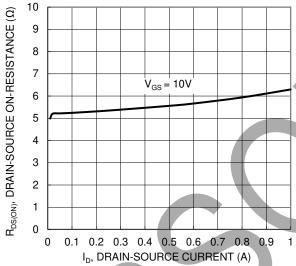


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

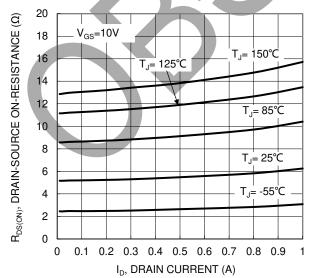
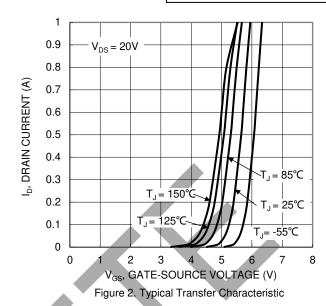
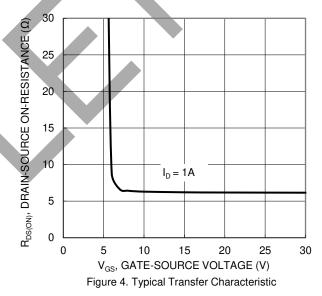


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





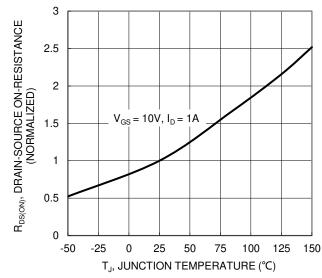


Figure 6. On-Resistance Variation with Temperature



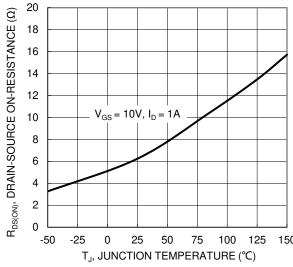
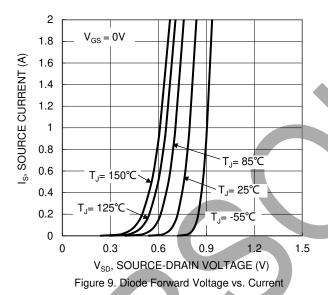
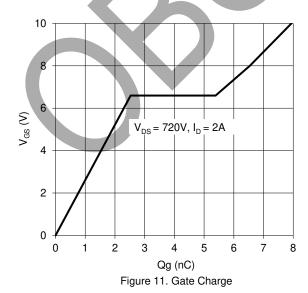
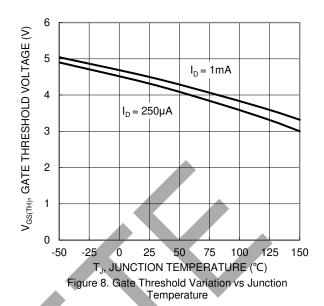
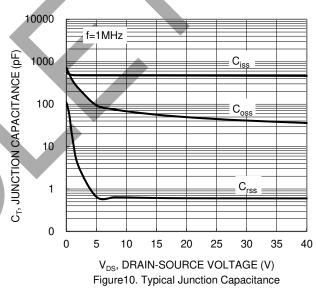


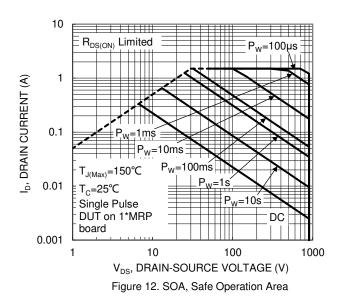
Figure 7. On-Resistance Variation with Temperature













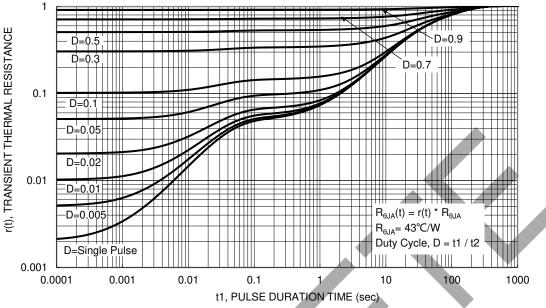


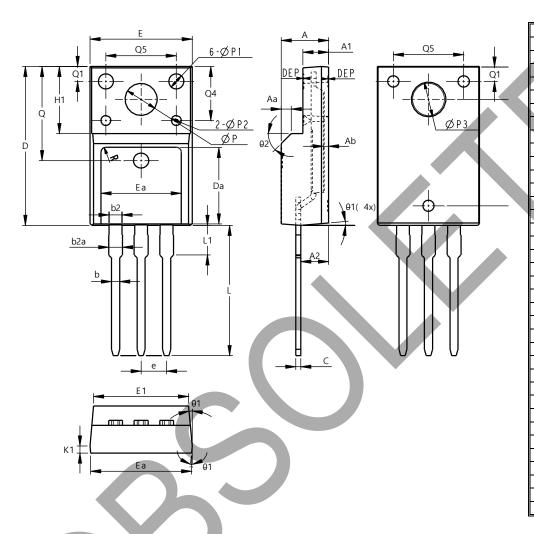
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

ITO220AB (Type TH)



ITO220AB (Type TH)							
	Dim Min Max Typ						
A	4.50	4.90	4.70				
A1	2.34	2.74	2.54				
A2	2.63	2.89	2.76				
Aa			F				
Ab	0.30	0.60	0.56				
b	0.75	0.90	0.80				
b2	1.23	1.38	1.28				
b2a	1.25	1.45	1.35				
С	0.45	0.60	0.50				
D	15.47	16.27	15.87				
Da	7.55	8.05	7.80				
е	2	.54 BS					
E	9.86	10.46	10.16				
E1	9.26	9.66	9.46				
Ea	7.70	8.30	8.00				
Eb	9.76	10.34	10.04				
H1	6	.70 RE	F				
L	12.58	13.38	12.98				
L1	2.81	3.05	2.93				
K1	0.65	0.75	0.70				
Q	9		F				
Q1	1.00	2.00	1.50				
Q2	13.50	14.30	13.90				
Q3	3.15	3.45	3.30				
Q4	5.15	5.65	5.40				
Q5	6.70	7.30	7.00				
ØΡ	3.06	3.40	3.18				
ØP1	1.40	1.60	1.50				
ØP2	0.95	1.05	1.00				
ØP3	3.30	3.60	3.45				
θ1	3º	7º	5º				
θ2	-	45⁰	-				
R	0.50 REF						
DEP	0.05	0.15	0.10				
All Dimensions in mm							



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