

NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

Parameter	Value
V _{CEO}	50V
I _C	100mA
R ₁	100kΩ

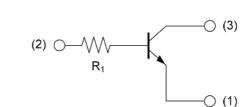
Outline SOT-23

(SST3)

•Inner circuit

Features

- 1) Built-In Biasing Resistor
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 4) Complementary PNP Types: DTA115TCA



- (1) EMITTER
- (2) BASE
- (3) COLLECTOR

Application

INVERTER, INTERFACE, DRIVER

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC115TCA	SOT-23 (SST3)	2924	T116	180	8	3000	09

● **Absolute maximum ratings** (T_a = 25°C)

Parameter	Symbol	Values	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	100	mA
Device discipation	P _D *1	200	mW
Power dissipation	P _D *2	350	mW
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Collector-base breakdown voltage	BV _{CBO}	I _C = 50μA	50	1	1	V	
Collector-emitter breakdown voltage	BV _{CEO} I _C = 1mA		50	-	-	V	
Emitter-base breakdown voltage	BV _{EBO}	I _E = 50μA	5	1	1	٧	
Collector cut-off current	I _{CBO}	V _{CB} = 50V	1	1	500	nA	
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	500	nA	
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = 1mA, I _B = 0.1mA	-	-	300	mV	
DC current gain	h _{FE}	V _{CE} = 5V, I _C = 1mA	100	250	600	-	
Input resistance	R ₁	-	70	100	130	kΩ	
Transition frequency	f _T *3	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz	

^{*1} Each terminal mounted on a reference land.

^{*2} Mounted on a ceramic board(7.0×5.0×0.6mm).

^{*3} Characteristics of built-in transistor

● Electrical characteristic curves (T_a =25°C)

Fig.1 Grounded emitter propagation characteristics

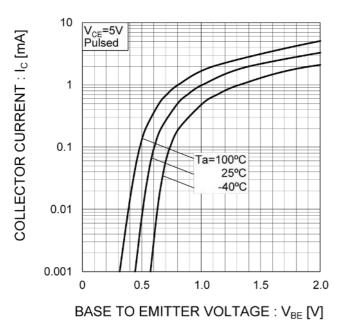


Fig.2 Typical Output Characteristics

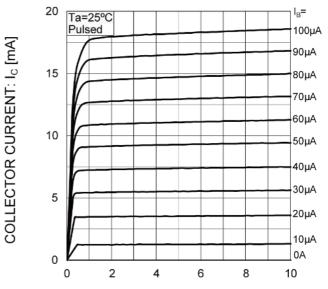


Fig.3 DC Current Gain vs. Collector Current

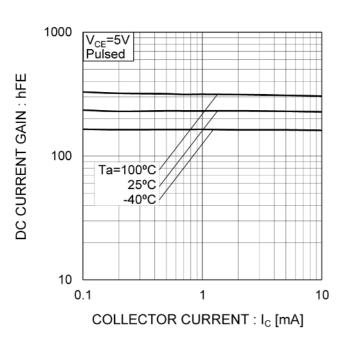
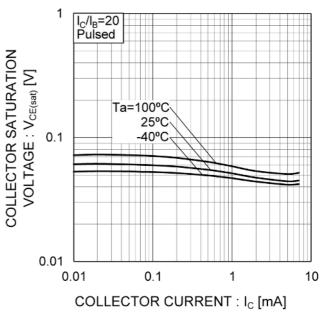
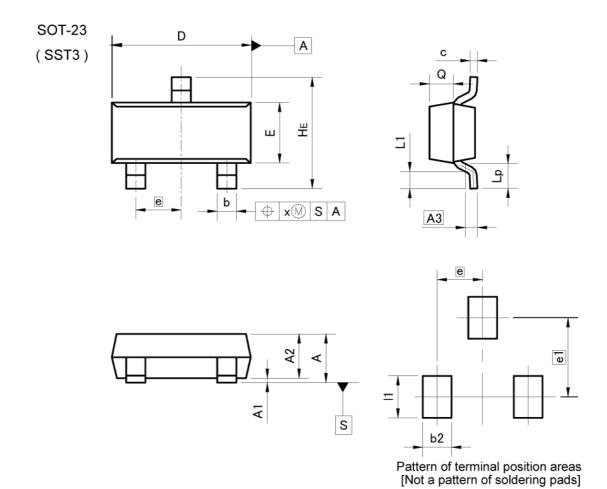


Fig.4 Collector-Emitter Saturation
Voltage vs. Collector Current

COLLECTOR TO EMITTER VOLTAGE: V_{CE} [V]



Dimensions



DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	0.90	1.20	0.035	0.047	
A1	0.00	0.10	0.000	0.004	
A2	0.85	1.15	0.033	0.045	
A3	0.3	25	0.0	10	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.70	3.10	0.106	0.122	
E	1.20	1.50	0.047	0.059	
е	0.	95	0.0	37	
HE	2.20	2.60	0.087	0.102	
L1	0.20	s=s	0.008	15.55	
Lp	0.30	a - -a	0.012	15.50	
Q	0.40	0.60	0.016	0.024	
х	-3	0.10	1 	0.004	

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b2	-2	0.60	× 	0.024	
e1	1.70		0.0	067	
11	- s	0.90	8 10	0.035	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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CLASSIV	CLASSII	CLASSⅢ	CLASSIII

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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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